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#### 5. Publications

The originators of the Software shall be acknowledged in all written publications by referring to the following publication: *"Jonathan D'Hoore, Poona Bahrebar and Dirk Stroobandt. 3D NoC Emulation Model on a Single FPGA. In Proc. ACM/IEEE Int. Workshop on System-Level Interconnect Problems and Pathfinding (SLIP'20), pp. 1-8, 2020"*.

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