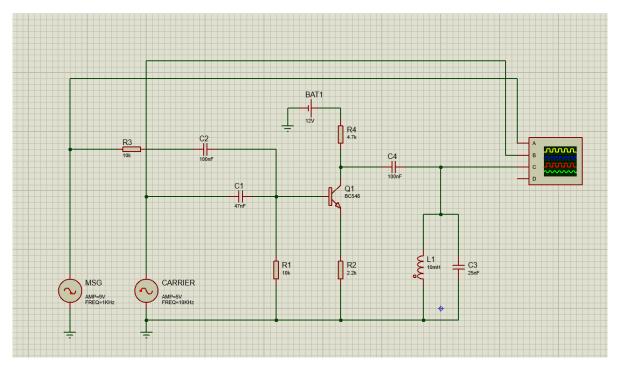
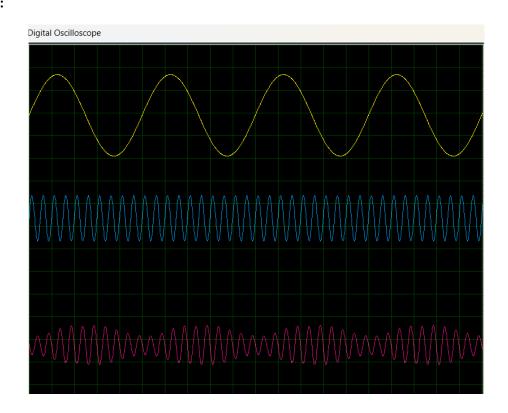
## **Analog Signal and Digital Signal**

Aim: To design two circuits for Analog Signal and Digital Signal

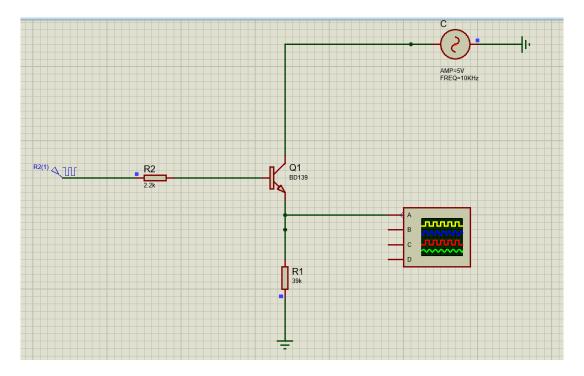
Circuit Diagram for Analog Signal: Amplitude Modulation



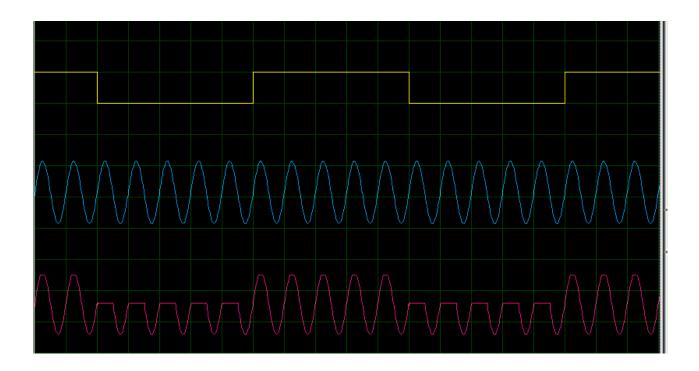
#### **Output:**



### Circuit Diagram for Digital Signal: Amplitude Shift keying



### Output:



# Amplitude modulation:

BC 548 transister is class c amplifier configuration > high efficiency > Suitable for Lc tank

carrier -> lokHZ,5V msg > 1KHZ, 9V

BCSYB amplifies and modulates the camer by varying its amplitude according to the message signal

LI-C3 brank circuit -> acts as filter + frequency selector for carrier frequency

output at collector

R, > loks -> Biase bias resistor R2 > 2.2 K. 2 -> emitter resistor -> negative feedback R3 => LOKIZ -> Limit corrent of modulating (msg) Signal Ry > 4.7Ke > collector load resistor

Gain  $\approx \frac{R_C}{R_E} = \frac{R_Y}{R_2} = \frac{4.7k_{\perp}}{a.ak_{\perp}} \approx a.13 \rightarrow \text{sufficient for low}$ power AM

Ry > if too large > output clipped R4 -> if too small -> weak signal

4.7Ks is optimum

Resonate frequency of tank circuit ~ lok#z  $f = \frac{1}{att\sqrt{Lc}} = \frac{1}{att\sqrt{(10mH)(25nF)}}$ 

Ask is a modulation technique where the amplitude of a carrier wave is varied according to a digital input. Signal (0 or 1) (+ Attention (but said tells

R2 = 2.2Ks

-> controls base current to switch QI fully on without damaging it.

 $\rightarrow \frac{\Gamma_B}{\Gamma_B} = \frac{\Gamma_C}{\Gamma_B}$  |  $\beta$  is correct gain.

B for B0139, typically 40-100

( Vin = 5V , β= 50 VBE 20.7V,

Desired Ic = 10 mA

RI is pull down resistor

IB = 19mA = 0.2mA

BD 139 is Very high

frequency Transistor

 $R_2 = \frac{Vin - V_{BE}}{I_0} = \frac{SV - 0.7V}{0.2mA} \approx 21.5 \text{K}.$ 

But 2.2 K. 2 is used for , safety

overdrives the base to ensure saturation under any condition.

Rz ensures the reliable switching and protect the bose

the mathematical representation of ASK is s(t)=[m(t)-Ac]cos(anfet)