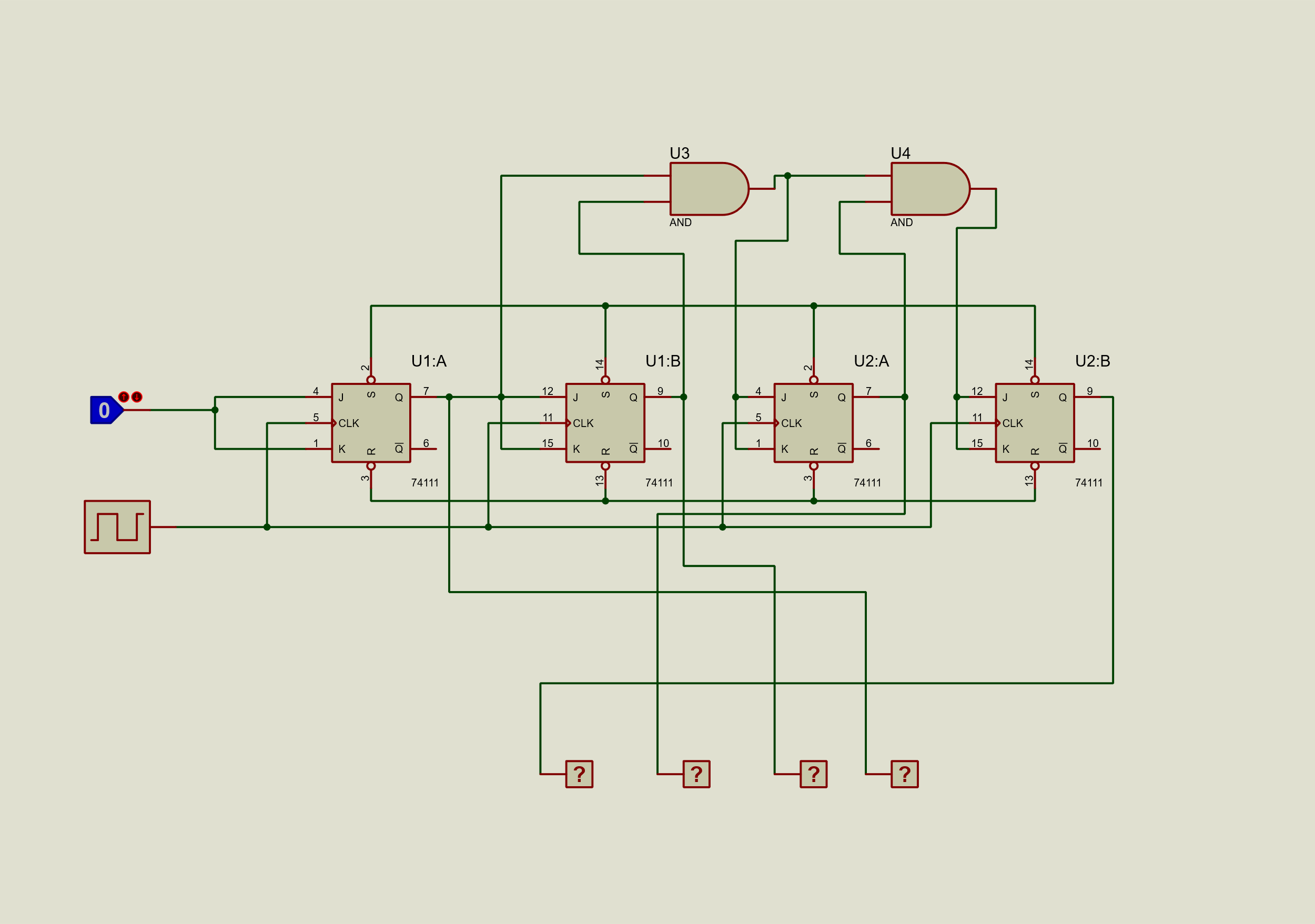
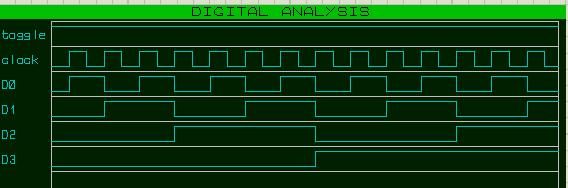
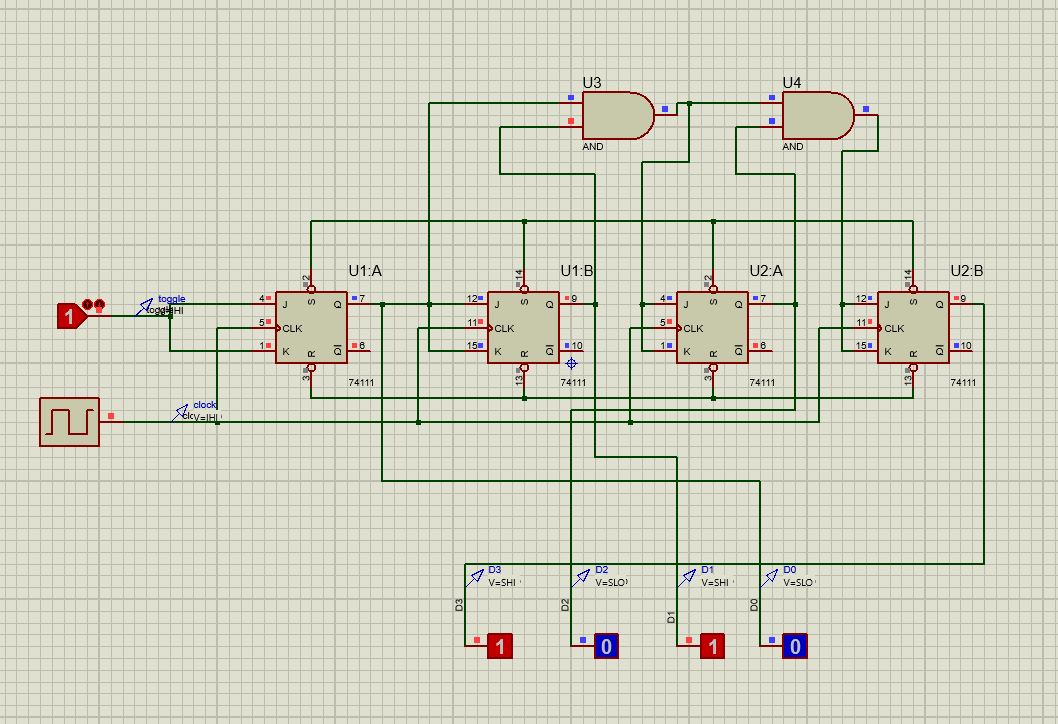
**Design, Simulation, and Implementation of a 4-bit Synchronous Binary Counter using JK flipflop**

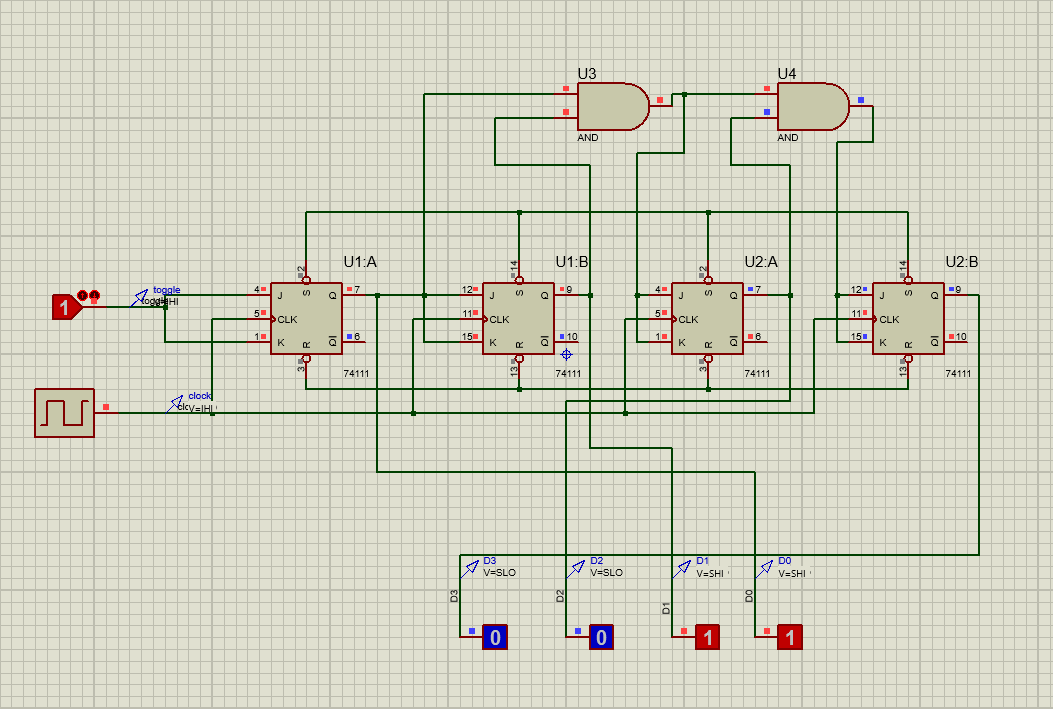
**Circuit diagram:**



Outputs:







Theory work:

