	TRANSMIT (HOST TO uC)									RECEIVE (uC to HOST)										Obs	
#	ID	DLC				DA	TΑ				ID	DLC	DATA								
1											000	8	48	65	6C	6C	6F	20	21	21	Hello msg from microcontroller
2	000	0																			Enter bootloader mode
3	010	4	08	04	00	00															Set flash address to 0x08040000
4	020	0																			Reset current frame
5	100	8	00	00	00	00	00	00	00	00	200	8	00	00	00	00	00	00	00	00	Send/Receive data frame 00
6	101	8	11	11	11	11	11	11	11	11	201	8	11	11	11	11	11	11	11	11	Send/Receive data frame 01
7	102	8	22	22	22	22	22	22	22	22	202	8	22	22	22	22	22	22	22	22	Send/Receive data frame 02
8	103	8	33	33	33	33	33	33	33	33	203	8	33	33	33	33	33	33	33	33	Send/Receive data frame 03
9	104	8	eto								204	8	etc								Send/Receive data frame 04
10	105	8	etc	:							205	8	etc								Send/Receive data frame 05
11	106	8	etc	;							206	8	etc								Send/Receive data frame 06
12	107	8	eto	:							207	8	etc								Send/Receive data frame 07
13	108	8	eto	;							208	8	etc								Send/Receive data frame 08
14	109	8	etc								209	8	etc								Send/Receive data frame 09
15	110	8	etc								210	8	etc								Send/Receive data frame 10
16	111	8	etc	;							211	8	etc								Send/Receive data frame 11
17	112	8	eto	:							212	8	etc								Send/Receive data frame 12
18	113	8	etc	:							213	8	etc								Send/Receive data frame 13
19	114	8	etc								214	8	etc								Send/Receive data frame 14
20	115	8	etc								215	8	etc								Send/Receive data frame 15
21	116	8	etc	;							216	8	etc								Send/Receive data frame 16
22	117	8	eto	:							217	8	etc								Send/Receive data frame 17
23	118	8	etc	:							218	8	etc								Send/Receive data frame 18
24	119	8	eto	:							219	8	etc								Send/Receive data frame 19
25	120	8	eto	:							220	8	etc								Send/Receive data frame 20
26	121	8	etc	;							221	8	etc								Send/Receive data frame 21
27	122	8	eto	:							222	8	etc								Send/Receive data frame 22
28	123	8	eto	;							223	8	etc								Send/Receive data frame 23
29	124	8	etc								224	8	etc								Send/Receive data frame 24
30	125	8	etc								225	8	etc								Send/Receive data frame 25
31	126	8	etc	:							226	8	etc								Send/Receive data frame 26
32	127	8	etc								227	8	etc								Send/Receive data frame 27
33	128	8	etc								228	8	etc								Send/Receive data frame 28
34	129	8	etc								229	8	etc								Send/Receive data frame 29
35	130	8	etc								230	8	etc								Send/Receive data frame 30
36	131	8	etc	:							231	8	etc								Send/Receive data frame 31
37											300	3	2E	9C	14						32 val received, CRC [frame nr, 1s tbyte, 2nd byte)
38											400	1	2E								flash write successful, [frame nr]
39	106	10	etc								2FF	3	FF	00	06						ERROR, expected frame 00, received frame 06
40	090	0																			jump to user app