## **REVISION HISTORY**

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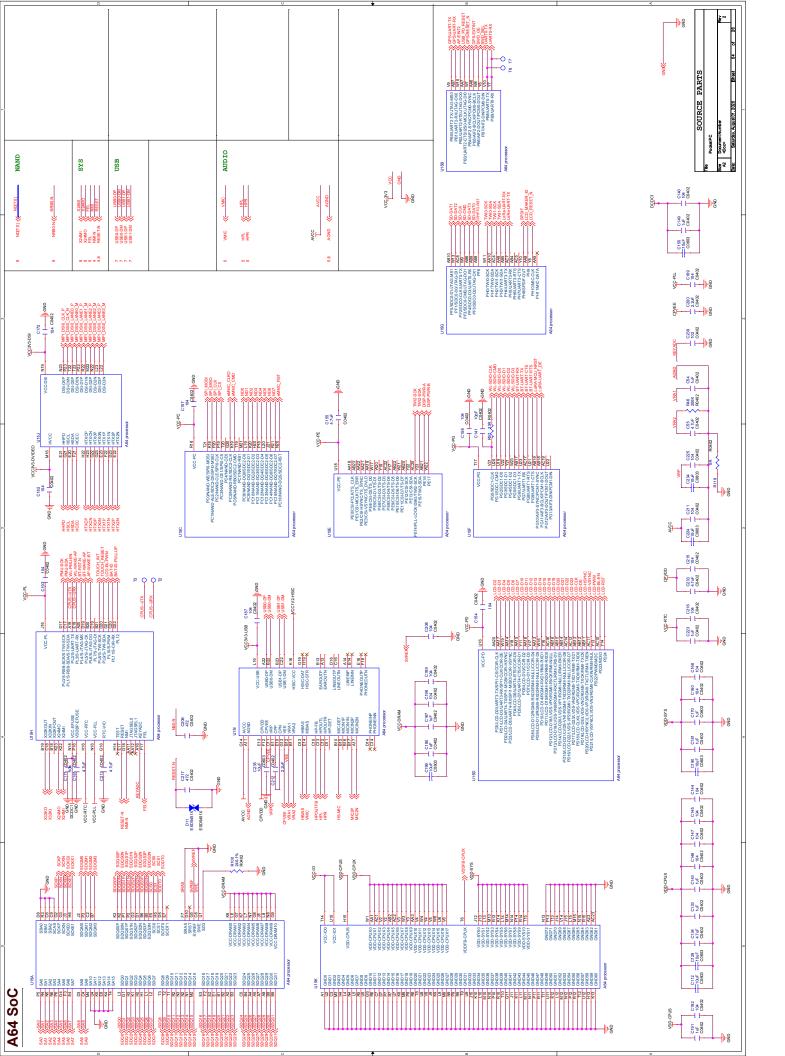
: A64 SoC : 2GB LPDDR3 : SDIO WIFI & BT : LCD TOUCH CONNECTOR PO1: REVISION HISTORY
PO2: AXP803 PMU
PO3: POWER/PMU
PO4: A64 SoC
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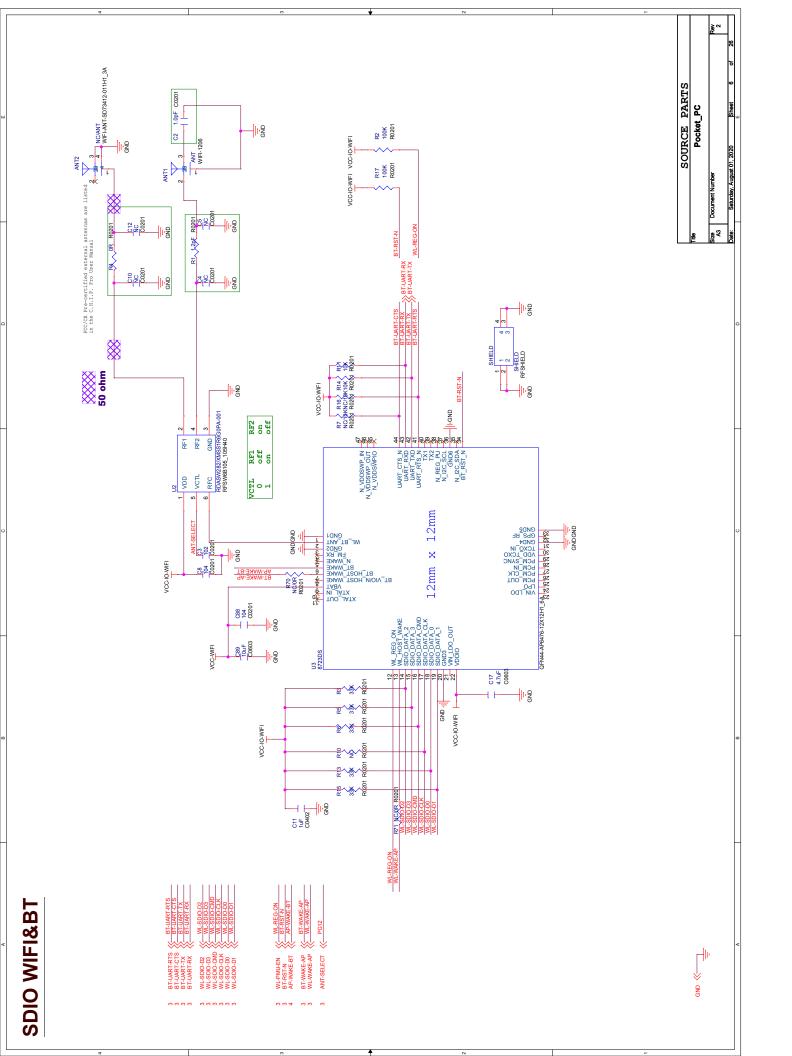
9: MICROSD
0: BESIDE CPU
1: LCD POWER REGULATOR
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9: USB-C PWR DELIVERY CONTROLER
0: USB C - HOST& ANALOG AUDIO
1: HDMI CONNECTOR

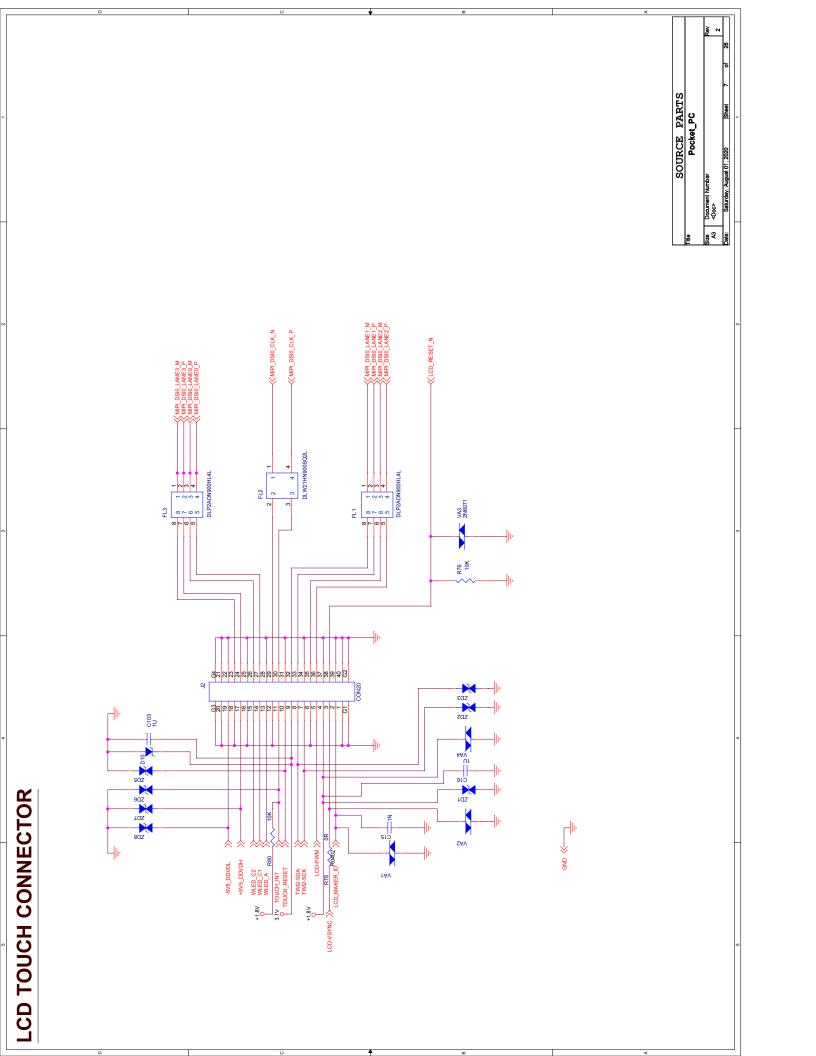
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Revision History	Pocket_PC			

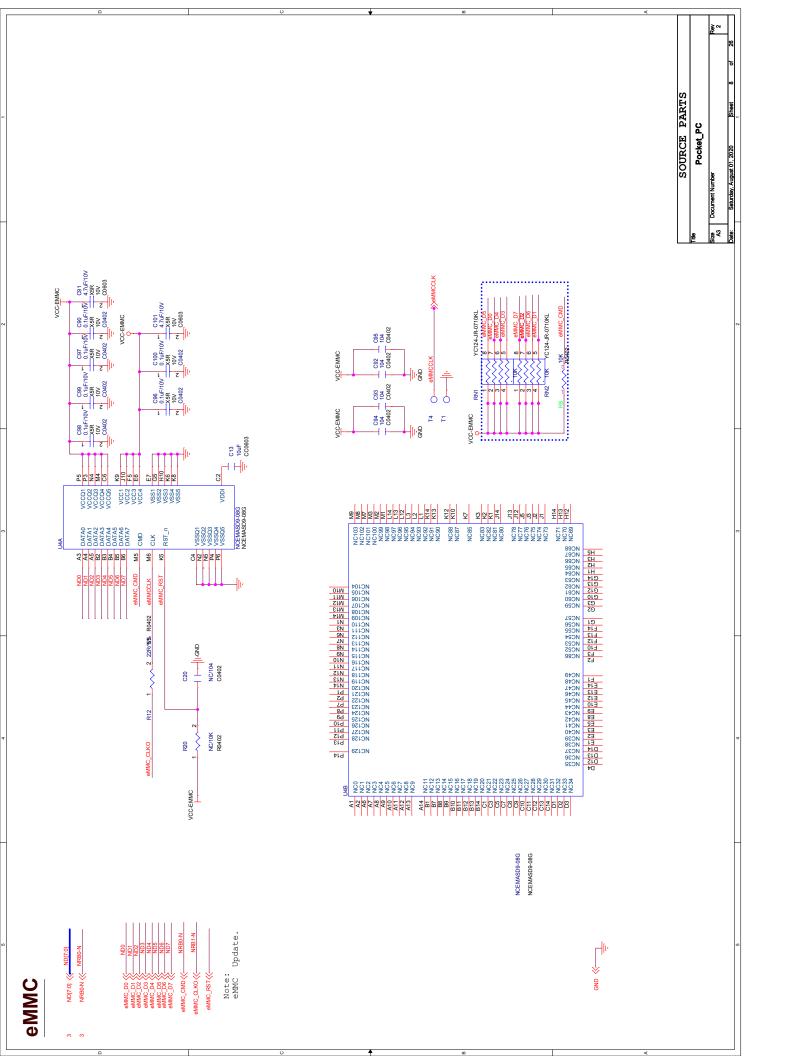
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ELD01 R0402 OR VCC-WIFI VDD-CPUX ELD01 VCC-PC NC R34 NC VDD18-LPDRR VCC1V2-LCD VCC-DRAM 1.2V/1.35V/1.5V AVCC VCC-PLL WIFI 1.8V 1.17 1.2V POWER/PMU 37 DCDC2-3 DLD04 DCDC5 FLD02 FLD01 DCDC6 ALD03

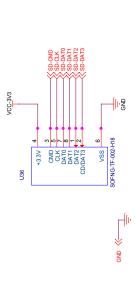


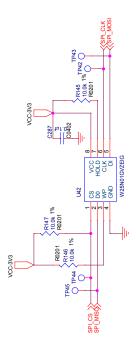






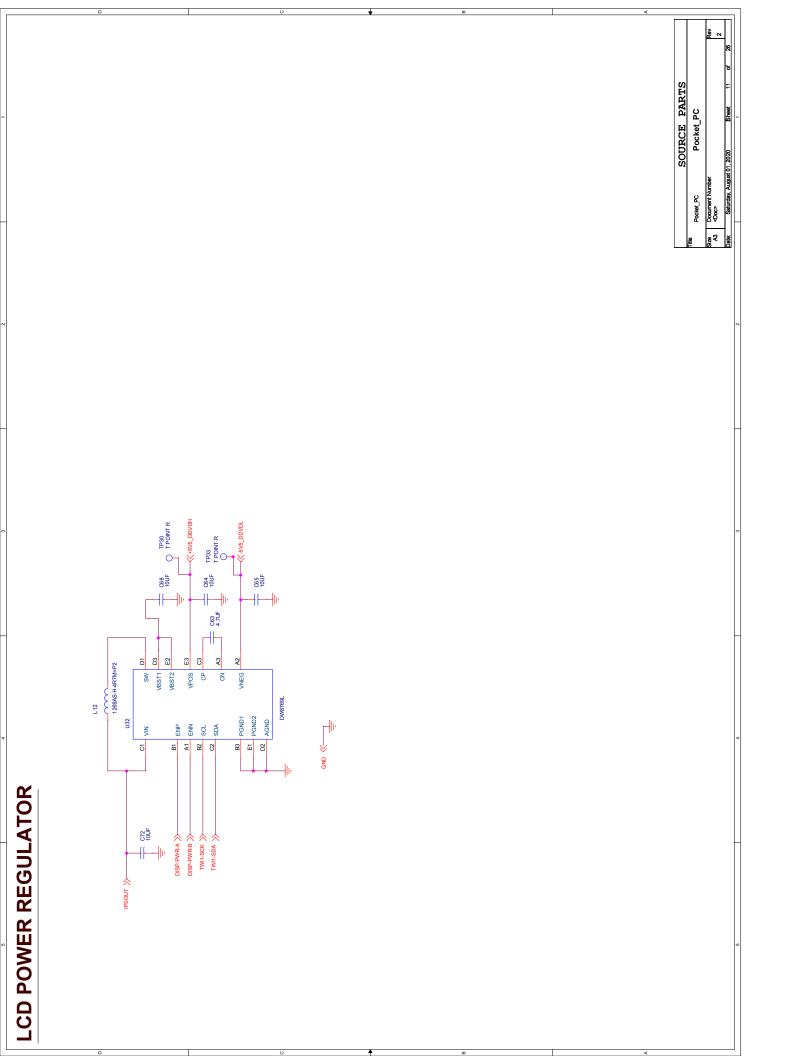




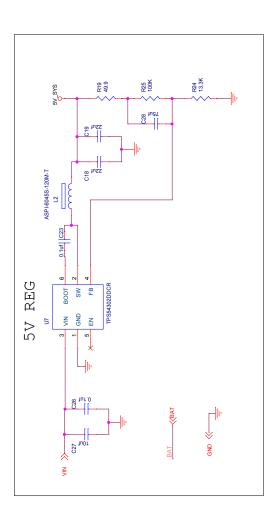


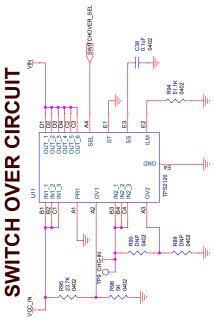
**SPI FLASH** 

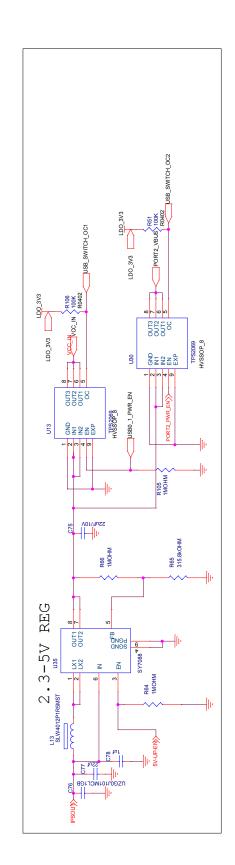
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# ADDITIONAL POWER REGULATORS

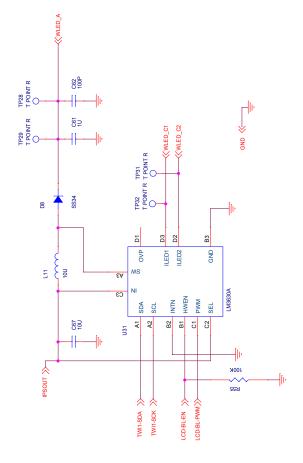


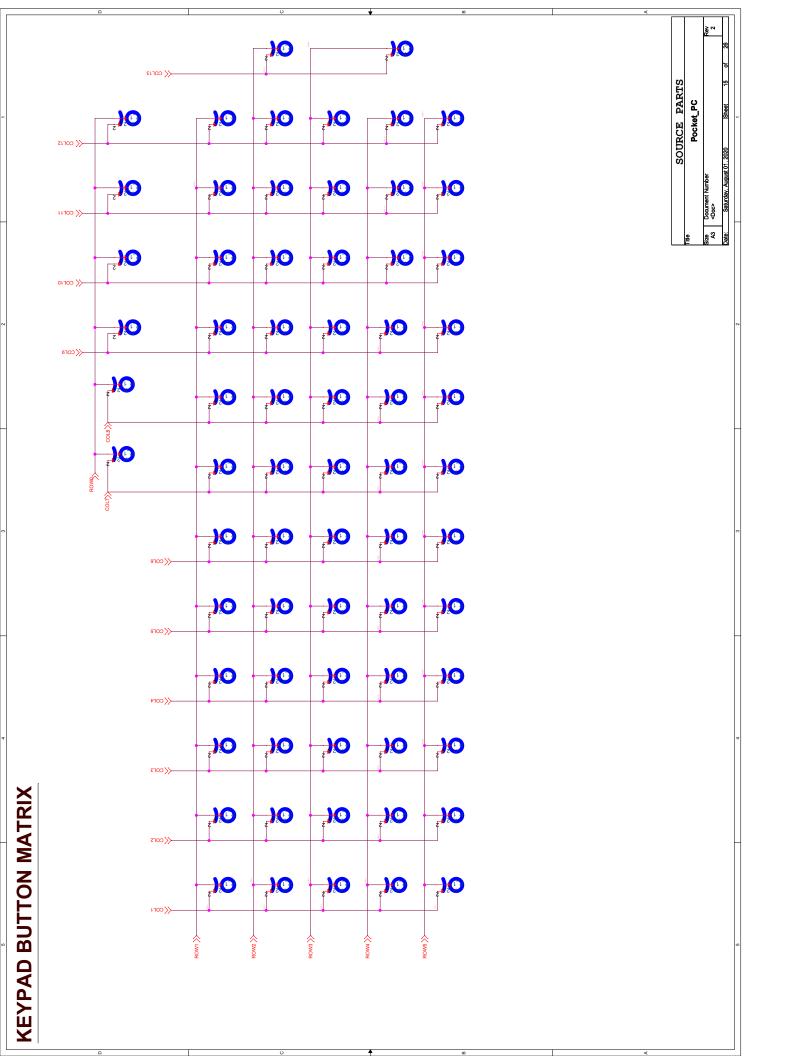


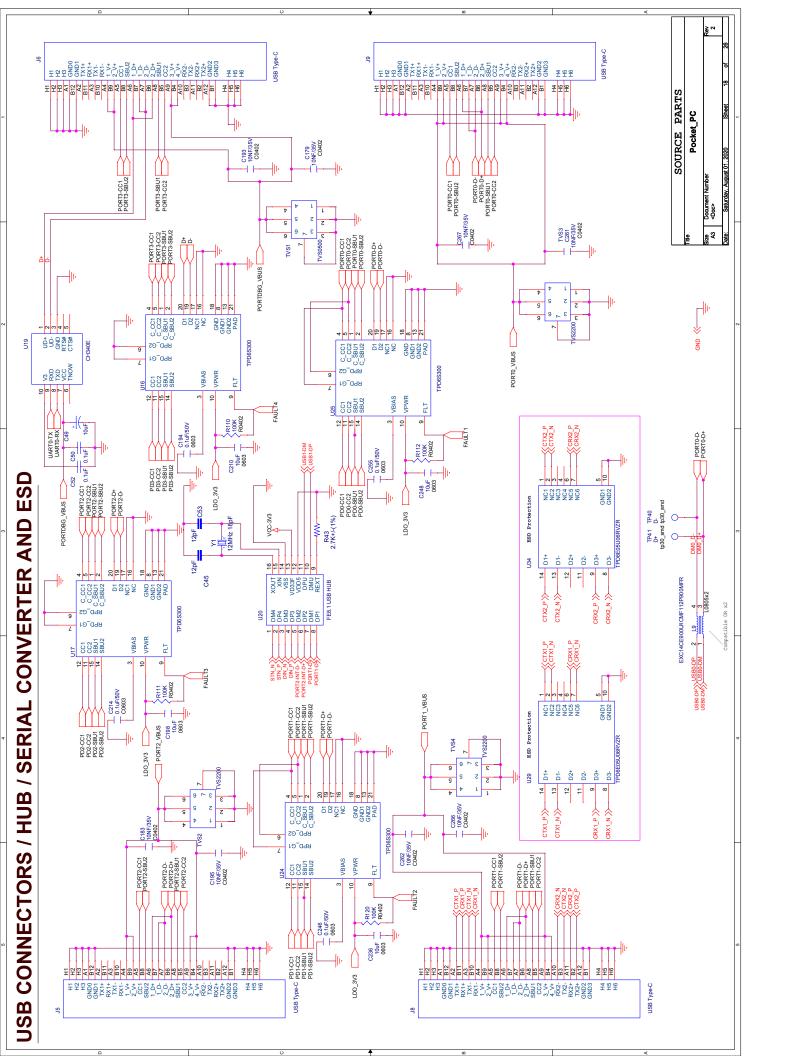


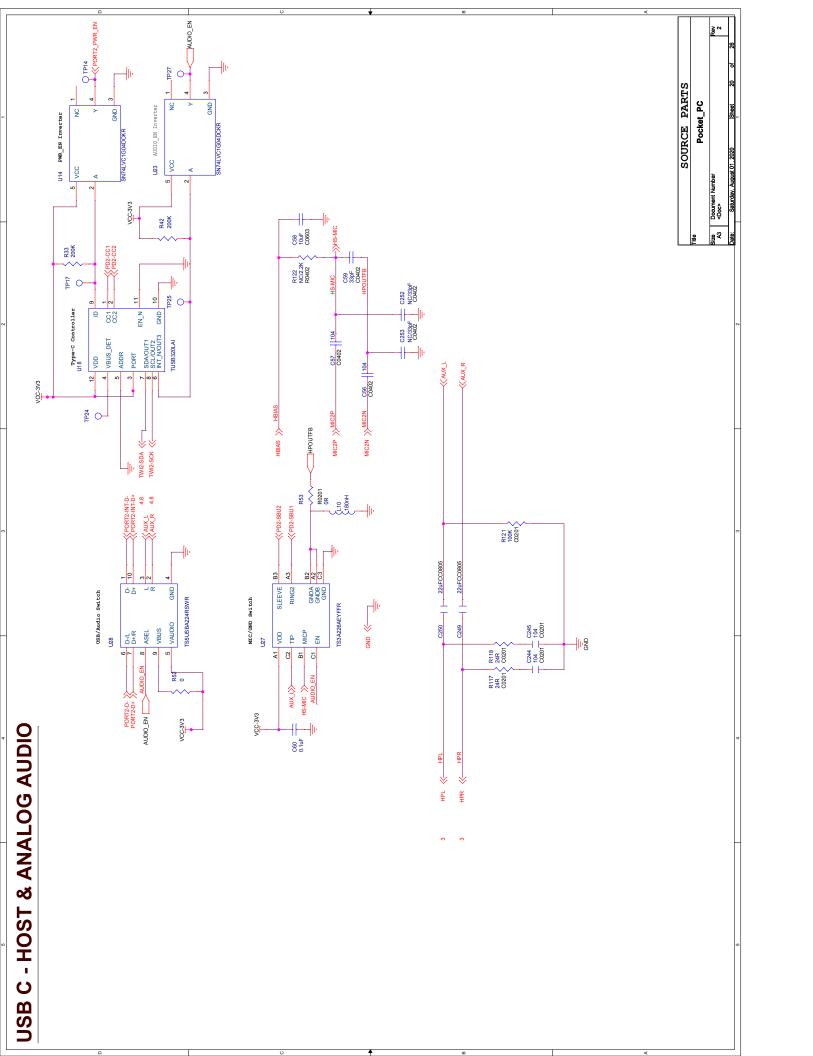
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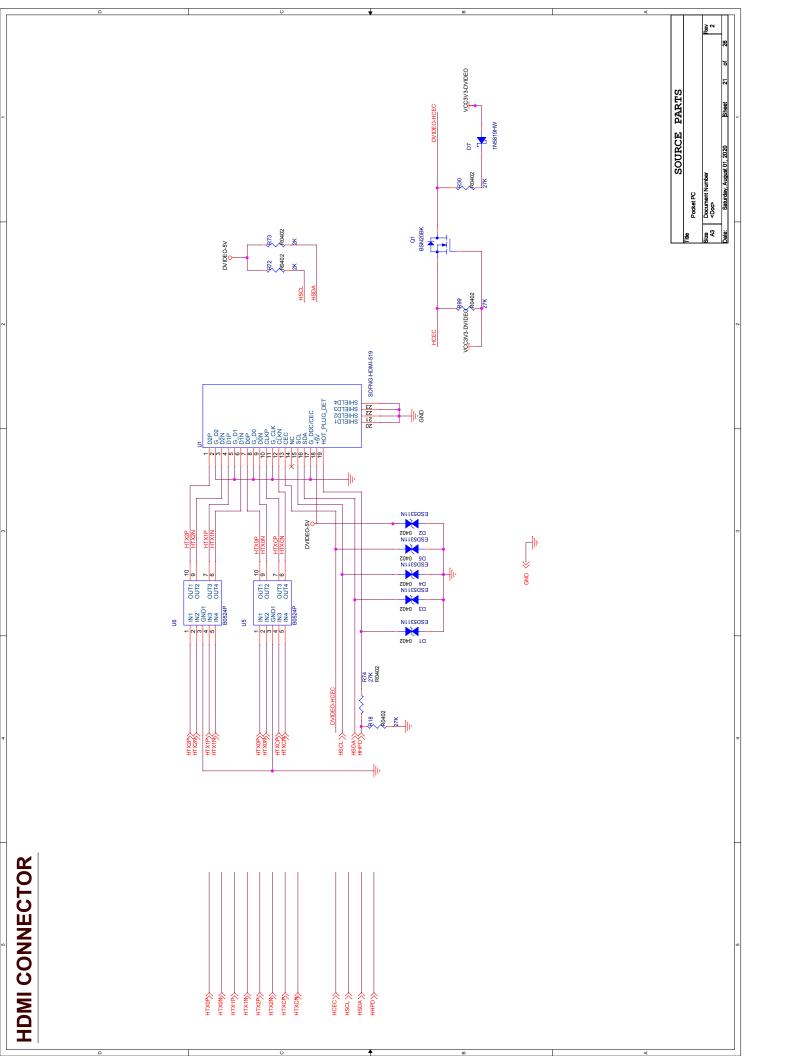
## LCD BACKLIGHT REGULATOR

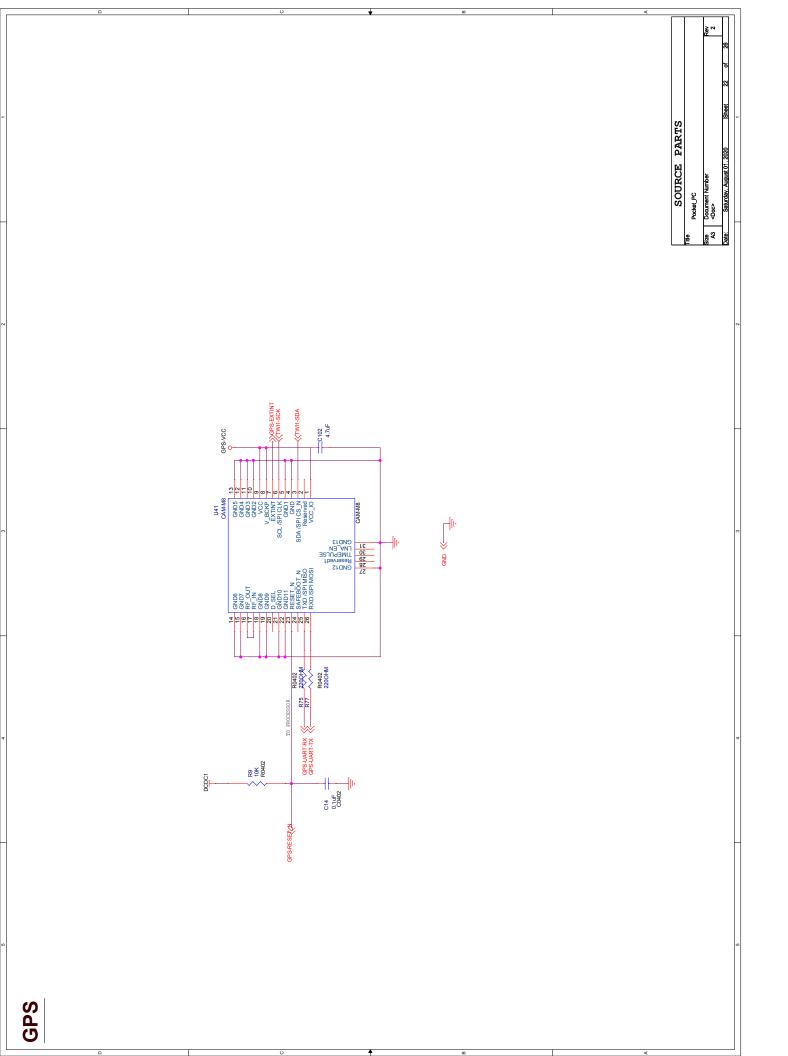






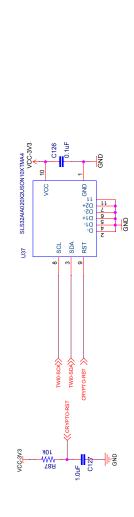


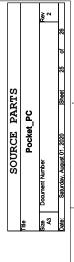














#### **TP LIST**

TP list Function	TP list	t Function
TP1 FEL	TP21	ELD03
TP2 DCDC1	TP22	VCC-RIC
TP3 ALDO2	TP23	USB-DRVVBUS
TP4 ALDO3	TP24	VBUS_DET
TP5 DLD01	TP25	STUO/N_TNI
TP6 DLDO2	TP26	PS
TP7 DLDO3	TP27	AUDIO_EN
TP8 ALDO1	TP28	WLED_A
TP9 CHG-IN	TP29	WLED_A
TP10 DLDO4	TP30	+5V5_DDVDH
TP11 DCDC2-3	TP31	WLED_C2
TP12 DCDC6	TP32	WLED_C1

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	PORT0-D+	SPI_CLK	SPI_MOSI	SPI_CS	SPI_MISO	
	TP41	TP42	TP43	TP44	TP45	

-5V5\_DDVDL TWI1-SCK

TWI0-SDA

TP35 TP36

PORT2\_PWR\_EN

TP13 TP14 TP15 TP16

DCDC5 FLD02

TWI2-SCK TWI2-SDA

> TP38 TP39 TP40

FLD01 ELD01

TP18

TP17

TP19

ID

ELD02

TP37

TWI0-SCK PORT0-D-

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