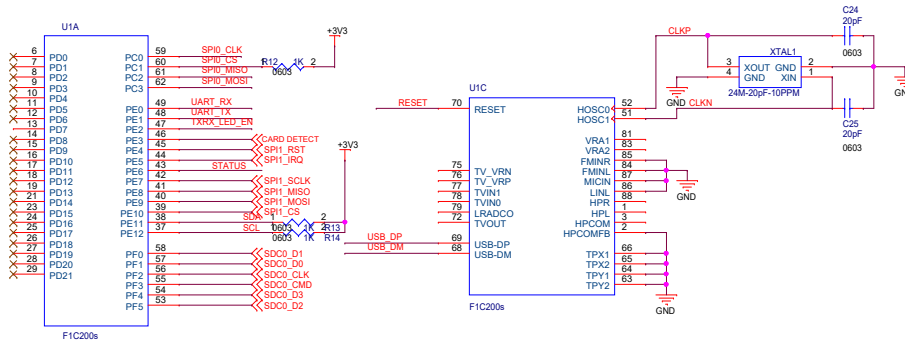
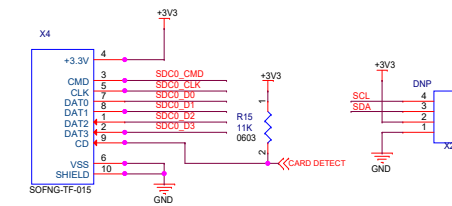


CPU

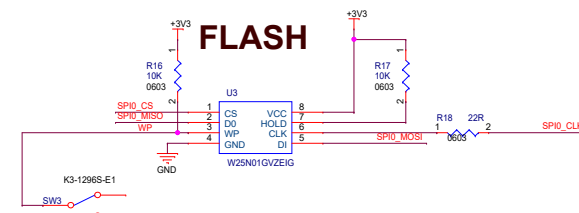


SD CARD

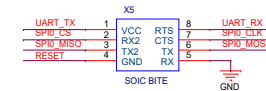
QWIC CONN



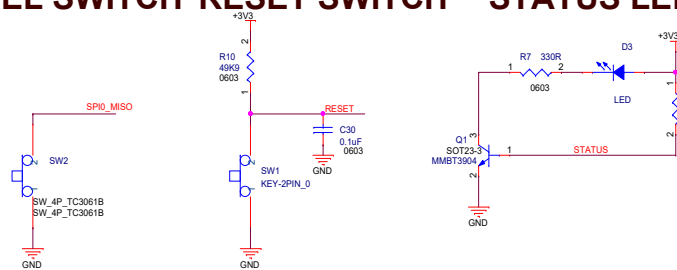
FLASH



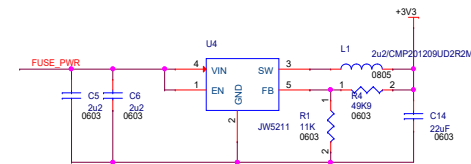
SOIC BITE



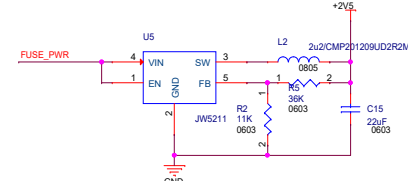
FEL SWITCH RESET SWITCH STATUS LED



VOLTAGE REGULATORS

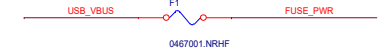


3.3V SYSTEM & I/O

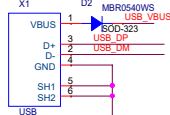


2.5V DRAM

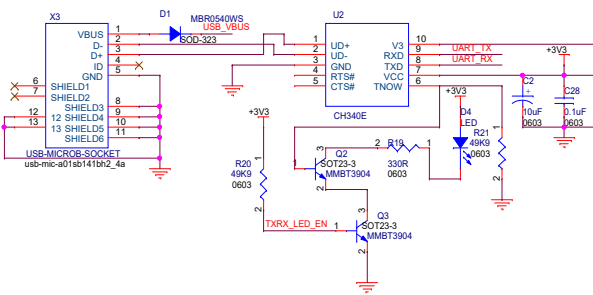
INPUT FUSE



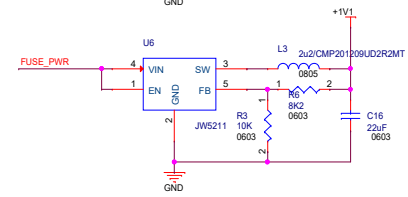
USB



USB Serial Debug



1.1V CORE VOLTAGE



SOURCE PARTS

POPSTICK		
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C	CC BY-SA 4.0	1.1
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