

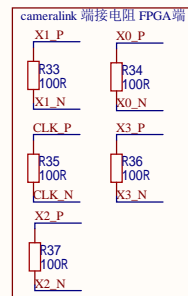
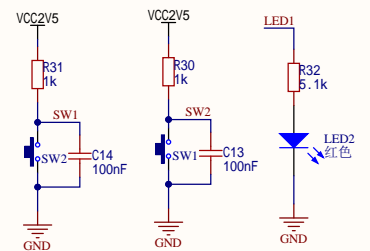
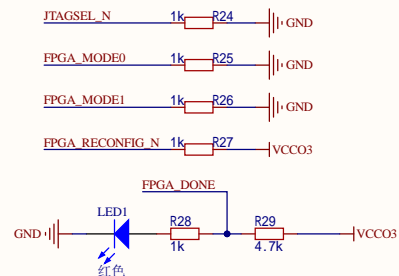
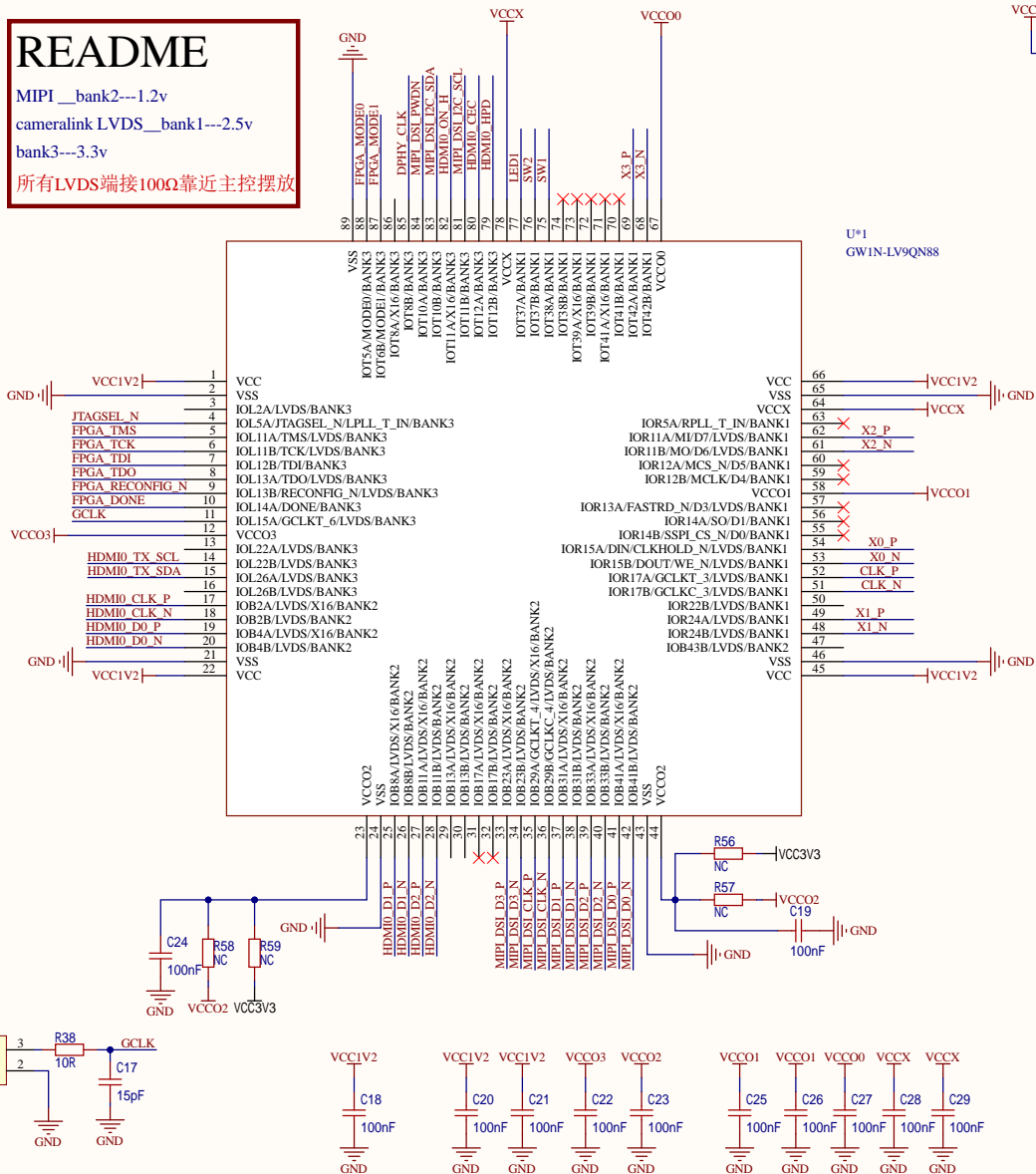
README

MIPI __bank2---1.2v

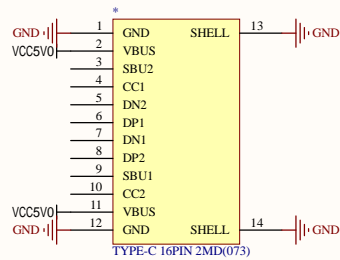
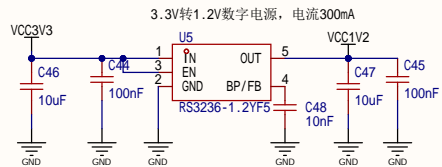
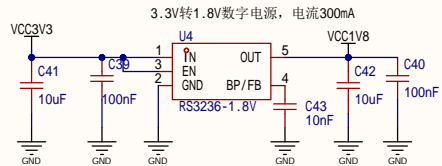
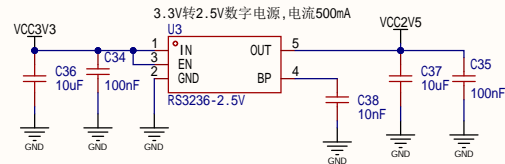
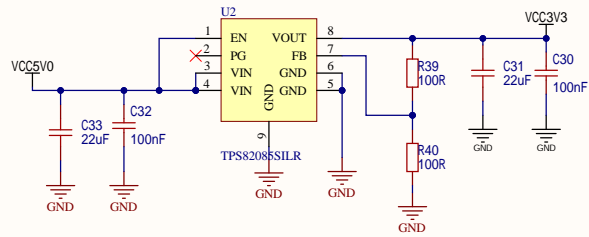
cameralink LVDS __bank1---2.5v

bank3---3.3v

所有LVDS端接100Ω靠近主控摆放



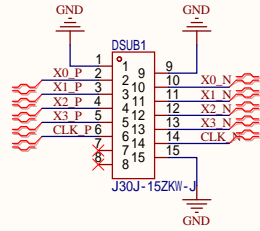
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Size	Number	Revision
A3		
Date:	3/16/2025	Sheet of
File:	C:\Users\...\GW1N.SchDoc	Drawn By:



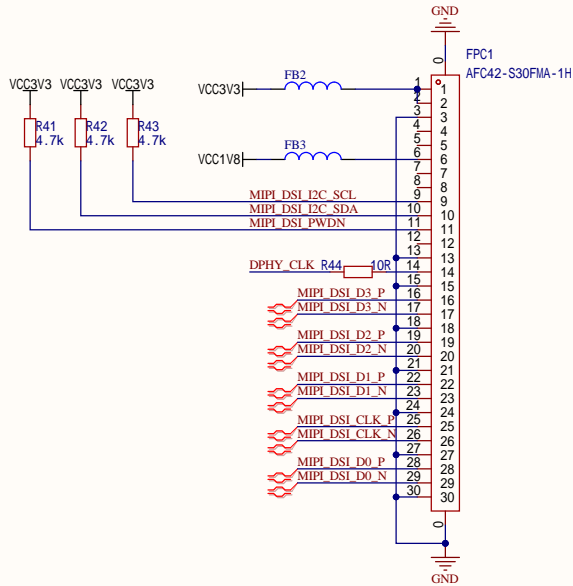
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Date: 3/16/2025	Sheet of	
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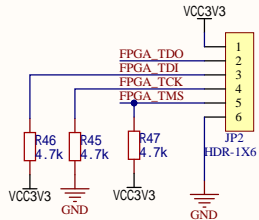
1, 9,	15LVDS_GND
2	X0+
10	X0-
3	X1+
11	X1-
4	X2+
12	X2-
5	X3+
13	X3-
6	CLK+
14	CLK-
7,8	NC



B



C



D

Title		
Size	Number	Revision
A3		
Date:	3/16/2025	Sheet of
File:	C:\Users\...\COM.SchDoc	Drawn By:

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C

D