

# MCAL User Manual for Fls\_17\_Dmu

## 32-bit TriCore™ AURIX™ TC3xx microcontroller

### About this document

#### Scope and purpose

This User Manual is intended to enable users to integrate the Microcontroller Abstraction Layer (MCAL) software for the TriCore™ AURIX™ family of 32-bit microcontrollers.

This document describes responsibilities of integrator in-charge of integrating MCAL software with the basic software (BSW) stack. This document also provides detailed information on safety, configuration and functions along with examples of usage of significant features.

*Note: Detailed information about package installation, safety and other generic information that are common across all modules are provided in MCAL User Manual General.*

#### Intended audience

This document is intended for anyone using the Fls\_17\_Dmu module of the TC3xx MCAL software.

#### Document conventions

**Table 1** Conventions

Convention	Explanation
<b>Bold</b>	Emphasizes heading levels, column headings, table and figure captions, screen names, windows, dialog boxes, menus, sub-menus
<i>Italics</i>	Denotes variable(s) and reference(s)
<code>Courier</code>	Denotes APIs, functions, interrupt handlers, events, data types, error handlers, file/folder names, directories, command line inputs, code snippets
<code>New</code>	
>	Indicates that a cascading sub-menu opens when you select a menu item
[cover parentID=<alpha numeric value>]	Used for traceability completeness. Reader should ignore these.

#### Reference documents

This User Manual should be read in conjunction with the following documents:

- AURIX™ TC3xx MCAL User Manual General
- Specification of Flash Driver, AUTOSAR\_SWS\_Flash\_Driver, AUTOSAR Release 4.2.2
- Specification of Flash Driver, AUTOSAR\_SWS\_Flash\_Driver, AUTOSAR Release 4.4.0

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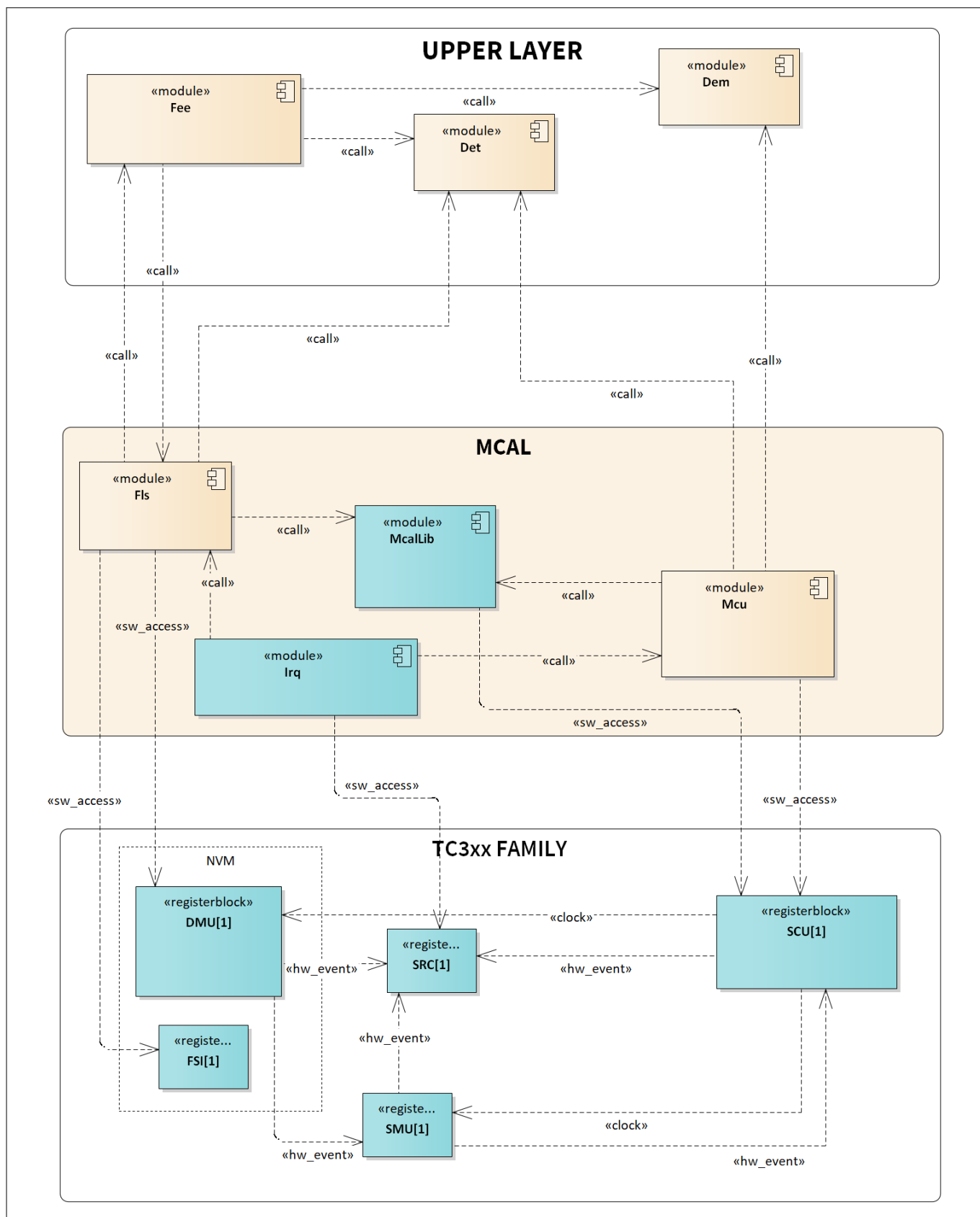
**1 Fls\_17\_Dmu driver****1 Fls\_17\_Dmu driver****1.1 User information****1.1.1 Description**

The FLS driver offers well-defined configuration and standard services as per AUTOSAR for the initialization, read, write and erase of DFlash0. Apart from this there are some non-AUTOSAR services provided as well for example Fls\_17\_Dmu\_CompareWordsSync, Fls\_17\_Dmu\_CancelNonEraseJobs, Fls\_17\_Dmu\_VerifyErase, Fls\_17\_Dmu\_VerifySectorErase, Fls\_17\_Dmu\_GetNotifCaller and so on. User gets an encapsulated access to the underlying DFlash0 through the FLS driver. The scope of the FLS driver is limited only to the DFlash0 Bank. The module is delivered as Post-Build variant.

**1.1.2 Hardware-software mapping**

This section describes the system view of the FLS driver and peripherals administered by it.

### 1 Fls\_17\_Dmu driver



**Figure 1 Mapping of hardware-software interfaces**

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**1 Fls\_17\_Dmu driver****1.1.2.1 DMU - DFlash0: primary hardware peripheral****DMU (DFlash0): primary hardware peripheral****Hardware functional features**

The FLS driver uses the DMU for operations such as read, write, suspend, resume, user content count (hardening) and erase DFlash0 memory. The key hardware functional features used by the driver are:

- Single ended sensing mode support for DFlash 0
- Writing and erasing DFlash 0:
  - i. 8 bytes page programming and 32 bytes burst programming
  - ii. Erase by multi-sector erase commands
- Suspend, resume for erase operation
- Interrupt service requests for end of busy (EOBM bit) for erase and write operations in hardware

The unsupported features of the DMU are:

- Complement sensing mode for DFlash0
- ECC error reporting to safety management unit (SMU)
- Suspend, resume for write operation

**Users of the hardware**

The FLSLOADER and FLS drivers utilize the DMU IP module. FLS is used during runtime and FLSLOADER is used during the boot. Hence, the access to the DMU registers is not concurrent

**Hardware diagnostic features**

- The ECC is used for error detection. Dynamic correction of single, double and triple-bit errors and detection of quad-bit errors
- The SMU alarms configured for the DMU are not monitored by the FLS driver

**Hardware events**

The following hardware events notified by SFR flags are used in FLS driver:

- Error flags are raised upon occurrence of errors in programming, erasing, reading or erase suspend / resume operation
- Erase verify error (EVER): This flag is set by the erase commands when there is an erase verification error
- Program verify error (PVER): This flag is set by the program commands when there is a program verification error
- Protection error (PROER): This flag is set by the hardware when write or erase command executed on protected memory section
- Operation Error (OPER): This flag is set by the hardware when Flash standard interface (FSI) encounters any error
- Sequence Error (SQER): This flag is set by the hardware when improper DMU command sequences are executed
- End of busy (EOBM): This flag enables the interrupt to report the end of erase and program operations



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**1 Fls\_17\_Dmu driver****1.1.2.2 FSI: dependent hardware peripheral****FSI: dependent hardware peripheral****Hardware functional features**

Following are the features supported by FSI:

- DMU interfaces to the FSI for all flash operations
- The result of user content count is given by FSI as the number of logic 1 bits in the selected pages at the selected reference current.

**Users of the hardware**

FLS driver accesses the FSI for hardening check operation.

**Hardware diagnostic features**

The SMU flags related to FSI are not monitored by the FLS driver.

**Hardware events**

- Hardware events from FSI are used by the FLS driver for hardening (user content count) check operation:
- The result of user content count command is returned as 13-bit unsigned integer with bits [7:0] in FSI\_COMM\_1.COMM1 [7:0] and bits [12:8] in FSI\_COMM\_2.COMM2 [4:0]. The result is returned as 13-bit unsigned integer with bits [7:0] in FSI\_COMM\_1.COMM1 [7:0] and bits [12:8] in FSI\_COMM\_2.COMM2 [4:0].

**1.1.2.3 SCU: dependent hardware peripheral****SCU: dependent hardware peripheral****Hardware functional features**

The FLS driver depends on the SCU IP for the clock, ENDINIT and reset functionalities. The driver requires the  $f_{SRI}$ ,  $f_{FSI}$  and  $f_{SPB}$  clock signals for functioning. The system clock is set up through the MCU driver. It is mandatory for the user to set up an appropriate system clock.

**Users of the hardware**

The SCU IP supplies clock for all the peripherals and the MCU driver is responsible for configuring the clock tree. To avoid conflicts due to simultaneous writes, update to all the ENDINIT protected registers is performed using the MCALLIB APIs.

**Hardware diagnostic features**

The SMU alarms configured for the SCU IP are not monitored by the FLS driver.

**Hardware events**

Hardware events from the SCU are not used by the FLS driver.

**1.1.2.4 SRC: dependent hardware peripherals****SRC: dependent hardware peripheral****Hardware functional features**

The FLS driver depends on the interrupt router for raising an interrupt to the CPU based on the end of busy event, which indicates the end or finish of the ongoing erase or write job in the HW.

**Users of the hardware**

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## **1 Fls\_17\_Dmu driver**

The interrupt router is configured either by the IRQ driver or the user software. Interrupt mode is not available when FLS is used with Infineon FEE.

### **Hardware diagnostic features**

The SMU alarms configured for interrupt router are not monitored by the FLS driver.

### **Hardware events**

The interrupt events raised by the interrupt router are serviced by the CPU. The FLS driver provides interrupt handlers as software interfaces, which must be invoked from the ISR. The following hardware events/interrupts are notified for DMU DFlash0:

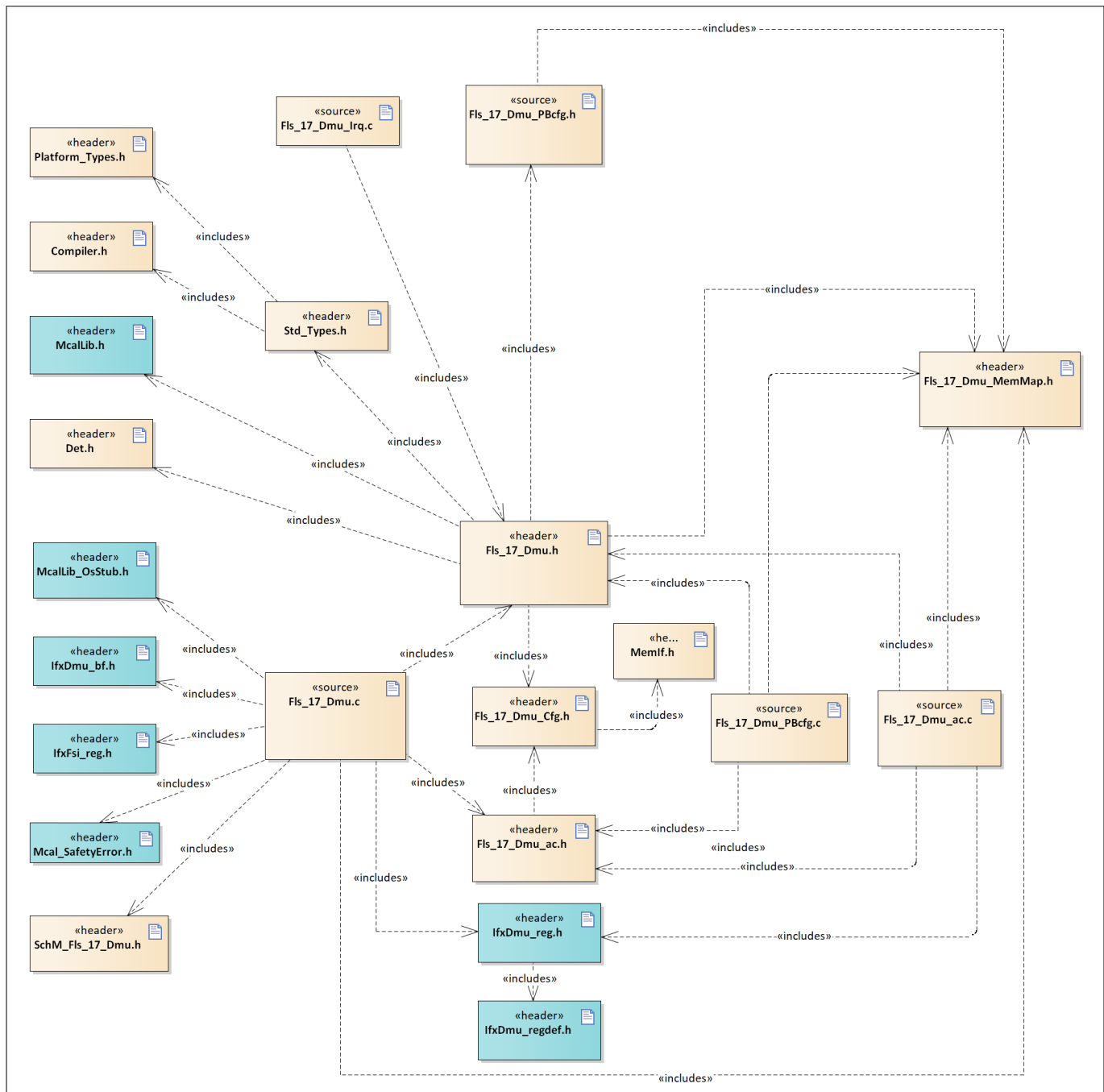
- Programming completion through end of busy (EOB)
- Erase completion through end of busy (EOB)

## **1.1.3 File structure**

### **1.1.3.1 C file structure**

This section provides details of the C files of the FLS driver.

### 1 Fls\_17\_Dmu driver



**Figure 2** Fls\_C\_file\_structure-1.png

**Table 2** C file structure

File name	Description
Compiler.h	Provides abstraction from compiler-specific keywords
Det.h	Provides the exported interfaces of Development Error Tracer
Fls_17_Dmu.c	This file contains functionality of the FLS driver. Version checks are also done in this file.
Fls_17_Dmu.h	This header file exports macros, type definitions, interrupt service routine and function prototypes for the Flash driver

## 1 Fls\_17\_Dmu driver

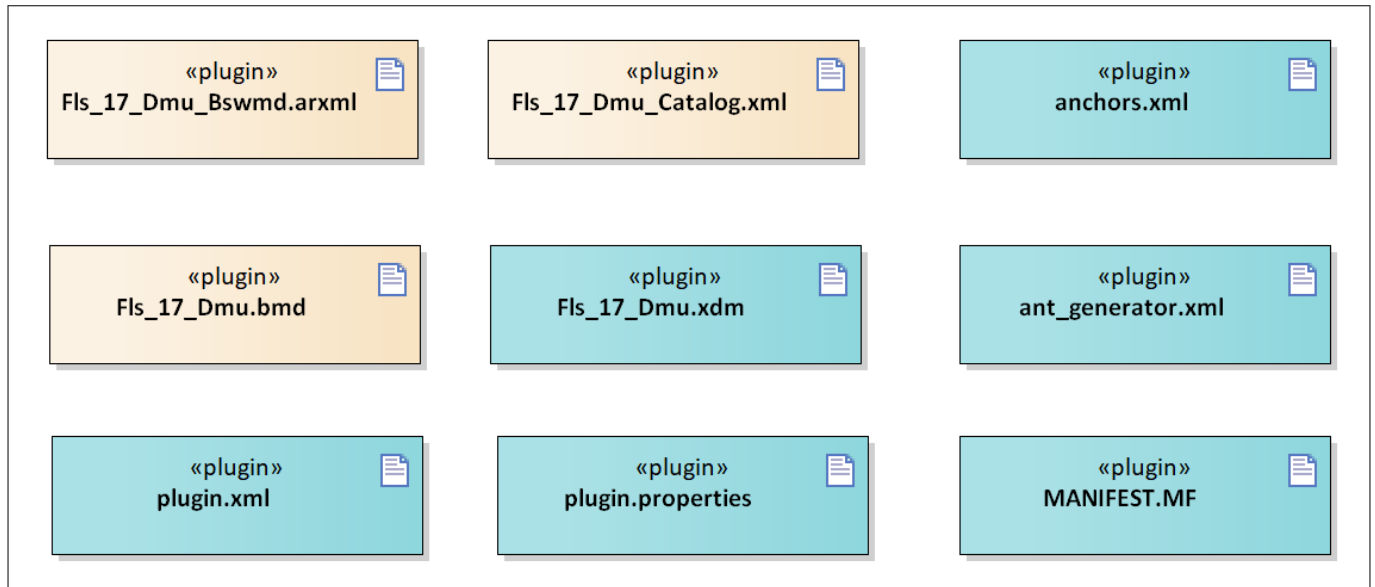
**Table 2 C file structure (continued)**

File name	Description
Fls_17_Dmu_Cfg.h	Contains driver pre-compile configuration parameters. Contain definitions for all pre-compile time configuration parameters defined as pre-processor directive which are specified for BSW module.
Fls_17_Dmu_Irq.c	Interrupt handler file for FLS.
Fls_17_Dmu_MemMap.h	File containing the memory section definitions used by the FLS driver.
Fls_17_Dmu_PBcfg.c	Contains driver post-build configuration parameters.
Fls_17_Dmu_PBcfg.h	File (generated) containing declaration of the post-build configuration data structures.
Fls_17_Dmu_ac.c	Command cycles for Flash operations.
Fls_17_Dmu_ac.h	Header file for macros used by Fls_17_Dmu_ac.c.
IfxDmu_bf.h	SFR header file for Dmu.
IfxDmu_reg.h	SFR header file for Dmu.
IfxDmu_regdef.h	SFR header file for Dmu.
IfxFsi_reg.h	SFR header file for FSI.
McalLib.h	Static header file defining prototypes of data structure and APIs exported by the MCALLIB.
McalLib_OsStub.h	McalLib_OsStub.h provides macros to support user mode of Tricore. This shall be included by other drivers to call OS APIs.
Mcal_SafetyError.h	Header file containing the prototype of the API for reporting safety-related errors.
MemIf.h	Header file containing exported interfaces and type definitions of MemIf module.
Platform_Types.h	Platform-specific type declaration file as defined by AUTOSAR.
SchM_Fls_17_Dmu.h	Header file containing prototype of the scheduled function of the Fls driver.
Std_Types.h	Standard type declaration file as defined by AUTOSAR. It is independent of compiler or platform.

### 1.1.3.2 Code generator plugin files

This section provides details of the code generator plugin files of the FLS driver.

### 1 Fls\_17\_Dmu driver



**Figure 3** Fls\_Code\_generator\_plugin\_files-1.png

**Table 3** Code generator plugin files

File name	Description
Fls_17_Dmu.bmd	AUTOSAR format XML data model schema file (for each device)
Fls_17_Dmu.xdm	Tresos format XML data model schema file
Fls_17_Dmu_Bswmd.arxml	AUTOSAR format module description file
Fls_17_Dmu_Catalog.xml	AUTOSAR format catalog file
MANIFEST.MF	Tresos plugin support file containing the meta-data for FLS driver
anchors.xml	Tresos anchors support file for the FLS driver
ant_generator.xml	Tresos support file to generate and rename multiple post-build configuration when using variation point.
plugin.properties	Tresos plugin support file for the FLS driver
plugin.xml	Tresos plugin support file for the FLS driver

### 1.1.4 Integration hints

This section lists the key points that an integrator or user of the FLS driver must consider.

#### 1.1.4.1 Intergration with AUTOSAR stack

This section lists the modules, which are not part of MCAL, but are required to integrate the FLS driver.

- **EcuM**

The ECU Manager module is a part of the AUTOSAR stack that manages common aspects of ECU. Specifically, in the context of the MCAL, the EcuM is used for initialization and de-initialization of the

## 1 Fls\_17\_Dmu driver

software drivers. The EcuM module provided in the MCAL package is a stub code and needs to be replaced with a complete EcuM module during the integration phase.

- **Memory mapping**

Memory mapping is a concept from AUTOSAR that allows relocation of text, variables, constants and configuration data to user-specific memory regions. To achieve this, all the re-locatable elements of the driver are encapsulated in different memory-section macros. These macros are defined in the file `Fls_17_Dmu_MemMap.h`.

The `Fls_17_Dmu_MemMap.h` file is provided in the MCAL package as a stub code. The integrator must place appropriate compiler pragmas within the memory-section macros. The pragmas ensure that the elements are re-located to the correct memory region. A sample implementation listing the memory-section macros is shown as follows.

```
#if defined FLS_17_DMU_START_SEC_VAR_CLEARED_ASIL_B_LOCAL_UNSPECIFIED
/*User pragma here*/
  #undef FLS_17_DMU_START_SEC_VAR_CLEARED_ASIL_B_LOCAL_UNSPECIFIED
  #undef MEMMAP_ERROR
#elif defined FLS_17_DMU_STOP_SEC_VAR_CLEARED_ASIL_B_LOCAL_UNSPECIFIED
/*User pragma here*/
  #undef FLS_17_DMU_STOP_SEC_VAR_CLEARED_ASIL_B_LOCAL_UNSPECIFIED
  #undef MEMMAP_ERROR
#elif defined FLS_17_DMU_START_SEC_VAR_CLEARED_ASIL_B_LOCAL_32
/*User pragma here*/
  #undef FLS_17_DMU_START_SEC_VAR_CLEARED_ASIL_B_LOCAL_32
  #undef MEMMAP_ERROR
#elif defined FLS_17_DMU_STOP_SEC_VAR_CLEARED_ASIL_B_LOCAL_32
/*User pragma here*/
  #undef FLS_17_DMU_STOP_SEC_VAR_CLEARED_ASIL_B_LOCAL_32
  #undef MEMMAP_ERROR
#elif defined FLS_17_DMU_START_SEC_CONFIG_DATA_ASIL_B_LOCAL_UNSPECIFIED
/*User pragma here*/
  #undef FLS_17_DMU_START_SEC_CONFIG_DATA_ASIL_B_LOCAL_UNSPECIFIED
  #undef MEMMAP_ERROR
#elif defined FLS_17_DMU_STOP_SEC_CONFIG_DATA_ASIL_B_LOCAL_UNSPECIFIED
/*User pragma here*/
  #undef FLS_17_DMU_STOP_SEC_CONFIG_DATA_ASIL_B_LOCAL_UNSPECIFIED
  #undef MEMMAP_ERROR
#elif defined FLS_17_DMU_START_SEC_CODE_ASIL_B_LOCAL
/*User pragma here*/
  #undef FLS_17_DMU_START_SEC_CODE_ASIL_B_LOCAL
  #undef MEMMAP_ERROR
#elif defined FLS_17_DMU_STOP_SEC_CODE_ASIL_B_LOCAL
/*User pragma here*/
  #undef FLS_17_DMU_STOP_SEC_CODE_ASIL_B_LOCAL
  #undef MEMMAP_ERROR
#endif
```

- **DET**

The DET module is a part of the AUTOSAR stack that handles all the development, runtime errors and transient faults reported by the BSW. The FLS driver reports all the development errors through the `Det_ReportError()` API, runtime errors through the `Det_ReportRuntimeError()` API and transient faults through the `Det_ReportTransientFault()` API to the DET module. The user of the FLS

## 1 Fls\_17\_Dmu driver

driver must process all the errors reported to the DET module through the `Det_ReportError()`, `Det_ReportRuntimeError()` and `Det_ReportTransientFault()` APIs.

The `Det.h` and `Det.c` files are provided in the MCAL package as a stub code and need to be replaced with a complete DET module during the integration phase.

- **DEM**

The DEM module is not required for the integration of FLS driver.

- **SchM**

The SchM is not required for the integration of FLS driver.

- **Safety error**

The FLS driver will report all the detected safety errors through the `Mcal_ReportSafetyError()` API.

The driver performs only detection and reporting of the safety errors. The handling of the reported errors shall be done by the user. The `Mcal_ReportSafetyError()` API is provided in the `Mcal_SafetyError.c` and `Mcal_SafetyError.h` files as a stub code, and must be updated by the integrator to handle the reported errors.

- **Notifications and callbacks**

The FLS driver does not implement any notifications. However, the FLS driver reports the job end and error through notification function. These notification functions can be configured by the user in the EB Tresos.

- **Operating system(OS)**

The OS or application must ensure correct type of service and interrupt priority is configured in the SR register. Enabling and disabling of interrupts must also be managed by the OS or application.

The OS files provided by MCAL package is only an example code and must be updated by the integrator with the actual OS files for the desired function.

### 1.1.4.2 Multicore and Resource Manager

The FLS driver does not support execution on multiple cores.

### 1.1.4.3 MCU support

The FLS driver is dependent on the MCU driver for the clock configuration. The initialization of the FLS driver must be started only after completing the MCU initialization.

### 1.1.4.4 Port support

The FLS driver does not use any services provided by the PORT driver.

### 1.1.4.5 DMA support

The FLS driver does not use any services provided by the DMA driver.

### 1.1.4.6 Interrupt connections

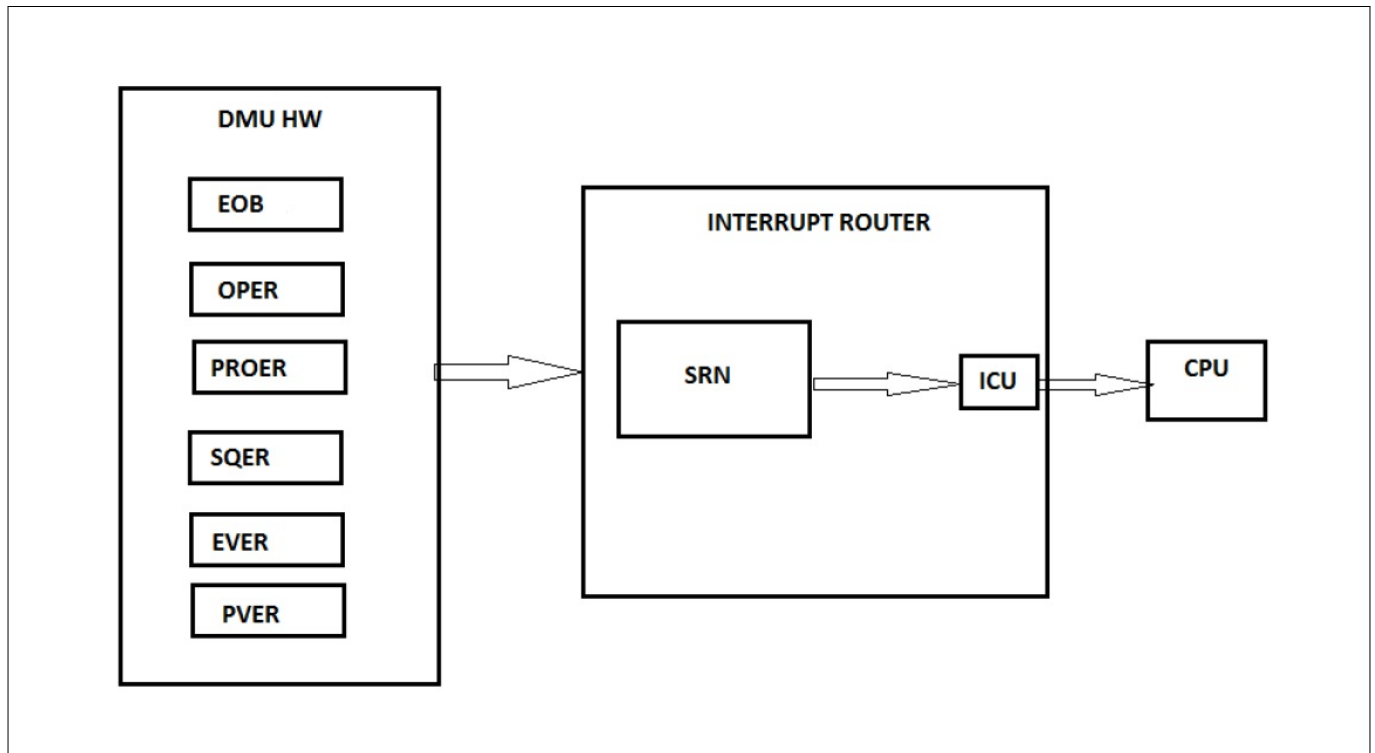
The following events can trigger an interrupt service request to the Interrupt Router (IR)

- End of BUSY(EOB): if `DMU_HF_EER.EOBM = 1B` and one of the `DMU_HF_STATUS` flags `D0BUSY`, `D1BUSY` or `PFlash` flags transitions from 1 to 0 then an interrupt service request is triggered (for example wake-up, erase sequences or program sequences)
- Operation Error (OPER): if `DMU_HF_EER.OPERM = 1B` and `DMU_HF_ERRSR.OPER` flag is set
- Protection Error (PROER): if `DMU_HF_EER.PROERM = 1B` and `DMU_HF_ERRSR.PROER` flag is set

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- Sequence Error (SQER): if DMU\_HF\_EER.SQERM = 1B and DMU\_HF\_ERRSR.SQER flag is set
- Program Verify Error (PVER): if DMU\_HF\_EER.PVERM = 1B and DMU\_HF\_ERRSR.PVER flag is set
- Erase Verify Error (EVER): if DMU\_HF\_EER.EVERM = 1B and DMU\_HF\_ERRSR.EVER flag is set

The event that triggered the interrupt can be determined from the DMU\_HF\_STATUS and DMU\_HF\_ERRSR registers. An interrupt event must be triggered when the event appears again and the corresponding status flag is still set. The FLS driver enables and uses only EOB interrupt. Other interrupt mentioned are not used by FLS driver. End of BUSY interrupts are only generated after completion of start-up. The following diagram depicts the interrupt connections of DMU data Flash:



**Figure 4** Interrupt mode

Invoking of interrupt handlers provided by the driver must be done by the user. A sample invocation of FLS driver interrupt handler is as follows:

```

ISR(DMUHOST_ISR)
/* Enable Global Interrupts */
{
    ENABLE();

    /* Call to Flash Interrupt function */
    Fls_17_Dmu_Isr();
}
  
```



## 1 Fls\_17\_Dmu driver

### 1.1.4.7 Example usage

This section explains an example usage scenario of the FLS driver for a nominal case. Applications usually adopt and modify the configuration and usage sequence as per their use-case.

#### Configuration of the driver

The configuration of the driver involves the following steps.

1. Configuration of the System Clock  $f_{sys}$ . This configuration is done using the MCU driver.
2. Configuration of the FLS driver: The Flash driver is delivered as a post-build. The configuration of sectors should be done in the `FlsSectorList` container.

The `FlsSector` within the `FlsSectorList` container requires the following

parameter: `FlsNumberOfSectors`(number of sectors), `FlsSectorSize`(sector size) and the `FlsSectorStartAddress`(Start/Begin address of the sector).

*Note: This also has a dependency on whether the IFX FEE has been used or not.*

#### Initialization of Flash driver

The following code snippet shows the steps involved in the initialization of the Flash driver.

```
#include "Std_Types.h"
#include "Mcu.h"
#include "Fls_17_Dmu.h"
#include "Irq.h"

extern const Mcu_ConfigType Mcu_Config;
extern const Fls_17_Dmu_ConfigType Fls_17_Dmu_Config;

/*Initialization of MCU*/
Mcu_Init(&Mcu_Config);
Mcu_InitClock(0U);
while(Mcu_GetPllStatus() != MCU_PLL_LOCKED);
Mcu_DistributePllClock();

/* Initialization of flash module */
Fls_17_Dmu_Init(&Fls_17_Dmu_Config);
#if FLS_USE_INTERRUPTS == ON
/* Configure FLS Module Interrupt Priority.
Use only for FLS INTERRUPT Mode. */
IrqDmu_Init();
#endif
```

#### Flash operations

`Fls_17_Dmu_MainFunction()` is the only scheduled function provided by the FLS driver. This function should be called periodically, so that it can process the jobs without hardware interrupt support. This API is a service for performing the processing of the Flash read, write, erase and compare jobs. The timeout monitoring of erase or write operations is done based on the hardware(STM) timers. Timeout monitoring is not done for read or compare as the read times are considerably small to monitor through `Fls_17_Dmu_MainFunction()` cycle time.

## 1 Fls\_17\_Dmu driver

The code snippet shows an example of the steps involved in erasing, writing and reading a data Flash bank after initialization of the Flash.

```
#define FLS_17_DMU_NVMSECTOR0_STARTADDRESS (0xaf000000U)
#define FLS_17_DMU_NVMSECTOR_SIZE (0x20000U)
#define FLS_17_DMU_PAGE_SIZE (8U)

....
uint8 Test_ProgData[2 * FLS_17_DMU_PAGE_SIZE]; /*write buffer*/
uint8 Test_ReadData[2 * FLS_17_DMU_PAGE_SIZE]; /*read buffer*/
...

/*Demo erase*/
Std_ReturnType Fls_DemoErase(void)
{
    /* Erase DFLASH BANK 0 */
    ReturnVal =
    Fls_17_Dmu_Erase(FLS_17_DMU_NVMSECTOR0_STARTADDRESS, FLS_17_DMU_NVMSECTOR_SIZE);

    /*If erase job scheduled properly*/
    if(ReturnVal == E_OK)
    {
        /* Poll till Erase completed */
        while(Fls_17_Dmu_GetStatus() != MEMIF_IDLE)
        {
            Delay(); /*Delay should correspond to the FlsCallCycle value configured by
the user*/
        }

        Fls_17_Dmu_MainFunction();
    }
    if(Fls_17_Dmu_GetJobResult() != MEMIF_JOB_OK)
    {
        ReturnVal = E_NOT_OK;
    }
}

/*Demo write*/
Std_ReturnType Fls_DemoWrite(void)
{
    /* Assuming the write bufferTest_ProgData is already filled with some data
Write first 2 pages of DFLASH BANK 0.*/
    ReturnVal = Fls_17_Dmu_Write(FLS_BANK0_ADDR, Test_ProgData, (2*FLS_PAGESIZE));

    /*If the write job scheduled properly*/
    if(ReturnVal == E_OK)
    {
        /* Poll till Erase completed */
        while(Fls_17_Dmu_GetStatus() != MEMIF_IDLE)
        {
            Delay(); /*Delay should correspond to the FlsCallCycle value configured by
the user*/
        }
    }
}
```

## 1 Fls\_17\_Dmu driver

```

    Fls_17_Dmu_MainFunction();
}
if(Fls_17_Dmu_GetJobResult() != MEMIF_JOB_OK)
{
    ReturnVal = E_NOT_OK;
}
}

Std_ReturnType Fls_DemoRead(void)
{
    /* Read the first two pages */
    ReturnVal = Fls_17_Dmu_Read(FLS_17_DMU_NVMSECTOR0_STARTADDRESS, Test_ReadData,
    (2 * FLS_PAGESIZE));

    if(ReturnVal == E_OK)
    {
        while(Fls_17_Dmu_GetStatus() != MEMIF_IDLE)
        {
            /* Wait till Write is completed */
            Fls_17_Dmu_MainFunction();
        }

        if(Fls_17_Dmu_GetJobResult() != MEMIF_JOB_OK)
        {
            ReturnVal = E_NOT_OK;
        }
    }
}

```

### Configuration of FlsIllegalStateNotification

User shall configure an user defined function to handle the illegal state. Please refer the FLS demo application for example.

### Concurrent access to DFlash0 from FLS driver and user application

It is the responsibility of the integrator to prevent concurrent access to the data flash including the UCB. FLS driver is designed assuming exclusive access to DFlash0. If DFlash0 is shared by FLS driver and user implemented application, then the user needs to take care of the handshaking between FLS driver and user application to avoid concurrent access to DFlash0.

## 1.1.5 Key architectural considerations

### 1.1.5.1 API Naming Convention

To meet AUTOSAR specification for the module with an upper multiplicity greater than 1, all the external interfaces in the FLS module are named in the following manner:

<Module Short Name>\_<VendorId>\_<VendorApiInfix>\_<ServiceName>()

The same is followed for error handling. For instance, the name for the Development error "Timeout exceeded" shall be formed in the following way: <MIP>\_E\_TIMEOUT where <MIP> is the Module implementation prefix of the BSW Module.

---

**1 Fls\_17\_Dmu driver****1.1.5.2 Error reporting in case of erase and write verification failures for ASR422 and ASR440 versions**

In case of ASR422, if erase verification failure (EVER) occurs, the following errors will be reported.

- FLS\_17\_DMU\_E\_VERIFY\_ERASE\_FAILED DET if DET/Safety is enabled
- FLS\_17\_DMU\_E\_ERASE\_FAILED RTE if runtime error detection is enabled

Similarly, in case of write verification failure, the following errors will be reported.

- FLS\_17\_DMU\_E\_VERIFY\_WRITE\_FAILED DET if DET/Safety is enabled
- FLS\_17\_DMU\_E\_WRITE\_FAILED RTE if runtime error detection is enabled

In case of ASR440, if erase verification failure (EVER) occurs, the following errors will be reported.

- FLS\_17\_DMU\_E\_VERIFY\_ERASE\_FAILED RTE if runtime error detection and `FlsEraseVerificationEnabled` are enabled
- FLS\_17\_DMU\_E\_ERASE\_FAILED transient fault

Similarly, in case of write verification failure, the following errors will be reported.

- FLS\_17\_DMU\_E\_VERIFY\_WRITE\_FAILED if runtime error detection and `FlsWriteVerificationEnabled` are enabled
- FLS\_17\_DMU\_E\_WRITE\_FAILED transient fault

## 1 Fls\_17\_Dmu driver

### 1.2 Assumptions of Use (AoU)

The AoU for the FLS driver are as follows.

- **Check for initialization**

The integrator shall ensure that proper initialization is done by calling the Fls\_17\_Dmu\_Init() API before invoking any other service of the FLS driver.

[cover parentID FLS={78C52790-FD02-4374-ABC5-1E94933BAAAA}]

- **FLS initialization and Initcheck**

The integrator shall verify the correctness of initialization by calling the Fls\_17\_Dmu\_InitCheck() API after the initialization is completed by the Fls\_17\_Dmu\_Init() API.

The Fls\_17\_Dmu\_InitCheck() API checks if the initialized fixed global SFRs and fixed global variables of the FLS driver are initialized according to the configuration.

[cover parentID FLS={4E3B5CD0-694B-410c-A6B1-EDEAE53603CB}]

- **Working of suspend in standalone mode**

When FLS driver is used in standalone mode, the Fls\_17\_Dmu\_SuspendErase() API shall be invoked by the application only when the previous job requested was an erase operation. This is to ensure that any operation other than erase is not suspended unintentionally.

[cover parentID FLS={F516B301-0F41-4864-B0E7-F92DAABC0EEA}]

- **Clock set-up**

Clocks are not set up by the FLS driver. The integrator shall ensure that the clocks needed for the flash operations on DFLASH0 are correctly set up using the MCU driver.

[cover parentID FLS={F34583B5-3E53-4bf2-8CCC-E64FC399B03B}]

- **Non-reentrant APIs**

The FLS driver's APIs are non-reentrant and therefore, the integrator shall ensure that multiple invocation of the FLS API(s) does not occur from different contexts, threads or cores.

[cover parentID FLS={71BD2EA3-E26F-44e3-ADCB-C2F0D64080D2}]

- **Non-usage of DFlash1**

When the FLS driver is being used for operations on the DFlash0, the integrator shall ensure that the DFlash1 is not used independently by any other driver, except for the HSM operations.

[cover parentID FLS={8CC1F5A8-581B-4c82-8364-12E90AF1E1DA}]

- **Using FLS for DFlash1 operation**

The integrator shall not use the FLS driver to perform operations on the DFlash1 hardware.

[cover parentID FLS={8D39DF0E-A919-4762-838D-4B9E9A90650C}]

- **ADER and bus access error behavior**

For bus access monitoring over SRI, the following errors are reported:

- SRI access address phase error:

If an ECC error occurs during the address phase of an SRI access, then the DMU\_HF\_ERRSR.ADER bit will be set and an error will be signaled to the SMU. The SRI access will terminate with an error. This error shall not be handled in the FLS driver and shall be handled by the user.

- SRI access write data phase error:

If an ECC error occurs on the data phase of an SRI write access, the an error will be signaled to the SMU. This error shall not be handled in the FLS driver and shall be handled by the user.

---

## 1 Fls\_17\_Dmu driver

[cover parentID FLS={D5F895FF-AD5F-4337-88E3-B5FC8116ADFF}]

- **Write address**

The integrator shall ensure the correctness of the TargetAddress for write operation and also ensure that this address is not protected against writes by the Flash driver.

[cover parentID FLS={4E1CFF64-D76E-440a-8B68-1EC5C9E9B28E}]

- **Access to FLS SFRs from CPU core**

Integrator shall ensure that the FLS driver is invoked from the CPU core that has access to the FLS SFR(s).

[cover parentID FLS={8120AD04-68B1-4eff-AE48-AD14FD6CCD14}]

- **Correctness of 'config pointer'**

The user shall ensure that the config pointer passed is correct.

[cover parentID FLS={95C5FF4A-CBB7-4c18-A117-B754402C4D2C}]

- **Correctness of DFlash0 size configuration**

The integrator shall ensure that the total size of DFlash0 in the hardware is greater than or equal to the size of the data flash (DFlash0) mentioned in the configuration.

[cover parentID FLS={EF72308E-CE84-46eb-9B83-D79951DB6D74}]

- **Invocation of Fls\_17\_Dmu\_GetNotifCaller() API**

The integrator shall ensure that Fls\_17\_Dmu\_GetNotifCaller() is called only from inside the callback notification functions invoked by the FLS.

The Fls\_17\_Dmu\_GetNotifCaller() is needed to identify the notification so that the caller can take appropriate action.

[cover parentID FLS={BE3D0479-4FB5-48e3-B995-10FE8AC2E49B}]

- **No multicore support**

Integrator shall ensure that all the FLS services are executed from one core only. The FLS does not support multicore capability.

[cover parentID FLS={49337170-0313-49f8-91CC-EE972E4A91FA}]

- **Precaution during read operation**

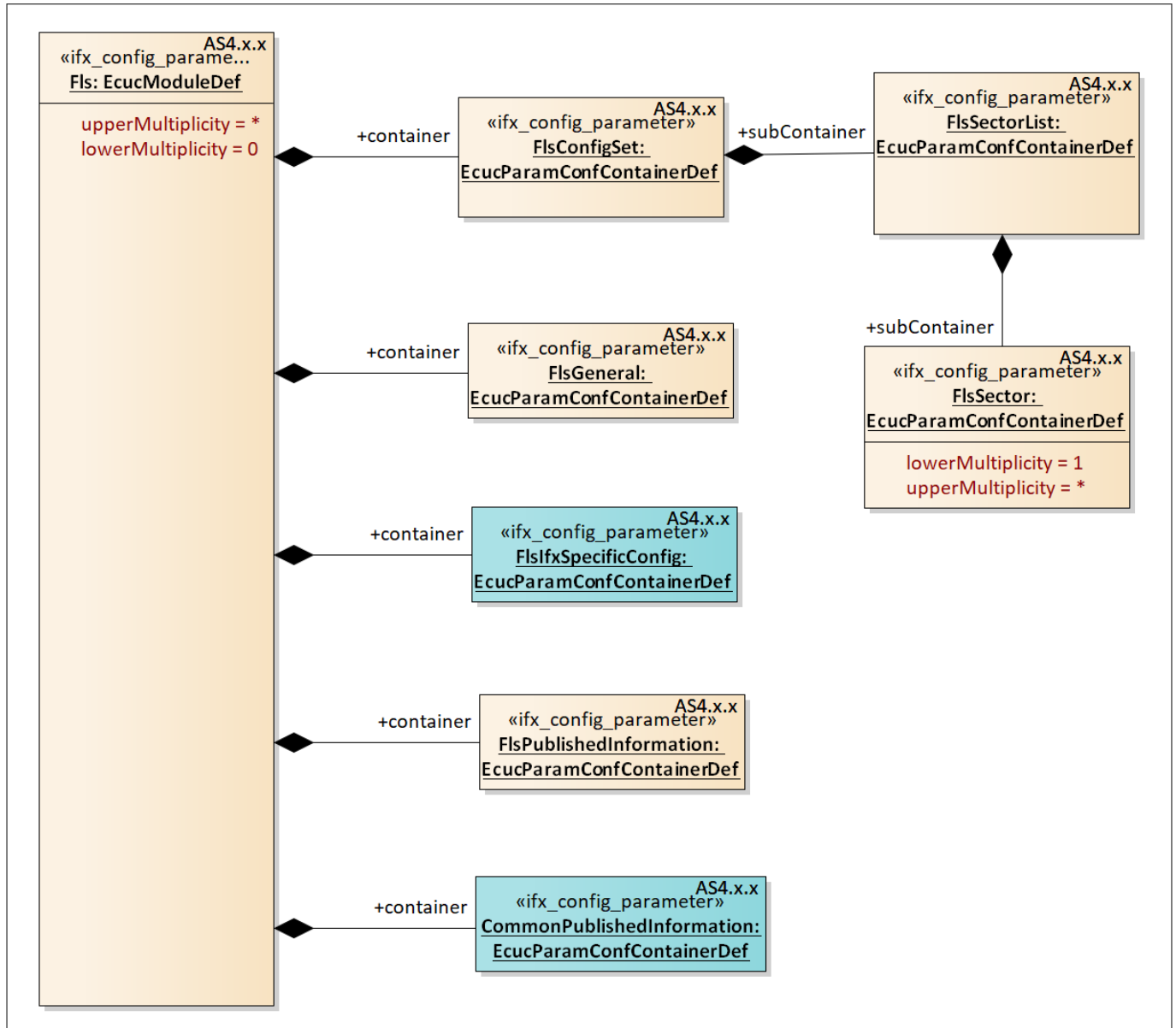
The integrator shall ensure that the source address given for read is not protected against reads.

[cover parentID FLS={F30D701D-9250-4fa0-A700-C8AB627D30A5}]

## 1 Fls\_17\_Dmu driver

### 1.3 Reference information

#### 1.3.1 Configuration interfaces



**Figure 5** Container hierarchy along with their configuration parameters

##### 1.3.1.1 Container: CommonPublishedInformation

This section describes the information about the module published by the FLS driver.

Post-Build Variant Multiplicity: -

Multiplicity Configuration Class: -

## 1 Fls\_17\_Dmu driver

### 1.3.1.1.1 ArMajorVersion

**Table 4** Specification for ArMajorVersion

<b>Name</b>	ArMajorVersion		
<b>Description</b>	Major version number of AUTOSAR specification on which the driver implementation is based on.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 255		
<b>Default value</b>	4		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.1.2 ArMinorVersion

**Table 5** Specification for ArMinorVersion

<b>Name</b>	ArMinorVersion		
<b>Description</b>	Minor version number of AUTOSAR specification on which the driver implementation is based on.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 255		
<b>Default value</b>	As per selected Autosar version		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.1.3 ArPatchVersion

**Table 6** Specification for ArPatchVersion

<b>Name</b>	ArPatchVersion		
-------------	----------------	--	--



**1 Fls\_17\_Dmu driver**
**Table 6 Specification for ArPatchVersion (continued)**

<b>Description</b>	Patch version number of AUTOSAR specification on which the driver implementation is based on.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 65535		
<b>Default value</b>	As per selected Autosar version		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.1.4 ModuleId**
**Table 7 Specification for ModuleId**

<b>Name</b>	ModuleId		
<b>Description</b>	Provides the module ID of the flash driver module ID as described by AUTOSAR : Wp1.1.2 Basic Software Module List		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 65535		
<b>Default value</b>	92		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.1.5 Release**
**Table 8 Specification for Release**

<b>Name</b>	Release		
<b>Description</b>	Specifies the derivate for which the configuration project is created.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucStringParamDef

## 1 Fls\_17\_Dmu driver

**Table 8 Specification for Release (continued)**

<b>Range</b>	String		
<b>Default value</b>	As per the configuration		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.1.6 SwMajorVersion

**Table 9 Specification for SwMajorVersion**

<b>Name</b>	SwMajorVersion		
<b>Description</b>	Major version number of the vendor specific implementation of the driver.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 255		
<b>Default value</b>	As per driver version.		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.1.7 SwMinorVersion

**Table 10 Specification for SwMinorVersion**

<b>Name</b>	SwMinorVersion		
<b>Description</b>	Minor version number of the vendor specific implementation of the driver.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 255		
<b>Default value</b>	As per driver version.		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-

## 1 Fls\_17\_Dmu driver

**Table 10 Specification for SwMinorVersion (continued)**

<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.1.8 SwPatchVersion

**Table 11 Specification for SwPatchVersion**

<b>Name</b>	SwPatchVersion		
<b>Description</b>	Patch version number of the vendor specific implementation of the driver.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 255		
<b>Default value</b>	As per driver version.		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.1.9 VendorApiInfix

**Table 12 Specification for VendorApiInfix**

<b>Name</b>	VendorApiInfix		
<b>Description</b>	<p>The parameter is used to specify the vendor specific name.</p> <p>Default value is set to Dmu, as this is the unique name of the Fls module provided by IFX.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucStringParamDef
<b>Range</b>	String		
<b>Default value</b>	Dmu		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-

## 1 Fls\_17\_Dmu driver

**Table 12 Specification for VendorApilInfix (continued)**

<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.1.10 VendorId

**Table 13 Specification for VendorId**

<b>Name</b>	VendorId		
<b>Description</b>	Specifies the vendor Id for Infineon		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 65535		
<b>Default value</b>	17		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.2 Container: Fls

This container holds the configuration of the FLS (internal or external) driver module.

The multiplicity describes the number of Flash drivers present, therefore, there will be one container for each Flash driver in the ECUC template. When no Flash driver is present, the multiplicity is 0.

Post-Build Variant Multiplicity: TRUE

Multiplicity Configuration Class: -

### 1.3.1.3 Container: FlsConfigSet

This container is for the runtime configuration parameters of the Flash driver.

Implementation Type: Fls\_17\_Dmu\_ConfigType.

Post-Build Variant Multiplicity: -

Multiplicity Configuration Class: -

**1 Fls\_17\_Dmu driver**
**1.3.1.3.1 FlsAcErase**
**Table 14 Specification for FlsAcErase**

<b>Name</b>	FlsAcErase		
<b>Description</b>	Address offset in RAM to which the erase flash access code shall be loaded. Used as function pointer to access the erase flash access code.  This parameter is not used and hence not supported. In TC3xx, Pflash and Dflash can be read in parallel and hence there is no need to load Dflash access code into RAM.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.3.2 FlsAcWrite**
**Table 15 Specification for FlsAcWrite**

<b>Name</b>	FlsAcWrite		
<b>Description</b>	Address offset in RAM to which the write flash access code shall be loaded. Used as function pointer to access the write flash access code.  This parameter is not used and hence not supported. In TC3xx, Pflash and Dflash can be read in parallel and hence there is no need to load Dflash access code into RAM.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1 Fls\_17\_Dmu driver**
**1.3.1.3.3 FlsCallCycle**
**Table 16 Specification for FlsCallCycle**

<b>Name</b>	FlsCallCycle		
<b>Description</b>	<p>Cycle time of calls of the main function for the Flash driver(in seconds).</p> <p>This parameter is used in the timeout monitoring for the write/erase jobs.</p> <p>A value of 10 ms is selected as default assuming that this duration would be a reasonable frequency to check the status of scheduled user jobs.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucFloatParamDef
<b>Range</b>	0.0001 - 1		
<b>Default value</b>	0.01		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.3.4 FlsDefaultMode**
**Table 17 Specification for FlsDefaultMode**

<b>Name</b>	FlsDefaultMode		
<b>Description</b>	<p>This parameter is the default read mode of the data flash(DFLASH0) on the device after initialization.</p> <p>The default value has been selected assuming that a read in MEMIF_MODE_SLOW mode(32 bytes) would be reasonable for the user.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucEnumerationParamDef
<b>Range</b>	<p>MEMIF_MODE_FAST: driver is working in the fast(burst) mode.</p> <p>MEMIF_MODE_SLOW: driver is working in the slow mode.</p>		
<b>Default value</b>	MEMIF_MODE_SLOW		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		

**1 Fls\_17\_Dmu driver**
**Table 17 Specification for FlsDefaultMode (continued)**

<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.
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**1.3.1.3.5 FlsEraseVerifyErrNotif**
**Table 18 Specification for FlsEraseVerifyErrNotif**

<b>Name</b>	FlsEraseVerifyErrNotif		
<b>Description</b>	<p>User defined notification function pointer of type 'void fn_name (void)'.</p> <p>This notification function is called by the FLS driver for giving notification of the EVER bit error during the erase job.</p> <p>If the FlsEraseVerifyErrNotif is configured as NULL, the notification functions are not called.</p> <p>This parameter is valid only if the Infineon FEE is used and should be configured as Fee_17_JobEraseErrorNotification. The Fee_17_JobEraseErrorNotification is the name of the Infineon FEE erase verification error notification function and therefore has been given as the default value. If the Infineon FEE is not used, then this parameter is not supported.</p> <p>The post build variant value is false for this parameter since the default value is non-editable.</p>		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucFunctionNameDef
<b>Range</b>	String		
<b>Default value</b>	Fee_17_JobEraseErrorNotification		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	FALSE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsIfxFeeUse		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.3.6 FlsJobEndNotification**
**Table 19 Specification for FlsJobEndNotification**

<b>Name</b>	FlsJobEndNotification		
<b>Description</b>	<p>User defined notification function pointer of type void fn_name (void).</p> <p>This notification function is called by the FLS driver on successful completion of the job.</p> <p>If the FlsJobEndNotification is configured as NULL, the notification functions are not called.</p> <p>If the Infineon FEE is used, it should be configured as Fee_JobEndNotification.</p> <p>Assuming the usage is with Infineon FEE, the default value has been set as Fee_JobEndNotification.</p> <p>The integrator or user has to verify the function address if numerical value is provided.</p>		

**1 Fls\_17\_Dmu driver**
**Table 19 Specification for FlsJobEndNotification (continued)**

<b>Multiplicity</b>	0..1	<b>Type</b>	EcucFunctionNameDef
<b>Range</b>	String		
<b>Default value</b>	Fee_JobEndNotification		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.3.7 FlsJobErrorNotification**
**Table 20 Specification for FlsJobErrorNotification**

<b>Name</b>	FlsJobErrorNotification		
<b>Description</b>	<p>User defined notification function pointer of type void fn_name (void).</p> <p>This notification function is called by the FLS driver on cancellation of the job or a failure in executing the job.</p> <p>If the FlsJobErrorNotification is configured as NULL, the notification functions is not called.</p> <p>If the Infineon FEE is used, it should be configured as Fee_JobErrorNotification.</p> <p>Assuming the usage with Infineon FEE, the default value has been given as Fee_JobErrorNotification.</p> <p>The integrator/user has to verify the function address if numerical value is provided.</p>		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucFunctionNameDef
<b>Range</b>	String		
<b>Default value</b>	Fee_JobErrorNotification		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		



## 1 Fls\_17\_Dmu driver

### 1.3.1.3.8 FlsMaxReadFastMode

**Table 21 Specification for FlsMaxReadFastMode**

<b>Name</b>	FlsMaxReadFastMode		
<b>Description</b>	<p>The maximum number of bytes to read in one cycle of the job processing of the Flash driver in fast mode. This configuration of this parameter will affect Compare and Blank check operation as well.</p> <p>The value configured for FlsMaxReadFastMode should be more than the value configured for FlsMaxReadNormalMode. Therefore, the default value has been set assuming a word aligned read address from data flash(DFLASH0) and more than the value of FlsMaxReadNormalMode.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	1 - FlsTotalSize		
<b>Default value</b>	64		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsTotalSize		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.3.9 FlsMaxReadNormalMode

**Table 22 Specification for FlsMaxReadNormalMode**

<b>Name</b>	FlsMaxReadNormalMode		
<b>Description</b>	<p>The maximum number of bytes to read in one cycle of the job processing of the Flash driver in normal mode. This configuration of this parameter will affect Compare and Blank check operation as well.</p> <p>The default value has been given assuming the read address from DFLASH0 is word aligned and is less than the value of FlsMaxReadFastMode.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	1 - FlsTotalSize		
<b>Default value</b>	32		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsTotalSize		

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**Table 22 Specification for FlsMaxReadNormalMode (continued)**

<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.
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### 1.3.1.3.10 FlsMaxWriteFastMode

**Table 23 Specification for FlsMaxWriteFastMode**

<b>Name</b>	FlsMaxWriteFastMode		
<b>Description</b>	<p>The maximum number of bytes to write in one cycle of the job processing of the Flash driver. In Aurix the write can be either page write (1 page = 8 bytes) or burst write (4 pages = 32 bytes).</p> <p>This parameter is not supported as the burst mode for write is used by default and if the length of data to be written is less than or equal to 24 bytes (that is less than or equal to 4 pages) then page write is used for these remaining bytes.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	32 - 32		
<b>Default value</b>	32		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.3.11 FlsMaxWriteNormalMode

**Table 24 Specification for FlsMaxWriteNormalMode**

<b>Name</b>	FlsMaxWriteNormalMode		
<b>Description</b>	<p>The maximum number of bytes to write in one cycle of the job processing of the Flash driver. In Aurix the write can be either page write (1 page = 8 bytes) or burst write (4 pages = 32 bytes).</p> <p>This parameter is not supported as the burst mode for write is used by default and if the length of data to be written is less than or equal to 24 bytes (that is less than or equal to 4 pages) then page write is used for these remaining bytes.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	32 - 32		
<b>Default value</b>	32		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-

**1 Fls\_17\_Dmu driver**
**Table 24 Specification for FlsMaxWriteNormalMode (continued)**

<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.3.12 FlsProgVerifyErrNotif**
**Table 25 Specification for FlsProgVerifyErrNotif**

<b>Name</b>	FlsProgVerifyErrNotif		
<b>Description</b>	<p>User defined notification function pointer of type void fn_name (void).</p> <p>This notification function is called by the FLS Driver for giving notification of the PVER error during write/programming job.</p> <p>If the FlsProgVerifyErrNotif is configured as NULL then the notification function is not called.</p> <p>This parameter is valid only if IFX FEE is used and should be configured as Fee_17_JobProgErrorNotification. The Fee_17_JobProgErrorNotification() is the Infineon FEE programming error notification and therefore has been given as the default value. If Infineon FEE is not used, then this parameter is not supported.</p> <p>The post build variant value is false for this parameter since the default value is non-editable.</p>		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucFunctionNameDef
<b>Range</b>	String		
<b>Default value</b>	Fee_17_JobProgErrorNotification		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	FALSE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsIfxFeeUse		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.3.13 FlsProtection**
**Table 26 Specification for FlsProtection**

<b>Name</b>	FlsProtection
<b>Description</b>	<p>This parameter is not supported as the protection is best handled by the FlsLoader.</p> <p>This parameter is unused and hence disabled.</p>

**1 Fls\_17\_Dmu driver**
**Table 26 Specification for FlsProtection (continued)**

<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.3.14 FlsWaitStateErrorCorrection**
**Table 27 Specification for FlsWaitStateErrorCorrection**

<b>Name</b>	FlsWaitStateErrorCorrection		
<b>Description</b>	<p>Defines wait state configuration for error correction.</p> <p>Minimum value for the ECC cycles : Ceiling(<math>t_{DFECC} * f_{FSI}</math>)</p> <p>The wait cycles to be programmed in the DMU_HF_DWAIT register is ECC cycles - 1.</p> <p>For example, if the <math>t_{DFECC} = 20</math> ns, with <math>f_{FSI} = 100</math> MHz. The number of error correction cycles equals 2 therefore program values are:</p> <p>DMU_HF_DWAIT.RECC = 1</p> <p>So for error correction cycles of 2, the value to be entered here is 1.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	FLS_17_DMU_WAITSTATE_ERRCOREC_0 - FLS_17_DMU_WAITSTATE_ERRCOREC_7		
<b>Default value</b>	FLS_17_DMU_WAITSTATE_ERRCOREC_1		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1 Fls\_17\_Dmu driver**
**1.3.1.3.15 FlsWaitStateRead**
**Table 28 Specification for FlsWaitStateRead**

<b>Name</b>	FlsWaitStateRead		
<b>Description</b>	<p>Defines wait state configuration for read access.</p> <p>Minimum value for the DFlash0 read cycles : Ceiling (<math>t_{DF} * f_{FSI}</math>)</p> <p>The wait cycles to be programmed in the DMU_HF_DWAIT register is DFLASH read cycles - 1.</p> <p>For example, if the <math>t_{DF} = 100</math> ns and <math>f_{FSI} = 100</math> MHz. The number of DFlash read cycles equals 10, therefore program values are:</p> <p>DMU_HF_DWAIT.RFLASH = 9</p> <p>So for read cycles of 10, the value to be entered here is 9.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	FLS_17_DMU_WAITSTATE_READ_0 - FLS_17_DMU_WAITSTATE_READ_255		
<b>Default value</b>	FLS_17_DMU_WAITSTATE_READ_9		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.4 Container: FlsDemEventParameterRefs**

Container for the references to DemEventParameter elements which shall be invoked using the Dem\_ReportErrorStatus API in case the corresponding error occurs. The EventId is taken from the referenced DemEventParameter's DemEventId value. The standardized errors are provided in the container and can be extended by vendor specific error references.

*Note: This container is not applicable and made non-editable. This configuration container is not used in the code but it is listed for AUTOSAR compatibility.*

Post-Build Variant Multiplicity: TRUE

Multiplicity Configuration Class: Post-Build

**1.3.1.4.1 FLS\_E\_COMPARE\_FAILED**
**Table 29 Specification for FLS\_E\_COMPARE\_FAILED**

<b>Name</b>	FLS_E_COMPARE_FAILED
<b>Description</b>	<p>Reference to the DemEventParameter which shall be issued when the error "Flash compare failed (HW)" has occurred.</p> <p><i>Note: This parameter is not applicable and made non-editable. This configuration parameter is not used in the code but it is listed for AUTOSAR compatibility.</i></p>

**1 Fls\_17\_Dmu driver**
**Table 29 Specification for FLS\_E\_COMPARE\_FAILED (continued)**

<b>Multiplicity</b>	0..1	<b>Type</b>	EcucSymbolicNameReferenceDef
<b>Range</b>	Reference to Node: DemEventParameter		
<b>Default value</b>	NULL		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.2.2.		

**1.3.1.4.2 FLS\_E\_ERASE\_FAILED**
**Table 30 Specification for FLS\_E\_ERASE\_FAILED**

<b>Name</b>	FLS_E_ERASE_FAILED		
<b>Description</b>	Reference to the DemEventParameter which shall be issued when the error "Flash erase failed (HW)" has occurred.  <i>Note: This parameter is not applicable and made non-editable. This configuration parameter is not used in the code but it is listed for AUTOSAR compatibility.</i>		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucSymbolicNameReferenceDef
<b>Range</b>	Reference to Node: DemEventParameter		
<b>Default value</b>	NULL		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.2.2.		

**1.3.1.4.3 FLS\_E\_READ\_FAILED**
**Table 31 Specification for FLS\_E\_READ\_FAILED**

<b>Name</b>	FLS_E_READ_FAILED
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**1 Fls\_17\_Dmu driver**
**Table 31 Specification for FLS\_E\_READ\_FAILED (continued)**

<b>Description</b>	Reference to the DemEventParameter which shall be issued when the error "Flash read failed (HW)" has occurred.  <i>Note: This parameter is not applicable and made non-editable. This configuration parameter is not used in the code but it is listed for AUTOSAR compatibility.</i>		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucSymbolicNameReferenceDef
<b>Range</b>	Reference to Node: DemEventParameter		
<b>Default value</b>	NULL		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.2.2.		

**1.3.1.4.4 FLS\_E\_UNEXPECTED\_FLASH\_ID**
**Table 32 Specification for FLS\_E\_UNEXPECTED\_FLASH\_ID**

<b>Name</b>	FLS_E_UNEXPECTED_FLASH_ID		
<b>Description</b>	Reference to the DemEventParameter which shall be issued when the error "Expected hardware ID not matched" has occurred.  <i>Note: This parameter is not applicable and made non-editable. This configuration parameter is not used in the code but it is listed for AUTOSAR compatibility.</i>		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucSymbolicNameReferenceDef
<b>Range</b>	Reference to Node: DemEventParameter		
<b>Default value</b>	NULL		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.2.2.		

**1 Fls\_17\_Dmu driver**
**1.3.1.4.5 FLS\_E\_WRITE\_FAILED**
**Table 33 Specification for FLS\_E\_WRITE\_FAILED**

<b>Name</b>	FLS_E_WRITE_FAILED		
<b>Description</b>	Reference to the DemEventParameter which shall be issued when the error "Flash write failed (HW)" has occurred.  <i>Note: This container is not applicable and made non-editable. This configuration container is not used in the code but it is listed for AUTOSAR compatibility.</i>		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucSymbolicNameReferenceDef
<b>Range</b>	Reference to Node:		
<b>Default value</b>	NULL		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.2.2.		

**1.3.1.5 Container: FlsExternalDriver**

This container is present for external Flash drivers only. Internal Flash drivers do not use the parameter listed in this container, hence its multiplicity is 0 for internal drivers.

This container is not supported since FLS is an internal flash driver.

Post-Build Variant Multiplicity: -

Multiplicity Configuration Class: -

**1.3.1.5.1 FlsSpiReference**
**Table 34 Specification for FlsSpiReference**

<b>Name</b>	FlsSpiReference		
<b>Description</b>	Reference to SPI sequence (required for external Flash drivers).  This is not supported as external drivers are not supported.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucSymbolicNameReferenceDef
<b>Range</b>	Reference to Node: SpiSequence		
<b>Default value</b>	NULL		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	FALSE



**1 Fls\_17\_Dmu driver**
**Table 34 Specification for FlsSpiReference (continued)**

<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	Pre-Compile
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6 Container: FlsGeneral**

This container holds the for general parameters of the FLS driver. These parameters are always pre-compile.

Post-Build Variant Multiplicity: -

Multiplicity Configuration Class: -

**1.3.1.6.1 FlsAcLoadOnJobStart**
**Table 35 Specification for FlsAcLoadOnJobStart**

<b>Name</b>	FlsAcLoadOnJobStart		
<b>Description</b>	<p>If this parameter is enabled, then the erase access code is loaded in the RAM during Fls_17_Dmu_Erase() API call and unloaded after the completion or cancellation of the job.</p> <p>Similarly, the write access code is loaded in the RAM during the Fls_17_Dmu_Write() API call and unloaded after the completion or cancellation of the job.</p> <p>If this parameter is disabled, then the write and erase access code of the FLS driver are executed from the program flash.</p> <p>This parameter shall be non-editable. The FLS driver access code executes from program flash(PFlash). In TC3xx, Pflash and Dflash can be read in parallel and hence there is no need to load Dflash access code into RAM.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

## 1 Fls\_17\_Dmu driver

### 1.3.1.6.2 FlsBaseAddress

**Table 36 Specification for FlsBaseAddress**

<b>Name</b>	FlsBaseAddress		
<b>Description</b>	<p>The Flash memory start address (also see SWS_Fls_00208 and SWS_Fls_00209).</p> <p>This parameter defines the lower boundary for the read / write / erase/compare and blank check jobs.</p> <p>This parameter is fixed and not editable.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	Based on the target device -		
<b>Default value</b>	0xAF000000		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.6.3 FlsBlankCheckApi

**Table 37 Specification for FlsBlankCheckApi**

<b>Name</b>	FlsBlankCheckApi		
<b>Description</b>	<p>This parameter is used to enable/disable the Fls_17_Dmu_BlankCheck() API.</p> <p>The default value is set as FALSE for the optional features to minimize the executable code size.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1 Fls\_17\_Dmu driver**
**1.3.1.6.4 FlsCancelApi**
**Table 38 Specification for FlsCancelApi**

<b>Name</b>	FlsCancelApi		
<b>Description</b>	This parameter is used to enable/disable the Fls_17_Dmu_Cancel() API. The default value is set as FALSE for the optional features to minimize the executable code size.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.5 FlsCompareApi**
**Table 39 Specification for FlsCompareApi**

<b>Name</b>	FlsCompareApi		
<b>Description</b>	This parameter is used to enable/disable the Fls_17_Dmu_Compare() API. The default value is set as FALSE for the optional features to minimize the executable code size.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		

## 1 Fls\_17\_Dmu driver

**Table 39 Specification for FlsCompareApi (continued)**

<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.
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### 1.3.1.6.6 FlsDevErrorDetect

**Table 40 Specification for FlsDevErrorDetect**

<b>Name</b>	FlsDevErrorDetect		
<b>Description</b>	Parameter enables or disables the Default Error Tracer (DET) detection and reporting. The default value of this parameter is set to FALSE to minimize the executable code size.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.6.7 FlsDriverIndex

**Table 41 Specification for FlsDriverIndex**

<b>Name</b>	FlsDriverIndex		
<b>Description</b>	This parameter is used to assign an index to the FLS driver. The default value is set to minimum.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 254		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	ECU
<b>Dependency</b>	-		

**1 Fls\_17\_Dmu driver**
**Table 41 Specification for FlsDriverIndex (continued)**

<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.
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**1.3.1.6.8 FlsEccErrorInfoApi**
**Table 42 Specification for FlsEccErrorInfoApi**

<b>Name</b>	FlsEccErrorInfoApi		
<b>Description</b>	<p>This parameter is used to enable or disable the service/API to get the page address of the most recent ECC error that occurred.</p> <p>TRUE: Service to get ECC error information is available.  FALSE: Service to get ECC error information is not available.</p> <p>The default value is set as FALSE for the optional features to minimize the executable code size.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	None	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsIfxFeeUse		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.9 FlsEcucPartitionRef**
**Table 43 Specification for FlsEcucPartitionRef**

<b>Name</b>	FlsEcucPartitionRef		
<b>Description</b>	<p>Parameter maps the Flash driver to zero or one ECUC partition to make the driver API available in this partition.</p> <p><i>Note: Parameter support is added only for AUTOSAR schema compliance. This parameter is not used in code generation logic, hence this parameter is made editable false.</i></p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucReferenceDef
<b>Range</b>	Reference to Node: EcucPartition		
<b>Default value</b>	NULL		

**1 Fls\_17\_Dmu driver**
**Table 43 Specification for FlsEcucPartitionRef (continued)**

<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	TRUE
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	Pre-Compile
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	ECU
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.4.0.		

**1.3.1.6.10 FlsEraseVerificationEnabled**
**Table 44 Specification for FlsEraseVerificationEnabled**

<b>Name</b>	FlsEraseVerificationEnabled		
<b>Description</b>	Compile switch to enable erase verification TRUE: memory region is checked to be erased FALSE: memory region is not checked to be erased		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.4.0.		

**1.3.1.6.11 FlsGetJobResultApi**
**Table 45 Specification for FlsGetJobResultApi**

<b>Name</b>	FlsGetJobResultApi		
<b>Description</b>	This parameter is used to enable/disable the Fls_17_Dmu_GetJobResult() API. The default value is set as FALSE for the optional features to minimize the executable code size.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef

**1 Fls\_17\_Dmu driver**
**Table 45 Specification for FlsGetJobResultApi (continued)**

<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.12 FlsGetStatusApi**
**Table 46 Specification for FlsGetStatusApi**

<b>Name</b>	FlsGetStatusApi		
<b>Description</b>	This parameter is used to enable/disable the Fls_17_Dmu_GetStatus() API. The default value is set as FALSE for the optional features to minimize the executable code size.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.13 FlsIfxFeeUse**
**Table 47 Specification for FlsIfxFeeUse**

<b>Name</b>	FlsIfxFeeUse
<b>Description</b>	This parameter is used to enable/disable the use of Infineon FEE specific APIs.

**1 Fls\_17\_Dmu driver**
**Table 47 Specification for FlsIfxFeeUse (continued)**

	The default value is set TRUE assuming that FLS driver is used with Infineon FEE.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	TRUE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.14 FlsInitApiMode**
**Table 48 Specification for FlsInitApiMode**

<b>Name</b>	FlsInitApiMode		
<b>Description</b>	<p>This parameter is used for configuring the 'User' or 'Supervisor' mode for initialization in the FLS driver.</p> <p>By default access level of all the APIs is set to supervisor so that, there is no dependency on the OS functions to write into the access protected SFRs.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucEnumerationParamDef
<b>Range</b>	FLS_17_DMU_MCAL_SUPERVISOR: mode used is SUPERVISOR FLS_17_DMU_MCAL_USER1: operating mode used is USER1		
<b>Default value</b>	FLS_17_DMU_MCAL_SUPERVISOR		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsRuntimeApiMode		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		



**1 Fls\_17\_Dmu driver**
**1.3.1.6.15 FlsInitCheckApi**
**Table 49 Specification for FlsInitCheckApi**

<b>Name</b>	FlsInitCheckApi		
<b>Description</b>	Switch to enable the safety check for initialization using Fls_17_Dmu_InitCheck() API. The default value is set to FALSE for the optional features to minimize the executable code size.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.16 FlsRunTimeErrorDetect**
**Table 50 Specification for FlsRunTimeErrorDetect**

<b>Name</b>	FlsRunTimeErrorDetect		
<b>Description</b>	The activation of the runtime errors is configurable (ON / OFF) at the pre-compile time. FlsRunTimeErrorDetect should also be configured to true if FlsSafetyEnable is enabled. The default value is set as TRUE to ensure that the error detection is enabled and relevant issues are handled during product life cycle.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	TRUE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL

## 1 Fls\_17\_Dmu driver

**Table 50 Specification for FlsRunTimeErrorDetect (continued)**

<b>Dependency</b>	-
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

### 1.3.1.6.17 FlsRuntimeApiMode

**Table 51 Specification for FlsRuntimeApiMode**

<b>Name</b>	FlsRuntimeApiMode		
<b>Description</b>	<p>This configuration parameter gives the mode in which the runtime API is used.</p> <p>By default access level of all the APIs is set to supervisor so that, there is no dependency on the OS functions to write into the access protected SFRs.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucEnumerationParamDef
<b>Range</b>	<p>FLS_17_DMU_MCAL_SUPERVISOR: The mode used is SUPERVISOR</p> <p>FLS_17_DMU_MCAL_USER1: operating mode used is USER1</p>		
<b>Default value</b>	FLS_17_DMU_MCAL_SUPERVISOR		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.6.18 FlsSafetyEnable

**Table 52 Specification for FlsSafetyEnable**

<b>Name</b>	FlsSafetyEnable		
<b>Description</b>	<p>This parameter is used to enable/disable the safety notifications for the FLS module.</p> <p>The default value is set to TRUE to ensure that the safety issues are addressed.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	<p>TRUE</p> <p>FALSE</p>		
<b>Default value</b>	TRUE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-

**1 Fls\_17\_Dmu driver**
**Table 52 Specification for FlsSafetyEnable (continued)**

<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.19 FlsSetModeApi**
**Table 53 Specification for FlsSetModeApi**

<b>Name</b>	FlsSetModeApi		
<b>Description</b>	This parameter is used to enable/disable the Fls_17_Dmu_SetMode() API. The default value is set to FALSE for the optional feature to minimize the executable code size.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.20 FlsTimeoutSupervisionEnabled**
**Table 54 Specification for FlsTimeoutSupervisionEnabled**

<b>Name</b>	FlsTimeoutSupervisionEnabled		
<b>Description</b>	Compile switch to enable/disable timeout supervision TRUE: timeout supervision for erase and write jobs is enabled FALSE: timeout supervision for erase and write jobs is disabled		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE		

**1 Fls\_17\_Dmu driver**
**Table 54 Specification for FlsTimeoutSupervisionEnabled (continued)**

	FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.4.0.		

**1.3.1.6.21 FlsTotalSize**
**Table 55 Specification for FlsTotalSize**

<b>Name</b>	FlsTotalSize		
<b>Description</b>	<p>This parameter is used to calculate the upper boundary for the read/write/erase/compare and blank check jobs.</p> <p>Entire DFlash 0 area is used only by FEE because the unused area (other than the area used for EEPROM emulation) in DFLASH0 data flash becomes unusable for any other purpose as it would incur too many disturbs from the cycled EEPROM area.</p> <p>If the FEE operates in the double sector mode only, then the minimum size should be 8kb, because the logical sector size of the DFLASH0 data flash is 4kb and FEE needs minimum 2 sectors (double sector algorithm), the minimum value that can be configured for this configuration parameter is limited to 8kb.</p> <p>If the FEE operates in quasi only mode then the minimum size would be 4kb as per the minimum logical block size of the DFLASH0 data flash.</p> <p>If the FEE operates in both double sector and quasi state, the minimum value to be used for quasi would be 4kb and the remaining would be divided into 2 sectors of equal size.</p> <p>Similarly, if the minimum size for double sector(8kb) is used, when both double sector and quasi has to operate, then the remaining area could be used for quasi, in multiples of 4kb.</p> <p>While configuring this parameter, user has to take care of the total DFLASH0 size available on a variant.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	4096 - 1048576		
<b>Default value</b>	Based on Target Device		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL

## 1 Fls\_17\_Dmu driver

**Table 55** Specification for FlsTotalSize (continued)

<b>Dependency</b>	-
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

### 1.3.1.6.22 FlsUseInterrupts

**Table 56** Specification for FlsUseInterrupts

<b>Name</b>	FlsUseInterrupts		
<b>Description</b>	<p>Job processing triggered by hardware interrupt.</p> <p>True: Job processing triggered by interrupt (hardware controlled)</p> <p>False: Job processing not triggered by interrupt (software controlled)</p> <p>This parameter is non-editable and set to false when Infineon FEE is used.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	<p>TRUE</p> <p>FALSE</p>		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.6.23 FlsVersionInfoApi

**Table 57** Specification for FlsVersionInfoApi

<b>Name</b>	FlsVersionInfoApi		
<b>Description</b>	This parameter is used to enable/disable the Fls_17_Dmu_GetVersionInfo() API.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	<p>TRUE</p> <p>FALSE</p>		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-

**1 Fls\_17\_Dmu driver**
**Table 57 Specification for FlsVersionInfoApi (continued)**

<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.6.24 FlsWriteVerificationEnabled**
**Table 58 Specification for FlsWriteVerificationEnabled**

<b>Name</b>	FlsWriteVerificationEnabled		
<b>Description</b>	Compile switch to enable/disable write verification. TRUE: written data is compared directly after write FALSE: written data is not compared directly after write		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar version 4.4.0.		

**1.3.1.7 Container: FlsIfxSpecificConfig**

This container lists all the Infineon specific pre-compile configuration parameters

Post-Build Variant Multiplicity: -

Multiplicity Configuration Class: -

**1.3.1.7.1 FlsEraseSuspendTimeout**
**Table 59 Specification for FlsEraseSuspendTimeout**

<b>Name</b>	FlsEraseSuspendTimeout
<b>Description</b>	Timeout parameter for the erase suspend feature (number of loops).

**1 Fls\_17\_Dmu driver**
**Table 59 Specification for FlsEraseSuspendTimeout (continued)**

<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	12000 - 65535		
<b>Default value</b>	12000		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsUseEraseSuspend		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.7.2 FlsIllegalStateNotification**
**Table 60 Specification for FlsIllegalStateNotification**

<b>Name</b>	FlsIllegalStateNotification		
<b>Description</b>	This parameter is a pointer to a notification function, which is called when the FLS driver reaches an illegal state. The illegal state here signifies that the FLS driver is not able to proceed. No more FLS request is triggered. In such a case, system reset is recommended.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucFunctionNameDef
<b>Range</b>	String		
<b>Default value</b>	NULL_PTR		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.7.3 FlsStateVarStruct**
**Table 61 Specification for FlsStateVarStruct**

<b>Name</b>	FlsStateVarStruct		
<b>Description</b>	This parameter is used to provide the name of the structure containing the entire global variables specific to the Flash driver.		

**1 Fls\_17\_Dmu driver**
**Table 61 Specification for FlsStateVarStruct (continued)**

<b>Multiplicity</b>	1..1	<b>Type</b>	EcucStringParamDef
<b>Range</b>	String		
<b>Default value</b>	FlsStateVar		
<b>Post-build variant value</b>	TRUE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Post-Build	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.7.4 FlsUseEraseSuspend**
**Table 62 Specification for FlsUseEraseSuspend**

<b>Name</b>	FlsUseEraseSuspend		
<b>Description</b>	Compile switch to enable or disable the FLS erase suspend and erase resume features. STD_ON : FLS suspend/resume feature for erase is enabled STD_OFF: FLS suspend/resume feature for erase is disabled		
<b>Multiplicity</b>	0..1	<b>Type</b>	EcucBooleanParamDef
<b>Range</b>	TRUE FALSE		
<b>Default value</b>	FALSE		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	Post-Build
<b>Origin</b>	IFX	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8 Container: FlsPublishedInformation**

Additional published parameters not covered by CommonPublishedInformation container.

Note that these parameters do not have any configuration class setting, because they are published information.

Post-Build Variant Multiplicity: -



## 1 Fls\_17\_Dmu driver

Multiplicity Configuration Class: -

### 1.3.1.8.1 FlsAcLocationErase

**Table 63 Specification for FlsAcLocationErase**

<b>Name</b>	FlsAcLocationErase		
<b>Description</b>	<p>Position in RAM, to which the erase flash access code has to be loaded.</p> <p>Only relevant if the erase flash access code is not position independent. If this information is not provided it is assumed that the erase flash access code is position independent and that therefore the RAM position can be freely configured.</p> <p>This parameter is not applicable as the flash driver access code executes from program flash. Therefore, this parameter is not supported.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

### 1.3.1.8.2 FlsAcLocationWrite

**Table 64 Specification for FlsAcLocationWrite**

<b>Name</b>	FlsAcLocationWrite		
<b>Description</b>	<p>Position in RAM, to which the write flash access code has to be loaded.</p> <p>Only relevant if the write flash access code is not position independent. If this information is not provided it is assumed that the write flash access code is position independent and that therefore the RAM position can be freely configured.</p> <p>This parameter is not relevant as flash driver access code executes from program flash. Therefore, this parameter is not supported.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-

**1 Fls\_17\_Dmu driver**
**Table 64 Specification for FlsAcLocationWrite (continued)**

<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8.3 FlsAcSizeErase**
**Table 65 Specification for FlsAcSizeErase**

<b>Name</b>	FlsAcSizeErase		
<b>Description</b>	Number of bytes in the RAM needed for the erase Flash access code.  This parameter is not relevant as the flash driver access code executes from program flash. Therefore, this is not supported.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8.4 FlsAcSizeWrite**
**Table 66 Specification for FlsAcSizeWrite**

<b>Name</b>	FlsAcSizeWrite		
<b>Description</b>	Number of bytes in the RAM needed for the write Flash access code.  This parameter is not relevant as the Flash access code executes from program flash. Therefore, this is not supported.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-

**1 Fls\_17\_Dmu driver**
**Table 66 Specification for FlsAcSizeWrite (continued)**

<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8.5 FlsEraseTime**
**Table 67 Specification for FlsEraseTime**

<b>Name</b>	FlsEraseTime		
<b>Description</b>	Maximum time to erase one logical sector in microseconds. The default value is given as per datasheet considering simultaneous access of DF0 and DF1 from TriCore and HSM respectively. Tolerance of 10% is considered additionally on the actual erase time required for one sector (1.5 seconds).		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucFloatParamDef
<b>Range</b>	0.0 - 4294967295.0		
<b>Default value</b>	1897500		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8.6 FlsErasedValue**
**Table 68 Specification for FlsErasedValue**

<b>Name</b>	FlsErasedValue		
<b>Description</b>	The contents of an erased Flash memory cell. The default value is selected as 0 as this is the value on DFLASH0 after erase.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-

**1 Fls\_17\_Dmu driver**
**Table 68 Specification for FlsErasedValue (continued)**

<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8.7 FlsExpectedHwId**
**Table 69 Specification for FlsExpectedHwId**

<b>Name</b>	FlsExpectedHwId		
<b>Description</b>	Unique identifier of the hardware device that is expected by the driver (the device for which the driver has been implemented).  This parameter is not used as it is applicable only for external flash drivers.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucStringParamDef
<b>Range</b>	String		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8.8 FlsSpecifiedEraseCycles**
**Table 70 Specification for FlsSpecifiedEraseCycles**

<b>Name</b>	FlsSpecifiedEraseCycles		
<b>Description</b>	Number of erase cycles specified for the Flash device (usually given in the device data sheet). The default value is selected based on the datasheet.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 4294967295		
<b>Default value</b>	125000		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-

**1 Fls\_17\_Dmu driver**
**Table 70 Specification for FlsSpecifiedEraseCycles (continued)**

<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.8.9 FlsWriteTime**
**Table 71 Specification for FlsWriteTime**

<b>Name</b>	FlsWriteTime		
<b>Description</b>	Maximum time for one write operation, in microseconds, that is, burst write (32 bytes). The default value has been given based on the target parameter of the hardware DFLASH0 for burst write considering simultaneous access of DF0 and DF1. Tolerance of 10% is considered additionally on the actual write time required for one burst operation (140 microseconds).		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucFloatParamDef
<b>Range</b>	0.0 - 4294967295.0		
<b>Default value</b>	5154		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Published-Information	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.9 Container: FlsSector**

This container contains configuration description of a flashable sector.

Post-Build Variant Multiplicity: FALSE

Multiplicity Configuration Class: -

**1.3.1.9.1 FlsNumberOfSectors**
**Table 72 Specification for FlsNumberOfSectors**

<b>Name</b>	FlsNumberOfSectors
<b>Description</b>	Number of continuous sectors with identical values for FlsSectorSize and FlsPageSize(in bytes). The FlsSectorStartAddress parameter denotes the start address of the first sector.

**1 Fls\_17\_Dmu driver**
**Table 72 Specification for FlsNumberOfSectors (continued)**

	The maximum and the default value for this parameter is '2' as it is used with the double sector algorithm.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	1 - 2		
<b>Default value</b>	2		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.9.2 FlsPageSize**
**Table 73 Specification for FlsPageSize**

<b>Name</b>	FlsPageSize		
<b>Description</b>	Size of one FLS age in bytes. This parameter is fixed, therefore, not configurable.		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	8 - 8		
<b>Default value</b>	8		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	-		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.9.3 FlsSectorSize**
**Table 74 Specification for FlsSectorSize**

<b>Name</b>	FlsSectorSize
<b>Description</b>	Size of the FLS Sector (in bytes).

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**Table 74 Specification for FlsSectorSize (continued)**

	<p>For double sector data, this parameter will of the size of one of the sectors. If no quasi-static data is used, then the value of this parameter will typically be half of FlsTotalSize and should be in the multiple of 4 Kbytes. For quasi-static data this contains the quasi region and should be in the multiple of 4K bytes.</p> <p>If both double sector and quasi-static data are used then two containers should be used to specify the sector size appropriately such that the total size is justified. For example, the minimum size for quasi would be 4 kb and the rest could be dedicated for using the double sector algorithm. For more details, refer to FlsNumberOfSectors.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	4096 - 1048576		
<b>Default value</b>	DFLASH0 total size divided by 2 (DFLASH0 total size varies)		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsTotalSize		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

**1.3.1.9.4 FlsSectorStartaddress**
**Table 75 Specification for FlsSectorStartaddress**

<b>Name</b>	FlsSectorStartaddress		
<b>Description</b>	<p>Start address offset of the DFlash0 sector from the configured Flash base address to access a certain Flash memory area.</p> <p>FLS base address is always added to this address to arrive at the correct address.</p>		
<b>Multiplicity</b>	1..1	<b>Type</b>	EcucIntegerParamDef
<b>Range</b>	0 - 1044480		
<b>Default value</b>	0		
<b>Post-build variant value</b>	FALSE	<b>Post-build variant multiplicity</b>	-
<b>Value configuration class</b>	Pre-Compile	<b>Multiplicity configuration class</b>	-
<b>Origin</b>	AUTOSAR_ECUC	<b>Scope</b>	LOCAL
<b>Dependency</b>	FlsTotalSize		
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.		

## 1 Fls\_17\_Dmu driver

### 1.3.1.10 Container: FlsSectorList

List of flashable sectors and pages.

Post-Build Variant Multiplicity: -

Multiplicity Configuration Class: -

## 1.3.2 Functions - Type definitions

### 1.3.2.1 Fls\_17\_Dmu\_AddressType

**Table 76** Specification for Fls\_17\_Dmu\_AddressType

<b>Syntax</b>	Fls_17_Dmu_AddressType	
<b>Type</b>	uint32	
<b>File</b>	Fls_17_Dmu.h	
<b>Range</b>	0 – 4294967295	Size depends on target platform and DFLASH0 data flash memory on the flash device.
<b>Description</b>	<p>Used as an address offset from the configured Flash base address to access a certain Flash memory area.</p> <p>The Fls_17_Dmu_AddressType type has the lower limit as 0 and the FLS base address is always added to it to arrive at the correct address.</p>	
<b>Source</b>	AUTOSAR	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.2.2 Fls\_17\_Dmu\_ConfigType

**Table 77** Specification for Fls\_17\_Dmu\_ConfigType

<b>Syntax</b>	Fls_17_Dmu_ConfigType	
<b>Type</b>	Structure	
<b>File</b>	Fls_17_Dmu.h	
<b>Range</b>	HW dependent structure	Structure to hold the Flash driver configuration set. The contents of the initialization data structure are specific to the Flash memory hardware.
<b>Description</b>	A pointer to such a structure is provided to the Flash driver initialization routine for configuration of the driver and Flash memory hardware.	
<b>Source</b>	AUTOSAR	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	



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### 1.3.2.3 Fls\_17\_Dmu\_HardenType

**Table 78 Specification for Fls\_17\_Dmu\_HardenType**

<b>Syntax</b>	Fls_17_Dmu_HardenType	
<b>Type</b>	uint8	
<b>File</b>	Fls_17_Dmu.h	
<b>Range</b>	0 - FLS_17_DMU_HARDENCHK_NOTREQD	Hardening not required
	2 - FLS_17_DMU_HARDENCHK_ERROR	Hardening failed due to some error.
	1 - FLS_17_DMU_HARDENCHK_REQRD	Hardening required
<b>Description</b>	Used to specify the hardening update (whether hardening is required or not or any failure occurred during the hardening check).	
<b>Source</b>	IFX	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.2.4 Fls\_17\_Dmu\_Job\_Type

**Table 79 Specification for Fls\_17\_Dmu\_Job\_Type**

<b>Syntax</b>	Fls_17_Dmu_Job_Type	
<b>Type</b>	uint8	
<b>File</b>	Fls_17_Dmu.h	
<b>Range</b>	0 - FLS_NO_JOB	No notification was called
	1 - FLS_WRITE_JOB	Notification for the write job
	2 - FLS_ERASE_JOB	Notification for the erase job
	3 - FLS_READ_JOB	Notification for the read job
	4 - FLS_COMPARE_JOB	Notification for the compare job
	6 - FLS_CANCEL_JOB	Notification for the canceled job
	9 - FLS_BLANKCHECK_JOB	Notification for the blank check
<b>Description</b>	Specifies the type of job for which the notification was called.	
<b>Source</b>	IFX	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.2.5 Fls\_17\_Dmu\_LengthType

**Table 80 Specification for Fls\_17\_Dmu\_LengthType**

<b>Syntax</b>	Fls_17_Dmu_LengthType
<b>Type</b>	uint32
<b>File</b>	Fls_17_Dmu.h

## 1 Fls\_17\_Dmu driver

**Table 80 Specification for Fls\_17\_Dmu\_LengthType (continued)**

<b>Range</b>	0 – 4294967295	Should be the same type as Fls_AddressType because of arithmetic operations. Size depends on the target platform and the DFLASH0 data flash memory on the device.
<b>Description</b>	Specifies the number of bytes to read/write/erase/compare.	
<b>Source</b>	AUTOSAR	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.2.6 Fls\_17\_Dmu\_NotifFunctionPtrType

**Table 81 Specification for Fls\_17\_Dmu\_NotifFunctionPtrType**

<b>Syntax</b>	Fls_17_Dmu_NotifFunctionPtrType
<b>Type</b>	Pointer to a function of type void Function_Name ( void )
<b>File</b>	Fls_17_Dmu.h
<b>Description</b>	Function pointer type for callback functions. Used for job end, job error and illegal functions.
<b>Source</b>	IFX
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

## 1.3.3 Functions - APIs

This section lists all the APIs of the FLS driver.

### 1.3.3.1 Fls\_17\_Dmu\_BlankCheck

**Table 82 Specification for Fls\_17\_Dmu\_BlankCheck API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_BlankCheck (     const Fls_17_Dmu_AddressType TargetAddress,     const Fls_17_Dmu_LengthType Length )</pre>	
<b>Service ID</b>	0x0A	
<b>Sync/Async</b>	Asynchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	TargetAddress Length	Address in the DFlash0 data flash memory from which the blank check should be started. Min.: 0 Max.: FLS_17_DMU_TOTAL_SIZE - 1

**1 Fls\_17\_Dmu driver**
**Table 82 Specification for Fls\_17\_Dmu\_BlankCheck API (continued)**

		Number of bytes to be checked for erase pattern. Min.: 1 Max.: FLS_17_DMU_TOTAL_SIZE - TargetAddress
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: request for blank checking has been accepted by the module E_NOT_OK: request for blank checking has not been accepted by the module
<b>Description</b>	The Fls_17_Dmu_BlankCheck should verify, whether a given memory area has been erased but not (yet) programmed. The function should limit the maximum number of checked Flash cells per main function cycle to the configured value FlsMaxReadNormalMode or FlsMaxReadFastMode, respectively.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_E_UNINIT, FLS_17_DMU_E_PARAM_LENGTH, FLS_17_DMU_E_PARAM_ADDRESS, FLS_17_DMU_E_BUSY	
<b>Configuration dependencies</b>	FlsBlankCheckApi	
<b>User hints</b>	-	
<b>SFR accessed</b>	-	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1.3.3.2 Fls\_17\_Dmu\_Cancel**
**Table 83 Specification for Fls\_17\_Dmu\_Cancel API**

<b>Syntax</b>	<pre>void Fls_17_Dmu_Cancel (     void )</pre>	
<b>Service ID</b>	0x03	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-

## 1 Fls\_17\_Dmu driver

**Table 83 Specification for Fls\_17\_Dmu\_Cancel API (continued)**

<b>Parameters (in - out)</b>	-	-
<b>Return</b>	void	void
<b>Description</b>	Cancels an ongoing job. <i>Note: Fls_17_Dmu_Cancel() shall not be invoked from interrupt context.</i>	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_E_UNINIT	
<b>Configuration dependencies</b>	FlsCancelApi	
<b>User hints</b>	-	
<b>SFR accessed</b>	-	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.3.3 Fls\_17\_Dmu\_CancelNonEraseJobs

**Table 84 Specification for Fls\_17\_Dmu\_CancelNonEraseJobs API**

<b>Syntax</b>	<pre>void Fls_17_Dmu_CancelNonEraseJobs (     void )</pre>	
<b>Service ID</b>	0x23	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	void	void
<b>Description</b>	<p>This is an Infineon specific API and not listed in the SWS.</p> <p>Service for canceling the ongoing flash jobs except the erase job. This function aborts the pending jobs (except the erase job), so that directly after returning from this function, a new job can be accepted by the driver.</p> <p>The function resets the internal job processing variables of the driver (such as address, length and data pointer) and sets the driver state to idle.</p>	

**1 Fls\_17\_Dmu driver**
**Table 84 Specification for Fls\_17\_Dmu\_CancelNonEraseJobs API (continued)**

	The routine sets the job result to MEMIF_JOB_CANCELED, if the job result currently has the following value: MEMIF_JOB_PENDING. Otherwise, it leaves the job result unchanged.
<b>Source</b>	IFX
<b>Error handling</b>	-
<b>Configuration dependencies</b>	FlsIfxFeeUse
<b>User hints</b>	-
<b>SFR accessed</b>	-
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

**1.3.3.4 Fls\_17\_Dmu\_Compare**
**Table 85 Specification for Fls\_17\_Dmu\_Compare API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_Compare (     const Fls_17_Dmu_AddressType SourceAddress,     const uint8 * const TargetAddressPtr,     const Fls_17_Dmu_LengthType Length )</pre>	
<b>Service ID</b>	0x08	
<b>Sync/Async</b>	Asynchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	SourceAddress TargetAddressPtr Length	Source address in the DFLASH0 data flash memory. This address offset is added to the data flash memory base address. Min.: 0 Max.: FLS_17_DMU_TOTAL_SIZE - 1 Pointer to the target data buffer Number of bytes to compare Min.: 1 Max.: FLS_17_DMU_TOTAL_SIZE - SourceAddress
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: compare command is accepted E_NOT_OK: compare command is not accepted

**1 Fls\_17\_Dmu driver**
**Table 85 Specification for Fls\_17\_Dmu\_Compare API (continued)**

<b>Description</b>	Compares the contents of an area of the DFLASH0 data flash memory with that of an application data buffer.
<b>Source</b>	AUTOSAR
<b>Error handling</b>	FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_E_BUSY, FLS_17_DMU_E_UNINIT, FLS_17_DMU_E_PARAM_DATA, FLS_17_DMU_E_PARAM_LENGTH, FLS_17_DMU_E_PARAM_ADDRESS
<b>Configuration dependencies</b>	FlsCompareApi
<b>User hints</b>	-
<b>SFR accessed</b>	-
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

**1.3.3.5 Fls\_17\_Dmu\_CompareWordsSync**
**Table 86 Specification for Fls\_17\_Dmu\_CompareWordsSync API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_CompareWordsSync (     const Fls_17_Dmu_AddressType SourceAddress,     const uint32 * const TargetAddressPtr,     const uint32 Length )</pre>	
<b>Service ID</b>	0x22	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	SourceAddress TargetAddressPtr Length	Source address in the DFLASH0 data flash memory. This address offset is added to the DFLASH0 data flash memory base address. Pointer to the target data buffer. Number of words to be compared. It takes the value from 1 to DFLASH0 total size.
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: compare is successful E_NOT_OK: compare is not successful
<b>Description</b>	<p>This is an IFX specific API and not listed in the SWS.</p> <p>It is a service for comparing the contents on the DFLASH0 data flash memory synchronously.</p> <p><i>Note: The range check is performed only when 'FlsSafetyEnable' is enabled.</i></p>	

**1 Fls\_17\_Dmu driver**
**Table 86 Specification for Fls\_17\_Dmu\_CompareWordsSync API (continued)**

<b>Source</b>	IFX
<b>Error handling</b>	FLS_17_DMU_SE_BUSY, FLS_17_DMU_SE_PARAM_DATA, FLS_17_DMU_SE_PARAM_ADDRESS, FLS_17_DMU_E_COMPARE_FAILED, FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_SE_PARAM_LENGTH
<b>Configuration dependencies</b>	FlsIfxFeeUse
<b>User hints</b>	-
<b>SFR accessed</b>	DMU_HF_ECCC(rw), DMU_HF_ECCS(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

**1.3.3.6 Fls\_17\_Dmu\_Erase**
**Table 87 Specification for Fls\_17\_Dmu\_Erase API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_Erase (     const Fls_17_Dmu_AddressType TargetAddress,     const Fls_17_Dmu_LengthType Length )</pre>	
<b>Service ID</b>	0x01	
<b>Sync/Async</b>	Asynchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	TargetAddress Length	Target address in the DFLASH0 data flash memory. This address offset is added to the DFLASH0 data flash memory base address. Min.: 0 Max.: FLS_17_DMU_TOTAL_SIZE - 1 Number of bytes to erase Min.: 1 Max.: FLS_17_DMU_TOTAL_SIZE - TargetAddress
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: erase command accepted E_NOT_OK: erase command not accepted
<b>Description</b>	This API is a service for erasing one or more complete Flash sectors.	

## 1 Fls\_17\_Dmu driver

**Table 87 Specification for Fls\_17\_Dmu\_Erase API (continued)**

<b>Source</b>	AUTOSAR
<b>Error handling</b>	FLS_17_DMU_E_ERASE_FAILED, FLS_17_DMU_SE_ILLGL_OPERTN, FLS_17_DMU_E_UNINIT, FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_E_BUSY, FLS_17_DMU_E_PARAM_LENGTH, FLS_17_DMU_E_PARAM_ADDRESS
<b>Configuration dependencies</b>	-
<b>User hints</b>	-
<b>SFR accessed</b>	DMU_HF_ERRSR(r), DMU_HF_OPERATION(r), DMU_HF_SUSPEND(r), STM_TIM0(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

### 1.3.3.7 Fls\_17\_Dmu\_GetEccErrorPageAddress

**Table 88 Specification for Fls\_17\_Dmu\_GetEccErrorPageAddress API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_GetEccErrorPageAddress (     uint32 * const PageAddressPtr )</pre>	
<b>Service ID</b>	0x2E	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	PageAddressPtr	Pointer to store the page address where the last ECC error occurred
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: The requested job has been executed. E_NOT_OK: The requested job has not been executed due to any of the following reasons: - Driver is busy - Passed pointer is null
<b>Description</b>	This is an IFX specific API and not listed in the SWS. Service to get the address of the flash page where last Multi-bit ECC error is detected. If there is no Multi-bit ECC error detected in the current power cycle, then the value of PageAddressPtr parameter will be 0xFFFFFFFF.	



## 1 Fls\_17\_Dmu driver

**Table 88 Specification for Fls\_17\_Dmu\_GetEccErrorPageAddress API (continued)**

	<i>Note: The PageAddress of the last occurred ECC error is not maintained across power cycles.</i>
<b>Source</b>	IFX
<b>Error handling</b>	FLS_17_DMU_SE_BUSY, FLS_17_DMU_SE_PARAM_DATA
<b>Configuration dependencies</b>	FlsIfxFeeUse, FlsEccErrorInfoApi
<b>User hints</b>	-
<b>SFR accessed</b>	-
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

### 1.3.3.8 Fls\_17\_Dmu\_GetJobResult

**Table 89 Specification for Fls\_17\_Dmu\_GetJobResult API**

<b>Syntax</b>	<pre>MemIf_JobResultType Fls_17_Dmu_GetJobResult (     void )</pre>	
<b>Service ID</b>	0x05	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	MemIf_JobResultType	The result of the last job
<b>Description</b>	Returns the result of the last job. <i>Note: When the Infineon FEE is present, for the Fls_17_Dmu_CompareWordsSync(), Fls_17_Dmu_ReadWordsSync(), Fls_17_Dmu_VerifyErase() and Fls_17_dmu_verifySectorErase() APIs, the job result is not updated. Therefore, the job result returned for the mentioned APIs are of the previous jobs.</i>	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_E_UNINIT	
<b>Configuration dependencies</b>	FlsGetJobResultApi	
<b>User hints</b>	-	
<b>SFR accessed</b>	-	

## 1 Fls\_17\_Dmu driver

**Table 89 Specification for Fls\_17\_Dmu\_GetJobResult API (continued)**

<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.
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### 1.3.3.9 Fls\_17\_Dmu\_GetNotifCaller

**Table 90 Specification for Fls\_17\_Dmu\_GetNotifCaller API**

<b>Syntax</b>	<pre>Fls_17_Dmu_Job_Type Fls_17_Dmu_GetNotifCaller (     void )</pre>	
<b>Service ID</b>	0x29	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Fls_17_Dmu_Job_Type	FLS job that raised the notification
<b>Description</b>	Returns the FLS job that raised the notification. It should be called only from the callback notification functions of the upper layers. This is an Infineon specific API and not listed in the SWS.	
<b>Source</b>	IFX	
<b>Error handling</b>	-	
<b>Configuration dependencies</b>	FlsIfxFeeUse	
<b>User hints</b>	-	
<b>SFR accessed</b>	-	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.3.10 Fls\_17\_Dmu\_GetOperStatus

**Table 91 Specification for Fls\_17\_Dmu\_GetOperStatus API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_GetOperStatus (     void )</pre>
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**1 Fls\_17\_Dmu driver**
**Table 91 Specification for Fls\_17\_Dmu\_GetOperStatus API (continued)**

<b>Service ID</b>	0x26	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: no OPER error E_NOT_OK: OPER error occurred
<b>Description</b>	This is an Infineon specific API and not listed in the SWS. Returns whether the OPER error had occurred or not.	
<b>Source</b>	IFX	
<b>Error handling</b>	-	
<b>Configuration dependencies</b>	-	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_ERRSR(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1.3.3.11 Fls\_17\_Dmu\_GetStatus**
**Table 92 Specification for Fls\_17\_Dmu\_GetStatus API**

<b>Syntax</b>	<pre>MemIf_StatusType Fls_17_Dmu_GetStatus (     void )</pre>	
<b>Service ID</b>	0x04	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Reentrant	
<b>Parameters (in)</b>	-	-

## 1 Fls\_17\_Dmu driver

**Table 92 Specification for Fls\_17\_Dmu\_GetStatus API (continued)**

<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	MemIf_StatusType	The state of the driver
<b>Description</b>	Returns the driver state.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	-	
<b>Configuration dependencies</b>	FlsGetStatusApi	
<b>User hints</b>	-	
<b>SFR accessed</b>	-	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.3.12 Fls\_17\_Dmu\_GetVersionInfo

**Table 93 Specification for Fls\_17\_Dmu\_GetVersionInfo API**

<b>Syntax</b>	<pre>void Fls_17_Dmu_GetVersionInfo (     Std_VersionInfoType * const VersionInfoPtr )</pre>	
<b>Service ID</b>	0x10	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	VersionInfoPtr	Pointer to where to store the version information of this module.
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	void	-
<b>Description</b>	Returns the version information of this module.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_E_PARAM_POINTER	
<b>Configuration dependencies</b>	FlsVersionInfoApi	

## 1 Fls\_17\_Dmu driver

**Table 93 Specification for Fls\_17\_Dmu\_GetVersionInfo API (continued)**

<b>User hints</b>	-
<b>SFR accessed</b>	-
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.

### 1.3.3.13 Fls\_17\_Dmu\_Init

**Table 94 Specification for Fls\_17\_Dmu\_Init API**

<b>Syntax</b>	<pre>void Fls_17_Dmu_Init (     const Fls_17_Dmu_ConfigType * const ConfigPtr )</pre>	
<b>Service ID</b>	0x00	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	ConfigPtr	Pointer to the FLS driver configuration set.
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	void	void
<b>Description</b>	Initializes the Flash driver.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_E_PARAM_CONFIG, FLS_17_DMU_E_BUSY, FLS_17_DMU_E_INIT_FAILED, FLS_17_DMU_SE_INIT_FAILED, FLS_17_DMU_SE_HW_BUSY	
<b>Configuration dependencies</b>	-	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_DWAIT(rw), DMU_HF_ECCC(rw), DMU_HF_ECCW(rw), DMU_HF_EER(rw), DMU_HF_ERRSR(r), DMU_HF_MARGIN(rw), DMU_HF_PROCONUSR(r), DMU_HF_SUSPEND(rw), FSI_COMM_1(w), FSI_COMM_2(w) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

## 1 Fls\_17\_Dmu driver

### 1.3.3.14 Fls\_17\_Dmu\_InitCheck

**Table 95 Specification for Fls\_17\_Dmu\_InitCheck API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_InitCheck (     const Fls_17_Dmu_ConfigType ConfigPtr )</pre>	
<b>Service ID</b>	0x2B	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	ConfigPtr	None
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: if initialization comparison is success E_NOT_OK: if initialization comparison fails
<b>Description</b>	This API checks the initialization values.	
<b>Source</b>	IFX	
<b>Error handling</b>	-	
<b>Configuration dependencies</b>	FlsInitCheckApi	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_DWAIT(r), DMU_HF_ECCC(r), DMU_HF_ECCS(r), DMU_HF_ECCW(r), DMU_HF_EER(r), DMU_HF_ERRSR(r), DMU_HF_MARGIN(r), DMU_HF_SUSPEND(r), FSI_COMM_1(r), FSI_COMM_2(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.3.15 Fls\_17\_Dmu\_IsHardeningRequired

**Table 96 Specification for Fls\_17\_Dmu\_IsHardeningRequired API**

<b>Syntax</b>	<pre>Fls_17_Dmu_HardenType Fls_17_Dmu_IsHardeningRequired (     const Fls_17_Dmu_AddressType TargetAddress,     const uint8 AlignChk )</pre>
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**1 Fls\_17\_Dmu driver**
**Table 96 Specification for Fls\_17\_Dmu\_IsHardeningRequired API (continued)**

<b>Service ID</b>	0x28	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	TargetAddress AlignChk	<p>Target address in Flash memory. This address offset is added to the Flash memory base address</p> <p>This parameter signifies whether the hardening is to be done at the page level or WL level. The following are the values which will be used for indication:</p> <ul style="list-style-type: none"> <li>- hardening is done at the page level if the value of this parameter is: FLS_17_DMU_PAGE_HARDEN(0x55)</li> <li>- hardening is done at the 'Word-line level' if the value of this parameter is: FLS_17_DMU_WORDLINE_HARDEN(0xAA)</li> </ul>
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Fls_17_Dmu_HardenType	<p>0 - FLS_17_DMU_HARDEN_NOTREQRD: Hardening is not required.</p> <p>1 - FLS_17_DMU_HARDEN_REQRD: Hardening is required.</p> <p>2 - FLS_17_DMU_HARDEN_ERROR: Hardening failed due to error.</p>
<b>Description</b>	<p>This is an Infineon specific API and not listed in the SWS.</p> <p>The function checks whether the contents of the DFLASH0 data flash memory at requested Page or WL address need hardening or not.</p>	
<b>Source</b>	IFX	
<b>Error handling</b>	FLS_17_DMU_E_HARDENCHK_FAIL, FLS_17_DMU_SE_PARAM_INVLD, FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_SE_PARAM_ADDRESS, FLS_17_DMU_SE_BUSY, FLS_17_DMU_SE_HW_TIMEOUT	
<b>Configuration dependencies</b>	FlsIfxFeeUse	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_CLRE(w), DMU_HF_CONTROL(rw), DMU_HF_ERRSR(r), FSI_COMM_1(rw), FSI_COMM_2(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1 Fls\_17\_Dmu driver**
**1.3.3.16 Fls\_17\_Dmu\_Read**
**Table 97 Specification for Fls\_17\_Dmu\_Read API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_Read (     const Fls_17_Dmu_AddressType SourceAddress,     uint8 * const TargetAddressPtr,     const Fls_17_Dmu_LengthType Length )</pre>	
<b>Service ID</b>	0x07	
<b>Sync/Async</b>	Asynchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	SourceAddress Length	Source address in the DFlash0 data flash memory. This address offset will be added to the DFlash0 data flash memory base address. Min.: 0 Max.: FLS_17_DMU_TOTAL_SIZE - 1 Number of bytes to read Min.: 1 Max.: FLS_17_DMU_TOTAL_SIZE - SourceAddress
<b>Parameters (out)</b>	TargetAddressPtr	Pointer to the target data buffer
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: read command has been accepted E_NOT_OK: read command has not been accepted
<b>Description</b>	Reads from flash memory.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_E_PARAM_ADDRESS, FLS_17_DMU_E_PARAM_DATA, FLS_17_DMU_E_UNINIT, FLS_17_DMU_E_BUSY, FLS_17_DMU_E_PARAM_LENGTH, FLS_17_DMU_SE_HW_BUSY	
<b>Configuration dependencies</b>	-	
<b>User hints</b>	-	
<b>SFR accessed</b>	-	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	



**1 Fls\_17\_Dmu driver**
**1.3.3.17 Fls\_17\_Dmu\_ReadWordsSync**
**Table 98 Specification for Fls\_17\_Dmu\_ReadWordsSync API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_ReadWordsSync (     const Fls_17_Dmu_AddressType SourceAddress,     uint32 * const TargetAddressPtr,     const uint32 Length )</pre>	
<b>Service ID</b>	0x21	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	SourceAddress Length	Source address in the DFLASH0 data flash memory. This address offset is added to the DFLASH0 base address.  Number of words to be read. It takes the value from 1 to DFLASH0 data flash size.
<b>Parameters (out)</b>	TargetAddressPtr	Pointer to target data buffer
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: read command is accepted E_NOT_OK: read command is not accepted
<b>Description</b>	This is an Infineon specific API and not listed in the SWS. It is a service to read synchronously from the DFLASH0 data flash memory. <i>Note: The range check is performed for the input parameters only when the 'FlsSafetyEnable' configuration parameter is enabled.</i>	
<b>Source</b>	IFX	
<b>Error handling</b>	FLS_17_DMU_E_READ_FAILED, FLS_17_DMU_SE_PARAM_LENGTH, FLS_17_DMU_SE_BUSY, FLS_17_DMU_SE_PARAM_DATA, FLS_17_DMU_SE_PARAM_ADDRESS, FLS_17_DMU_SE_HW_BUSY	
<b>Configuration dependencies</b>	FlsIfxFeeUse	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_ECCC(rw), DMU_HF_ECCS(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1 Fls\_17\_Dmu driver**
**1.3.3.18 Fls\_17\_Dmu\_ResumeErase**
**Table 99 Specification for Fls\_17\_Dmu\_ResumeErase API**

<b>Syntax</b>	Std_ReturnType Fls_17_Dmu_ResumeErase ( void )	
<b>Service ID</b>	0x2A	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: Erase resume command was accepted and passed or Erase was not suspended when this API was called E_NOT_OK: Erase resume command was not accepted or failed
<b>Description</b>	This is an IFX specific API and not listed in the SWS. It is a service for resuming a suspended erase of a sector.	
<b>Source</b>	IFX	
<b>Error handling</b>	FLS_17_DMU_E_RESUME_FAIL, FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_SE_BUSY, FLS_17_DMU_SE_HW_TIMEOUT	
<b>Configuration dependencies</b>	FlsUseEraseSuspend	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_CLRE(w), DMU_HF_ERRSR(r), DMU_HF_STATUS(r), DMU_HF_SUSPEND(r), STM_TIM0(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1.3.3.19 Fls\_17\_Dmu\_SetMode**
**Table 100 Specification for Fls\_17\_Dmu\_SetMode API**

<b>Syntax</b>	void Fls_17_Dmu_SetMode (
---------------	------------------------------

**1 Fls\_17\_Dmu driver**
**Table 100 Specification for Fls\_17\_Dmu\_SetMode API (continued)**

	<pre>const MemIf_ModeType Mode )</pre>	
<b>Service ID</b>	0x09	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non reentrant	
<b>Parameters (in)</b>	Mode	MEMIF_MODE_SLOW: slow read access MEMIF_MODE_FAST: fast read access
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	void	void
<b>Description</b>	Sets the flash operation mode of the driver.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_SE_PARAM_INVLD, FLS_17_DMU_E_BUSY, FLS_17_DMU_E_UNINIT	
<b>Configuration dependencies</b>	FlsSetModeApi	
<b>User hints</b>	-	
<b>SFR accessed</b>	-	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1.3.3.20 Fls\_17\_Dmu\_SuspendErase**
**Table 101 Specification for Fls\_17\_Dmu\_SuspendErase API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_SuspendErase (     void )</pre>	
<b>Service ID</b>	0x25	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-

**1 Fls\_17\_Dmu driver**
**Table 101 Specification for Fls\_17\_Dmu\_SuspendErase API (continued)**

<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: Erase suspend successful or erase is already suspended E_NOT_OK: erase suspend failed or this API is called when erase is not on-going
<b>Description</b>	This is an IFX specific API and not listed in the SWS. It is a service for suspending an ongoing erase of a sector.	
<b>Source</b>	IFX	
<b>Error handling</b>	FLS_17_DMU_SE_SUSPNDERASE_FAIL, FLS_17_DMU_SE_HW_TIMEOUT	
<b>Configuration dependencies</b>	FlsUseEraseSuspend	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_STATUS(r), DMU_HF_SUSPEND(rw) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1.3.3.21 Fls\_17\_Dmu\_VerifyErase**
**Table 102 Specification for Fls\_17\_Dmu\_VerifyErase API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_VerifyErase (     const Fls_17_Dmu_AddressType TargetAddress,     uint32 * const UnerasedWordlineAddressPtr,     uint8 * const UnerasedWordlineCountPtr )</pre>	
<b>Service ID</b>	0x24	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	TargetAddress	Target offset address in the DFLASH0 data flash memory. This address offset is added to the DFLASH0 data flash memory base address. The input value for TargetAddress can only be the start address of either of the sectors used by the Infineon FEE double sector algorithm.
<b>Parameters (out)</b>	UnerasedWordlineAddressPtr UnerasedWordlineCountPtr	Pointer to the first un-eraseable WL address. Pointer to the un-eraseable WL count.

**1 Fls\_17\_Dmu driver**
**Table 102 Specification for Fls\_17\_Dmu\_VerifyErase API (continued)**

<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: Erase verification command was accepted and passed E_NOT_OK: Erase verification command was not accepted or failed with more than two un-erasable WL
<b>Description</b>	This is an Infineon specific API and not listed in the SWS. It is a synchronous service to verify the erase operation performed on one of the two sectors(as per double sector algorithm used by Infineon FEE).	
<b>Source</b>	IFX	
<b>Error handling</b>	FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_SE_PARAM_DATA, FLS_17_DMU_SE_BUSY, FLS_17_DMU_SE_PARAM_ADDRESS	
<b>Configuration dependencies</b>	FlsIxFeeUse	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_ECCC(rw), DMU_HF_MARGIN(rw) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1.3.3.22 Fls\_17\_Dmu\_VerifySectorErase**
**Table 103 Specification for Fls\_17\_Dmu\_VerifySectorErase API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_VerifySectorErase (     const Fls_17_Dmu_AddressType TargetAddress,     uint32 * const UnerasedWordlineAddressPtr,     uint8 * const UnerasedWordlineCountPtr,     const uint8 Sector )</pre>	
<b>Service ID</b>	0x2C	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	TargetAddress Sector	Target offset address in the DFLASH0 data flash memory. This parameter is an address offset and is added to the DFLASH0 data flash memory base address.The TargetAddress can only be the value of the start address of either of the sectors of the Infineon FEE double sector algorithm.

**1 Fls\_17\_Dmu driver**
**Table 103 Specification for Fls\_17\_Dmu\_VerifySectorErase API (continued)**

		Logical sub sector number (of the corresponding NVM sector) to be verified
<b>Parameters (out)</b>	UnerasedWordlineAddress Ptr UnerasedWordlineCountPtr	Pointer to the first un-erased WL address. Pointer to the un-erased WL count.
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: Erase verification operation is accepted and passed. E_NOT_OK: Erase verification operation is not accepted or failed with more than two un-erasable WLs.
<b>Description</b>	This is an Infineon specific API and not listed in the SWS. It is a service for verifying the erase of a logical sub sector synchronously.	
<b>Source</b>	IFX	
<b>Error handling</b>	FLS_17_DMU_SE_PARAM_DATA, FLS_17_DMU_SE_BUSY, FLS_17_DMU_SE_PARAM_INVLD, FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_SE_PARAM_ADDRESS	
<b>Configuration dependencies</b>	FlsIfxFeeUse	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_ECCC(rw), DMU_HF_MARGIN(rw) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

**1.3.3.23 Fls\_17\_Dmu\_Write**
**Table 104 Specification for Fls\_17\_Dmu\_Write API**

<b>Syntax</b>	<pre>Std_ReturnType Fls_17_Dmu_Write (     const Fls_17_Dmu_AddressType TargetAddress,     const uint8 * const SourceAddressPtr,     const Fls_17_Dmu_LengthType Length )</pre>
<b>Service ID</b>	0x02
<b>Sync/Async</b>	Asynchronous
<b>ASIL Level</b>	B
<b>Re-entrancy</b>	Non reentrant

**1 Fls\_17\_Dmu driver**
**Table 104 Specification for Fls\_17\_Dmu\_Write API (continued)**

<b>Parameters (in)</b>	TargetAddress SourceAddressPtr Length	Target address in the DFlash0 hardware memory. This address offset is be added to the DFlash0 base address. Min.: 0 Max.: FLS_17_DMU_TOTAL_SIZE - 1 Pointer to the source data buffer. Number of bytes to write Min.: 1 Max.: FLS_17_DMU_TOTAL_SIZE - TargetAddress
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	Std_ReturnType	E_OK: write operation accepted E_NOT_OK: write operation not accepted
<b>Description</b>	Writes one or more complete flash pages.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_E_PARAM_DATA, FLS_17_DMU_E_BUSY, FLS_17_DMU_E_PARAM_ADDRESS, FLS_17_DMU_SE_HW_BUSY, FLS_17_DMU_E_VERIFY_ERASE_FAILED, FLS_17_DMU_E_UNINIT, FLS_17_DMU_E_WRITE_FAILED, FLS_17_DMU_E_PARAM_LENGTH	
<b>Configuration dependencies</b>	-	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_ECCC(rw), DMU_HF_ECCS(r), DMU_HF_ERRSR(r), DMU_HF_OPERATION(r), DMU_HF_STATUS(r), STM_TIM0(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.4 Notifications and Callbacks

The FLS driver does not provide any notification or callbacks.

### 1.3.5 Scheduled functions

This section lists all the scheduled functions of the FLS driver.

## 1 Fls\_17\_Dmu driver

### 1.3.5.1 Fls\_17\_Dmu\_MainFunction

**Table 105 Specification for Fls\_17\_Dmu\_MainFunction API**

<b>Syntax</b>	<pre>void Fls_17_Dmu_MainFunction (     void )</pre>	
<b>Service ID</b>	0x06	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	void	-
<b>Description</b>	This API is a service for performing the read, write, erase, compare and blank check jobs on the DFLASH0 hardware.	
<b>Source</b>	AUTOSAR	
<b>Error handling</b>	FLS_17_DMU_E_ERASE_FAILED, FLS_17_DMU_E_READ_FAILED, FLS_17_DMU_E_COMPARE_FAILED, FLS_17_DMU_E_UNINIT, FLS_17_DMU_E_TIMEOUT, FLS_17_DMU_E_VERIFY_WRITE_FAILED, FLS_17_DMU_E_VERIFY_ERASE_FAILED, FLS_17_DMU_E_WRITE_FAILED, FLS_17_DMU_E_BLANKCHECK_FAILED	
<b>Configuration dependencies</b>	-	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_CLRE(w), DMU_HF_ECCC(rw), DMU_HF_ECCS(r), DMU_HF_ERRSR(rw) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.6 Interrupt service routines

This section lists all the interrupt handlers of the FLS driver.



## 1 Fls\_17\_Dmu driver

### 1.3.6.1 Fls\_17\_Dmu\_Isr

**Table 106 Specification for Fls\_17\_Dmu\_Isr API**

<b>Syntax</b>	<pre>void Fls_17_Dmu_Isr (     void )</pre>	
<b>Service ID</b>	0x2D	
<b>Sync/Async</b>	Synchronous	
<b>ASIL Level</b>	B	
<b>Re-entrancy</b>	Non Reentrant	
<b>Parameters (in)</b>	-	-
<b>Parameters (out)</b>	-	-
<b>Parameters (in - out)</b>	-	-
<b>Return</b>	void	-
<b>Description</b>	This interrupt is mapped to the node: SRC_DMU0. This services the Write and Erase Jobs.	
<b>Source</b>	IFX	
<b>Error handling</b>	FLS_17_DMU_E_WRITE_FAILED, FLS_17_DMU_E_VERIFY_ERASE_FAILED, FLS_17_DMU_SE_INVALID_ISR, FLS_17_DMU_E_VERIFY_WRITE_FAILED, FLS_17_DMU_E_ERASE_FAILED	
<b>Configuration dependencies</b>	FlsUseInterrupts	
<b>User hints</b>	-	
<b>SFR accessed</b>	DMU_HF_CLRE(w), DMU_HF_ECCC(rw), DMU_HF_ECCS(r), DMU_HF_ERRSR(rw), DMU_HF_SUSPEND(r) <i>Note : The list includes all the SFRs accessed in the context of the API. It lists the SFRs accessed by the driver and called interfaces from other drivers. During runtime, the SFRs accessed from this list may vary based on configuration and execution context.</i>	
<b>Autosar Version</b>	Applicable for Autosar versions 4.2.2 and 4.4.0.	

### 1.3.7 Callout

The driver does not support any callout functions.

### 1.3.8 Errors Handling

This section describes the various error types reported by the FLS driver.

**1 Fls\_17\_Dmu driver**

Error Name: Description	Source	Error ID (AS422)	Type (AS422)	Error ID (AS440)	Type (AS440)
<b>FLS_17_DMU_E_BLANKCHECK_FAILED:</b> Reported when the blank-check operation fails.	IFX	0x1E	RUNTIME	0x1E	RUNTIME
<b>FLS_17_DMU_E_BUSY:</b> Reported when the any FLS driver API service is called while the driver is still busy executing previous operation.	AUTOSAR	0x06	DET_SAFETY	0x06	DET_SAFETY
<b>FLS_17_DMU_E_COMPARE_FAILED:</b> Reported when the compare operation fails.	AUTOSAR	0x04	RUNTIME	0x04	TRANSIENT
<b>FLS_17_DMU_E_ERASE_FAILED:</b> Reported when the erase operation on DFLASH0 fails.	AUTOSAR	0x01	RUNTIME	0x01	TRANSIENT
<b>FLS_17_DMU_E_HARDENCHK_FAIL:</b> This is reported when the hardening check fails due to the hardware error.	IFX	0x37	RUNTIME	0x37	RUNTIME
<b>FLS_17_DMU_E_INIT_FAILED:</b> This runtime error is reported if OPER error is detected during initialization.	IFX	0x39	RUNTIME	0x39	RUNTIME
<b>FLS_17_DMU_E_PARAM_ADDRESS:</b> Reported when the FLS driver API service is called with the target/source address that is out of the range or when the passed address is not sector or page aligned.	AUTOSAR	0x02	DET_SAFETY	0x02	DET_SAFETY
<b>FLS_17_DMU_E_PARAM_CONFIG:</b> Reported when the FLS driver API service is called with a wrong parameter.	AUTOSAR	0x01	DET_SAFETY	0x01	DET_SAFETY
<b>FLS_17_DMU_E_PARAM_DATA:</b> Reported when the FLS driver API service is called with the value of source/target address as NULL pointer.	AUTOSAR	0x04	DET_SAFETY	0x04	DET_SAFETY
<b>FLS_17_DMU_E_PARAM_LENGTH:</b> Reported when the FLS driver API service is called with wrong length.	AUTOSAR	0x03	DET_SAFETY	0x03	DET_SAFETY
<b>FLS_17_DMU_E_PARAM_POINTER:</b> Reported when the FLS driver's	AUTOSAR	0x0a	DET_SAFETY	0x0a	DET_SAFETY

**1 Fls\_17\_Dmu driver**

Error Name: Description	Source	Error ID (AS422)	Type (AS422)	Error ID (AS440)	Type (AS440)
Fls_17_Dmu_GetVersionInfo() API service is called with a NULL pointer as argument.					
<b>FLS_17_DMU_E_READ_FAILED:</b> Reported when the read operation on DFLASH0 fails.	AUTOSAR	0x03	RUNTIME	0x03	TRANSIENT
<b>FLS_17_DMU_E_RESUME_FAIL:</b> This is reported when the resume of the erase operation fails due to the hardware error.	IFX	0x38	RUNTIME	0x38	RUNTIME
<b>FLS_17_DMU_E_TIMEOUT:</b> Reported when the timeout limit is exceeded during the execution of an FLS driver job.	AUTOSAR	0x09	DET_SAFETY	0x09	RUNTIME
<b>FLS_17_DMU_E_UNINIT:</b> Reported when any of the FLS driver's API service is called without properly initializing the driver.	AUTOSAR	0x05	DET_SAFETY	0x05	DET_SAFETY
<b>FLS_17_DMU_E_VERIFY_ERASE_FAILED:</b> Reported when the erase verification(blank check) fails.	AUTOSAR	0x07	DET_SAFETY	0x07	RUNTIME
<b>FLS_17_DMU_E_VERIFY_WRITE_FAILED:</b> Reported when the write verification (compare) fails.	AUTOSAR	0x08	DET_SAFETY	0x08	RUNTIME
<b>FLS_17_DMU_E_WRITE_FAILED</b> : Reported when write operation on DFLASH0 fails.	AUTOSAR	0x02	RUNTIME	0x02	TRANSIENT
<b>FLS_17_DMU_SE_BUSY:</b> This safety error is raised when the API service is called while the FLS driver is still busy.	IFX	0x06	SAFETY	0x06	SAFETY
<b>FLS_17_DMU_SE_HW_BUSY:</b> This is reported if the DFLASH0 flash bank is still busy with the operation.	IFX	0x6E	SAFETY	0x6E	SAFETY
<b>FLS_17_DMU_SE_HW_TIMEOUT:</b> This safety error is raised when the wait time for the execution of the suspend/resume operation expires.	IFX	0x73	SAFETY	0x73	SAFETY

## 1 Fls\_17\_Dmu driver

Error Name: Description	Source	Error ID (AS422)	Type (AS422)	Error ID (AS440)	Type (AS440)
<b>FLS_17_DMU_SE_ILLGL_OPER</b> <b>TN:</b> This safety error is raised when the erase operation is suspended and a new erase operation is initiated.	IFX	0x64	SAFETY	0x64	SAFETY
<b>FLS_17_DMU_SE_INIT_FAILED:</b> This safety error is reported when the FLS erase operation is suspended and Fls_17_Dmu_Init() is invoked or the DFLASH0 emulation mode is not set to single ended sensing mode.	IFX	0x5F	SAFETY	0x5F	SAFETY
<b>FLS_17_DMU_SE_INVALID_ISR:</b> Error is reported as a safety error when there are spurious(not valid) interrupts.	IFX	0x78	SAFETY	0x78	SAFETY
<b>FLS_17_DMU_SE_PARAM_ADD</b> <b>RESS:</b> Reported when the API service is called with the target/ source address that is out of the range or when the passed address is not sector or page aligned.	IFX	0x02	SAFETY	0x02	SAFETY
<b>FLS_17_DMU_SE_PARAM_DATA</b> : Reported when the API service is called, with the source/target address as NULL pointer.	IFX	0x04	SAFETY	0x04	SAFETY
<b>FLS_17_DMU_SE_PARAM_INVL</b> <b>D:</b> This safety error is reported when the parameter passed as argument of the function is not valid.	IFX	0x5A	SAFETY	0x5A	SAFETY
<b>FLS_17_DMU_SE_PARAM_LEN</b> <b>GTH:</b> Reported when the API service is called with wrong length.	IFX	0x03	SAFETY	0x03	SAFETY
<b>FLS_17_DMU_SE_SUSPNDERA</b> <b>SE_FAIL:</b> This safety error is raised when the suspend error(ERR) in the suspend register(HF_SUSPEND) is set.	IFX	0x50	SAFETY	0x50	SAFETY

### 1.3.9 Deviations and limitations

This section describes the deviations and limitations of the FLS driver.

## 1 Fls\_17\_Dmu driver

### 1.3.9.1 Deviations

This section describes the deviations of the FLS driver.

#### 1.3.9.1.1 Software specification deviations

This section describes the deviations from software specification.

**Table 107** Known deviations

Reference	Deviation
Protection setting is not used	Protection setting is not used in the FLS driver as it is more relevant for the FlsLoader driver and therefore the parameter <code>FlsProtection</code> is not supported.
<code>FlsMaxWriteFastMode</code> and <code>FlsMaxWriteNormalMode</code> configuration parameters are not supported	<code>FlsMaxWriteFastMode</code> / <code>FlsMaxWriteNormalMode</code> configuration parameters are not supported since write is performed for 8 or 32 bytes depending on the data size and page start address.
Runtime error	The runtime error reporting is configurable, if user disables the runtime error reporting, this is a deviation to AUTOSAR.
External flash driver	External flash driver is not supported.
Unexpected flash ID error	<code>FLS_E_UNEXPECTED_FLASH_ID</code> error is not supported as external flash driver is not configured.
<code>FlsAcLoadOnJobStart</code>	<code>FlsAcLoadOnJobStart</code> configuration parameter is not supported because write and erase flash access code is executed from flash.
<code>FlsAcLocationWrite</code>	<code>FlsAcLocationWrite</code> configuration parameter is not supported because the write access code is executed from flash.
<code>FlsAcLocationErase</code>	<code>FlsAcLocationErase</code> configuration parameter is not supported because the erase access code is executed from flash.
Availability of <code>Fls_17_Dmu_Compare</code> API	For ASR440, <code>Fls_17_Dmu_Compare</code> API is not made available by <code>Fls_Com.h</code> . Instead, it is made available by <code>Fls_17_Dmu.h</code> .
File structure	<code>MemIf.h</code> is included instead of <code>MemIf_Types.h</code> which is deviation for AUTOSAR 4.2.2 FLS file structure requirement. <code>MemIf.h</code> includes <code>MemIf_Types.h</code> , hence no functional impact.

#### 1.3.9.1.2 AMDC Violations

The FLS driver does not have any AMDC violations.

#### 1.3.9.1.3 VSMD Violations

This section describes the violations reported by the EB VSMD checker tool with respect to AUTOSAR.

**1 Fls\_17\_Dmu driver**
**Table 108 Violations reported by VSMD checker tool for EB03**

Rule ID:	EB03
VSMD Node(s):	/AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs/FLS_E_COMPARE_FAILED /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs/FLS_E_ERASE_FAILED /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs/FLS_E_READ_FAILED /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs/ FLS_E_UNEXPECTED_FLASH_ID /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs/FLS_E_WRITE_FAILED /AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsExternalDriver /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsJobEndNotification /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsJobErrorNotification
Description:	The StMD node has LOWER-MULTIPLICITY=0 and UPPER-MULTIPLICITY=1. The VSMD-node shall get the OPTIONAL-attribute instead of creating a list!
Additional information:	

**Table 109 Violations reported by VSMD checker tool for EB09**

Rule ID:	EB09
VSMD Node(s):	/AURIX2G/EcucDefs/Fls
Description:	EB specific rule to check consistency of parameter postBuildVariantUsed.
Additional information:	

**Table 110 Violations reported by VSMD checker tool for EcucSws\_1014**

Rule ID:	EcucSws_1014
VSMD Node(s):	/AURIX2G/EcucDefs/Fls /AURIX2G/EcucDefs/Fls/FlsConfigSet /AURIX2G/EcucDefs/Fls/FlsGeneral
Description:	Additional vendor specific parameter definitions (using ParameterTypes), container definitions and references shall be added to the VSMD according to the alphabetical order.
Additional information:	

**1 Fls\_17\_Dmu driver**
**Table 111 Violations reported by VSMD checker tool for EcucSws\_1035**

Rule ID:	EcucSws_1035
VSMD Node(s):	/AURIX2G/EcucDefs/Fls/AURIX2G/EcucDefs/Fls/ FlsConfigSet /AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsAcErase/AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsAcWrite/AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsCallCycle/AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDefaultMode/AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs/AURIX2G/EcucDefs/Fls/ FlsConfigSet/FlsDemEventParameterRefs/ FLS_E_COMPARE_FAILED/AURIX2G/EcucDefs/Fls/ FlsConfigSet/FlsDemEventParameterRefs/ FLS_E_ERASE_FAILED/AURIX2G/EcucDefs/Fls/ FlsConfigSet/FlsDemEventParameterRefs/ FLS_E_READ_FAILED/AURIX2G/EcucDefs/Fls/ FlsConfigSet/FlsDemEventParameterRefs/ FLS_E_UNEXPECTED_FLASH_ID/ AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsDemEventParameterRefs/FLS_E_WRITE_FAILED/ AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsExternalDriver/ AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsExternalDriver/ FlsSpiReference/AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsJobEndNotification/AURIX2G/EcucDefs/Fls/ FlsConfigSet/FlsJobErrorNotification/AURIX2G/ EcucDefs/Fls/FlsConfigSet/FlsMaxReadFastMode/ AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsMaxReadNormalMode/AURIX2G/EcucDefs/Fls/ FlsConfigSet/FlsMaxWriteFastMode/AURIX2G/ EcucDefs/Fls/FlsConfigSet/FlsMaxWriteNormalMode/ AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsProtection/ AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsSectorList/ AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsSectorList/ FlsSector/AURIX2G/EcucDefs/Fls/FlsConfigSet/ FlsSectorList/FlsSector/FlsNumberOfSectors/ AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsSectorList/ FlsSector/FlsPageSize/AURIX2G/EcucDefs/Fls/ FlsConfigSet/FlsSectorList/FlsSector/FlsSectorSize/ AURIX2G/EcucDefs/Fls/FlsConfigSet/FlsSectorList/ FlsSector/FlsSectorStartaddress/AURIX2G/ EcucDefs/Fls/FlsGeneral /AURIX2G/EcucDefs/Fls/ FlsGeneral/FlsAcLoadOnJobStart/AURIX2G/ EcucDefs/Fls/FlsGeneral/FlsBaseAddress/AURIX2G/ EcucDefs/Fls/FlsGeneral/FlsBlankCheckApi/AURIX2G/ EcucDefs/Fls/FlsGeneral/FlsCancelApi/AURIX2G/ EcucDefs/Fls/FlsGeneral/FlsCompareApi/AURIX2G/ EcucDefs/Fls/FlsGeneral/FlsDevErrorDetect/AURIX2G/ EcucDefs/Fls/FlsGeneral/FlsDriverIndex/AURIX2G/ EcucDefs/Fls/FlsGeneral/FlsGetJobResultApi/ AURIX2G/EcucDefs/Fls/FlsGeneral/FlsGetStatusApi/ AURIX2G/EcucDefs/Fls/FlsGeneral/FlsSetModeApi/

**1 Fls\_17\_Dmu driver**
**Table 111 Violations reported by VSMD checker tool for EcucSws\_1035 (continued)**

	AURIX2G/EcucDefs/Fls/FlsGeneral/FlsTotalSize/ AURIX2G/EcucDefs/Fls/FlsGeneral/FlsUseInterrupts/ AURIX2G/EcucDefs/Fls/FlsGeneral/FlsVersionInfoApi/ AURIX2G/EcucDefs/Fls/FlsPublishedInformation/ AURIX2G/EcucDefs/Fls/FlsPublishedInformation/ FlsAcLocationErase/AURIX2G/EcucDefs/Fls/ FlsPublishedInformation/FlsAcLocationWrite/ AURIX2G/EcucDefs/Fls/FlsPublishedInformation/ FlsAcSizeErase/AURIX2G/EcucDefs/Fls/ FlsPublishedInformation/FlsAcSizeWrite/AURIX2G/ EcucDefs/Fls/FlsPublishedInformation/FlsEraseTime/ AURIX2G/EcucDefs/Fls/FlsPublishedInformation/ FlsErasedValue/AURIX2G/EcucDefs/Fls/ FlsPublishedInformation/FlsExpectedHwId/ AURIX2G/EcucDefs/Fls/FlsPublishedInformation/ FlsSpecifiedEraseCycles/AURIX2G/EcucDefs/Fls/ FlsPublishedInformation/FlsWriteTime
Description:	For Containers, Parameters and References elements UUID must be unique (also between StMD and VSMD).
Additional information:	

**Table 112 Violations reported by VSMD checker tool for EcucSws\_2101**

Rule ID:	EcucSws_2101
VSMD Node(s):	/AURIX2G/EcucDefs/Fls/POST_BUILD_VARIANT_USED
Description:	For each ConfigurationVariant supported by the ModuleDef, there must be one ImplementationConfigClass element. In VSMD, the ImplementationConfigClass is mandatory.
Additional information:	

**Table 113 Violations reported by VSMD checker tool for EcucSws\_6003**

Rule ID:	EcucSws_6003
VSMD Node(s):	/AURIX2G/EcucDefs/Fls
Description:	The SHORT-NAME of the AR-PACKAGEs of StMD and VSMD must be different to ensure a unique SHORT- NAME-path.
Additional information:	

**Table 114 Violations reported by VSMD checker tool for TpsEcuc\_06051\_ASR41**

Rule ID:	TpsEcuc_06051_ASR41
VSMD Node(s):	/AURIX2G/EcucDefs/Fls/POST_BUILD_VARIANT_USED
Description:	The implementationConfigClass of an EcucParameterDef or EcucAbstractReferenceDef in



## 1 Fls\_17\_Dmu driver

**Table 114**      **Violations reported by VSMD checker tool for TpsEcuc\_06051\_ASR41 (continued)**

	VSMD shall be the same or higher (where PreCompile configuration class is considered to be the lowest and PostBuild the highest) as in StMD with respect to the selected subset defined by the actually implemented supportedConfigVariant.
Additional information:	

### 1.3.9.2 Limitations

This section describes the limitations of the FLS driver.

**Table 115**      **Known limitations**

Reference	Limitation
FlsMaxWriteNormalMode, FlsMaxWriteFastMode	These parameters are fixed to 32 bytes.
Fls_17_Dmu_Cancel	Although the API is synchronous, hardware may be still busy after returning from Fls_17_Dmu_Cancel API due to already issued flash erase or write command sequence. In such scenarios, any new job issued may get rejected with return value as E_NOT_OK and safety error as FLS_17_DMU_E_HW_BUSY. The user may choose to retry or re-issue the same job again.
Timeout of flash operations	All timeout values used by the FLS module are calculated assuming the FSI operation at 100MHz.
Erase-suspend feature of FLS driver not to be used during simultaneous access of DFlash0 and DFlash1	<p>When the command to resume erase is initiated on DFlash0 by the FLS driver on the TriCore side and DFlash1 is already being accessed by FLS driver on the HSM side, then FSI gets into time-sliced mode of operation to cater to both the requests. Hence, the resume erase operation takes longer time than expected and may lead to timeout.</p> <p>In a scenario where FLS is used with IFX FEE, FEE retries the resume erase operation in case of a failed resume erase operation (due to timeout). During retry, there can be a situation where FLS resume erase operation is successful but the erase job end notification is never raised by the FLS driver. In this situation, FEE driver will hang. Hence, it is recommended to not use the erase-suspend feature during simultaneous access of DFlash0 and DFlash1.</p> <p>[cover parentID FLS={B5E62EDC-1205-401c-B511-6FF0F2C45C39}]</p>
When FLS is used with IFX FEE, QS and NVM features of FEE not to be used together during simultaneous access of DFlash0 and DFlash1	When user content count command is initiated on DFlash0 by the FLS driver on the TriCore side and DFlash1 is already being accessed by FLS driver on the HSM side, then FSI gets into time-sliced mode

**1 Fls\_17\_Dmu driver**
**Table 115 Known limitations (continued)**

Reference	Limitation
	<p>of operation to cater to both the requests. Hence, the user content count command sequence will take longer time than expected and may lead to timeout.</p> <p>In a scenario where FLS is used with IFX FEE, if timeout occurs, FLS will return hardening error. In turn, FEE will not perform hardening check and hardening of the current wordline or page. The data in this wordline or page may be lost since hardening was not done when needed. The probability of occurrence of this situation is low. Hence, it is recommended to not use both QS and NVM features of FEE together during simultaneous access of DFlash0 and DFlash1.</p> <p>[cover parentID FLS={4CD1AAE1-25D9-43b5-9629-0AFEC4D7FF8F}]</p>
Write and erase functionality - Impact of parallel operations on DFlash0 and DFlash1	<p>In the case of concurrent operations on DFlash0 and DFlash1(i.e. active time slicing), the erase time increases by about 15% for CPU erase commands and the write times are prolonged by 5ms. The increased values are considered for timeout calculations for asynchronous operations only. For synchronous operations (resume erase and hardening check), the increased values cause higher execution times and hence are not considered for timeout calculations.</p>

## Revision history

## Revision history

**Table 116**      **Revision history**

Date	Version	Description
2020-12-10	2.0	Released
2020-12-08	1.1	<ul style="list-style-type: none"> <li>- Removed limitation - Fls_17_Dmu_Write API - Regarding passing input parameter SourceAddressPtr to be word-aligned</li> <li>- Removed AMDC violations after ASR440 updates</li> <li>- Removed Software Specification deviations</li> <li>1. Error handling - FLS_17_DMU_E_ERASE_FAILED, FLS_17_DMU_E_WRITE_FAILED, FLS_17_DMU_E_READ_FAILED, FLS_17_DMU_E_COMPARE_FAILED</li> <li>2. Error handling - FLS_17_DMU_E_VERIFY_ERASE_FAILED, FLS_17_DMU_E_VERIFY_WRITE_FAILED, FLS_17_DMU_E_TIMEOUT</li> <li>3. Fls_17_Dmu_Write API - SourceAddressPtr (data buffer) alignment</li> <li>4. Configuration Parameter- FlsEcucPartitionRef</li> <li>5. Configuration Parameter- FlsWriteVerificationEnabled</li> <li>6. Configuration Parameter- FlsEraseVerificationEnabled</li> <li>7. Configuration Parameter- FlsTimeoutSupervisionEnabled</li> <li>8. Behavior of timeouts for erase and write jobs</li> <li>- Updated VSMD violations after ASR440 updates</li> <li>- Added 'Handling of errors when IFX FEE is used' under Key Architectural Considerations section</li> <li>- Updated Example usage regarding timeout handling of erase and write jobs</li> <li>- Added information regarding 'Configuration of FlsIllegalStateNotification' under Example usage section</li> <li>- Removed limitation - FLS_17_DMU_E_TIMEOUT Error</li> <li>- Added deviation - Availability of Fls_17_Dmu_Compare API</li> </ul>
2020-08-14	1.0	Released
2020-08-06	0.1	<ul style="list-style-type: none"> <li>- Initial Version</li> <li>- Fls_17_Dmu driver chapter moved from MC-ISAR_TC3xx_UM_Basic to this document</li> <li>- For 0000053912-11337, added AMDC violations</li> <li>- For 0000053912-11626, added VSMD violations</li> <li>- For 0000053912-12575, unsupported HW features removed. All information captured under 'Hardware-Software mapping' section</li> <li>- For 0000053912-12506, example usage section is corrected</li> </ul>

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**Revision history**
**Table 116**      **Revision history (continued)**

Date	Version	Description
		<ul style="list-style-type: none"> <li>- For 0000053912-12373, the type of <code>FlsSpiReference</code> is corrected</li> <li>- For 000053912-10907, added limitations related to timeout handling, usage of Erase-suspend and hardening features during parallel access of DFlash0 and DFlash1 by Tricore and HSM respectively, updated the timeout values for the write and erase operations considering parallel access of DFlash0 and DFlash1 by Tricore and HSM respectively, updated deviation - 'Behavior of timeouts for erase and write jobs'</li> <li>- For 0000053912-12329, spell check corrected under Hardware-Software mapping section</li> <li>- For 0000053912-12502 and 0000053912-12477, harmonization and format update in all the section</li> <li>- For 0000053912-12452, limitation regarding forcing the minimum value of <code>FlsCallCycle</code> to be 200 microseconds is removed</li> <li>- For 0000053912-13020, listed ASR440 deviations</li> <li>- Removed deviation - 'FLS_E_VERIFY_ERASE_FAILED in Interrupt mode'</li> </ul>

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