

# MCAL Configuration Verification Manual for MCALLIB

## 32-bit TriCore™ AURIX™ TC3xx microcontroller family

### About this document

#### Scope and purpose

This Configuration Data Reference document is applicable to all TC3xx devices in the TriCore™ AURIX™ family of 32-bit microcontrollers.

The purpose of this document is to facilitate the integrator to verify the generated code based on the input configuration parameters. This document describes details of structures, defines, macros and variables generated from the configuration parameters.

#### Intended audience

This document is intended for integrators who need to understand the logic of the generated configuration code of AURIX™ AUTOSAR.

#### Reference documents

This document should be read in conjunction with the following documents:

- AURIX™ TC3xx MCAL User Manual MCALLIB

## Table of contents

About this document.....	1
Table of contents.....	2
McalLib driver .....	3
1.1 File: McalLib_Cfg.h .....	3
1.1.1 Macro: MCALLIB_AR_RELEASE_MAJOR_VERSION .....	3
1.1.2 Macro: MCALLIB_AR_RELEASE_MINOR_VERSION .....	3
1.1.3 Macro: MCALLIB_AR_RELEASE_REVISION_VERSION .....	3
1.1.4 Macro: MCALLIB_SW_MAJOR_VERSION .....	4
1.1.5 Macro: MCALLIB_SW_MINOR_VERSION .....	4
1.1.6 Macro: MCALLIB_SW_PATCH_VERSION .....	4
1.1.7 Macro: MCALLIB_SAFETY_ENABLE .....	5
1.1.8 Macro: MCAL_NO_OF_CORES .....	5
1.1.9 Macro: MCAL_BACKUP_FREQUENCY .....	5
1.1.10 Macro: MCAL_PSPR<x>_GLOBAL_BASE_ADDR .....	6
1.1.11 Macro: MCAL_PSPR<x>_GLOBAL_END_ADDR .....	7
1.1.12 Macro: MCAL_DSPR<x>_GLOBAL_BASE_ADDR .....	7
1.1.13 Macro: MCAL_DSPR<x>_GLOBAL_END_ADDR .....	8
1.1.14 Macro: MCAL_MASTER_COREID .....	9
Revision history.....	10

## McalLib driver

## McalLib driver

This chapter describes the details of the configuration data generated from the MCALLIB driver.

## 1.1 File: McalLib\_Cfg.h

The generated header file contains all pre-compile configuration parameters. Pre-compile time configuration allows decoupling of the static configuration from implementation. The file is generated in 'inc' folder.

### 1.1.1 Macro: MCALLIB\_AR\_RELEASE\_MAJOR\_VERSION

**Table 1** MCALLIB\_AR\_RELEASE\_MAJOR\_VERSION

<b>Name</b>	MCALLIB_AR_RELEASE_MAJOR_VERSION	
<b>Description</b>	Major version number of AUTOSAR release on which the McalLib implementation is based on.	
<b>Verification method</b>	The macro is generated with the value present in 'CommonPublishedInformation/ArMajorVersion'.  <i>Note: The macro is not user configurable.</i>	
<b>Example(s)</b>	<b>Action</b>	<b>Generated output</b>
	Generate McalLib_Cfg.h file with ArMajorVersion 4	#define MCALLIB_AR_RELEASE_MAJOR_VERSION (4U)

### 1.1.2 Macro: MCALLIB\_AR\_RELEASE\_MINOR\_VERSION

**Table 2** MCALLIB\_AR\_RELEASE\_MINOR\_VERSION

<b>Name</b>	MCALLIB_AR_RELEASE_MINOR_VERSION	
<b>Description</b>	Minor version number of AUTOSAR release on which the McalLib implementation is based on.	
<b>Verification method</b>	The macro is generated with the value present in 'CommonPublishedInformation/ArMinorVersion'.  <i>Note: The macro is not user configurable.</i>	
<b>Example(s)</b>	<b>Action</b>	<b>Generated output</b>
	Generate McalLib_Cfg.h file with ArMinorVersion 2	#define MCALLIB_AR_RELEASE_MINOR_VERSION (2U)

### 1.1.3 Macro: MCALLIB\_AR\_RELEASE\_REVISION\_VERSION

**Table 3** MCALLIB\_AR\_RELEASE\_REVISION\_VERSION

<b>Name</b>	MCALLIB_AR_RELEASE_REVISION_VERSION	
<b>Description</b>	Revision version number of AUTOSAR release on which the McalLib implementation is	

## McalLib driver

	based on.	
<b>Verification method</b>	The macro is generated with the value present in 'CommonPublishedInformation/ArPatchVersion'.  <i>Note: The macro is not user configurable.</i>	
<b>Example(s)</b>	<b>Action</b>	<b>Generated output</b>
	Generate McalLib_Cfg.h file with ArPatchVersion 2	#define MCALLIB_AR_RELEASE_REVISION_VERSION (4U)

## 1.1.4 Macro: MCALLIB\_SW\_MAJOR\_VERSION

Table 4 MCALLIB\_SW\_MAJOR\_VERSION

<b>Name</b>	MCALLIB_SW_MAJOR_VERSION	
<b>Description</b>	Major version number of the McalLib module.	
<b>Verification method</b>	The macro is generated with the value present in 'CommonPublishedInformation/SwMajorVersion'.  <i>Note: The macro is not user configurable.</i>	
<b>Example(s)</b>	<b>Action</b>	<b>Generated output</b>
	Generate McalLib_Cfg.h file with SwMajorVersion 10	#define MCALLIB_SW_MAJOR_VERSION (10U)

## 1.1.5 Macro: MCALLIB\_SW\_MINOR\_VERSION

Table 5 MCALLIB\_SW\_MINOR\_VERSION

<b>Name</b>	MCALLIB_SW_MINOR_VERSION	
<b>Description</b>	Minor version number of the McalLib module.	
<b>Verification method</b>	The macro is generated with the value present in 'CommonPublishedInformation/SwMinorVersion'.  <i>Note: The macro is not user configurable.</i>	
<b>Example(s)</b>	<b>Action</b>	<b>Generated output</b>
	Generate McalLib_Cfg.h file with SwMinorVersion 10	#define MCALLIB_SW_MINOR_VERSION (10U)

## 1.1.6 Macro: MCALLIB\_SW\_PATCH\_VERSION

Table 6 MCALLIB\_SW\_PATCH\_VERSION

<b>Name</b>	MCALLIB_SW_PATCH_VERSION	
<b>Description</b>	Patch level version number of the McalLib module.	
<b>Verification method</b>	The macro is generated with the value present in	

## McalLib driver

	‘CommonPublishedInformation/SwPatchVersion’.	
	<i>Note: The macro is not user configurable.</i>	
Example(s)	Action	Generated output
	Generate McalLib_Cfg.h file with SwPatchVersion 0	#define MCALLIB_SW_PATCH_VERSION (0U)

## 1.1.7 Macro: MCALLIB\_SAFETY\_ENABLE

Table 7 MCALLIB\_SAFETY\_ENABLE

Name	MCALLIB_SAFETY_ENABLE	
Description	Enables/disables safety features	
Verification method	The macro is generated as STD_ON if McalLibSafetyEnable configuration parameter is set to ‘True’ else the macro is generated as STD_OFF.	
Example(s)	Action	Generated output
	McalLibSafetyEnable = True	#define MCALLIB_SAFETY_ENABLE (STD_ON)
	McalLibSafetyEnable = False	#define MCALLIB_SAFETY_ENABLE (STD_OFF)

## 1.1.8 Macro: MCAL\_NO\_OF\_CORES

Table 8 MCAL\_NO\_OF\_CORES

Name	MCAL_NO_OF_CORES	
Description	Specifies the number of cores available for the selected device.	
Verification method	The macro is generated based on number of cores available in the device.	
	<i>Note: The macro is not user configurable.</i>	
Example(s)	Action	Generated output
	Generate McalLib_Cfg.h file and total core in the device = 4	#define MCAL_NO_OF_CORES (4)
	Generate McalLib_Cfg.h file and total core in the device = 6	#define MCAL_NO_OF_CORES (6)

## 1.1.9 Macro: MCAL\_BACKUP\_FREQUENCY

Table 9 MCAL\_BACKUP\_FREQUENCY

Name	MCAL_BACKUP_FREQUENCY	
Description	Specifies the frequency of the back-up clock in MHz.	
Verification method	The macro is generated based on back-up clock available in the device.	

Note: The macro is not user configurable.

Example(s)	Action	Generated output
	Generate McalLib_Cfg.h file and Backup Frequency is 100MHz	#define MCAL_BACKUP_FREQUENCY (100)

### 1.1.10 Macro: MCAL\_PSPR<x>\_GLOBAL\_BASE\_ADDR

**Table 10** MCAL\_PSPR<x>\_GLOBAL\_BASE\_ADDR

Name	MCAL_PSPR<x>_GLOBAL_BASE_ADDR	
Description	Specifies the start address of PSPPR for core<x> where x=0 up to maximum number of cores available in hardware.	
Verification method	The macro is generated based on start address of PSPPR for core<x> in the device. If the PSPPR for the core is not available, the macro is generated as 0.	
	Note: The macro is not user configurable.	
Example(s)	Action	Generated output
	Generate McalLib_Cfg.h file with	#define MCAL_PSPR0_GLOBAL_BASE_ADDR (1880096768)
	<ul style="list-style-type: none"> <li>• PSPPR0 start address is 1880096768</li> </ul>	#define MCAL_PSPR1_GLOBAL_BASE_ADDR (1611661312)
	<ul style="list-style-type: none"> <li>• PSPPR1 start address is 1611661312</li> </ul>	#define MCAL_PSPR2_GLOBAL_BASE_ADDR (0)
	<ul style="list-style-type: none"> <li>• PSPPR2 is not available</li> </ul>	#define MCAL_PSPR3_GLOBAL_BASE_ADDR (1074790400)
	<ul style="list-style-type: none"> <li>• PSPPR3 start address is 1074790400</li> </ul>	#define MCAL_PSPR4_GLOBAL_BASE_ADDR (806354944)
	<ul style="list-style-type: none"> <li>• PSPPR4 start address is 806354944</li> </ul>	#define MCAL_PSPR5_GLOBAL_BASE_ADDR (0)
	<ul style="list-style-type: none"> <li>• PSPPR5 is not available</li> </ul>	
	<ul style="list-style-type: none"> <li>• PSPPR0 start address is 1611661312</li> </ul>	#define MCAL_PSPR0_GLOBAL_BASE_ADDR (1611661312)
	<ul style="list-style-type: none"> <li>• PSPPR1 is not available</li> </ul>	#define MCAL_PSPR1_GLOBAL_BASE_ADDR (0)
	<ul style="list-style-type: none"> <li>• PSPPR2 start address is 1343225856</li> </ul>	#define MCAL_PSPR2_GLOBAL_BASE_ADDR (1343225856)
	<ul style="list-style-type: none"> <li>• PSPPR3 is not available</li> </ul>	#define MCAL_PSPR3_GLOBAL_BASE_ADDR (0)
	<ul style="list-style-type: none"> <li>• PSPPR4 is not available.</li> </ul>	#define MCAL_PSPR4_GLOBAL_BASE_ADDR (0)
	<ul style="list-style-type: none"> <li>• PSPPR5 start address is 269484032</li> </ul>	#define MCAL_PSPR5_GLOBAL_BASE_ADDR (269484032)

### 1.1.11 Macro: MCAL\_PSPR<x>\_GLOBAL\_END\_ADDR

**Table 11** MCAL\_PSPR<x>\_GLOBAL\_END\_ADDR

<b>Name</b>	MCAL_PSPR<x>_GLOBAL_END_ADDR	
<b>Description</b>	Specifies the end address of PSPR for core<x> where x=0 up to maximum number of cores available in hardware.	
<b>Verification method</b>	<p>The macro is generated based on end address of PSPR for core&lt;x&gt; in the device. If the PSPR for the core is not available, the macro is generated as 0.</p> <p><i>Note: The macro is not user configurable.</i></p>	
<b>Example(s)</b>	<b>Action</b>	<b>Generated output</b>
	Generate McalLib_Cfg.h file with <ul style="list-style-type: none"> <li>• PSPR0 end address is 1880162303</li> <li>• PSPR1 end address is 1611726847</li> <li>• PSPR2 end address is 1343291391</li> <li>• PSPR3 is not available</li> <li>• PSPR4 is not available</li> <li>• PSPR5 end address is 269549567</li> </ul>	<pre>#define MCAL_PSPR0_GLOBAL_END_ADDR (1880162303) #define MCAL_PSPR1_GLOBAL_END_ADDR (1611726847) #define MCAL_PSPR2_GLOBAL_END_ADDR (1343291391) #define MCAL_PSPR3_GLOBAL_END_ADDR (0) #define MCAL_PSPR4_GLOBAL_END_ADDR (0) #define MCAL_PSPR5_GLOBAL_END_ADDR (269549567)</pre>
	<ul style="list-style-type: none"> <li>• PSPR0 end address is 1611726847</li> <li>• PSPR1 is not available</li> <li>• PSPR2 is not available</li> <li>• PSPR3 end address is 1074855935</li> <li>• PSPR4 end address is 806420479</li> <li>• PSPR5 is not available</li> </ul>	<pre>#define MCAL_PSPR0_GLOBAL_END_ADDR (1611726847) #define MCAL_PSPR1_GLOBAL_END_ADDR (0) #define MCAL_PSPR2_GLOBAL_END_ADDR (0) #define MCAL_PSPR3_GLOBAL_END_ADDR (1074855935) #define MCAL_PSPR4_GLOBAL_END_ADDR (806420479) #define MCAL_PSPR5_GLOBAL_END_ADDR (0)</pre>

### 1.1.12 Macro: MCAL\_DSPR<x>\_GLOBAL\_BASE\_ADDR

**Table 12** MCAL\_DSPR<x>\_GLOBAL\_BASE\_ADDR

<b>Name</b>	MCAL_DSPR<x>_GLOBAL_BASE_ADDR
<b>Description</b>	Specifies the start address of DSPR for core <x> where x=0 up to maximum number of cores available in hardware.
<b>Verification method</b>	The macro is generated based on end address of DSPR for core<x> in the device. If the

	PSPR for the core is not available, the macro is generated as 0.	
	<i>Note: The macro is not user configurable.</i>	
Example(s)	Action	Generated output
	Generate McalLib_Cfg.h file with <ul style="list-style-type: none"> <li>• DSPR0 start address is 1879048192</li> <li>• DSPR1 start address is 1610612736</li> <li>• DSPR2 is not available</li> <li>• DSPR3 is not available</li> <li>• DSPR4 start address is 805306368</li> <li>• DSPR5 start address is 268435456</li> </ul>	<pre>#define MCAL_DSPR0_GLOBAL_BASE_ADDR (1879048192) #define MCAL_DSPR1_GLOBAL_BASE_ADDR (1610612736) #define MCAL_DSPR2_GLOBAL_BASE_ADDR (0) #define MCAL_DSPR3_GLOBAL_BASE_ADDR (0) #define MCAL_DSPR4_GLOBAL_BASE_ADDR (805306368) #define MCAL_DSPR5_GLOBAL_BASE_ADDR (268435456)</pre>
	<ul style="list-style-type: none"> <li>• DSPR0 start address is 1610612736</li> <li>• DSPR1 is not available</li> <li>• DSPR2 start address is 1342177280</li> <li>• DSPR3 start address is 1073741824</li> <li>• DSPR4 is not available</li> <li>• DSPR5 is not available</li> </ul>	<pre>#define MCAL_DSPR0_GLOBAL_BASE_ADDR (1610612736) #define MCAL_DSPR1_GLOBAL_BASE_ADDR (0) #define MCAL_DSPR2_GLOBAL_BASE_ADDR (1342177280) #define MCAL_DSPR3_GLOBAL_BASE_ADDR (1073741824) #define MCAL_DSPR4_GLOBAL_BASE_ADDR (0) #define MCAL_DSPR5_GLOBAL_BASE_ADDR (0)</pre>

### 1.1.13 Macro: MCAL\_DSPR<x>\_GLOBAL\_END\_ADDR

**Table 13** MCAL\_DSPR<x>\_GLOBAL\_END\_ADDR

<b>Name</b>	MCAL_DSPR<x>_GLOBAL_END_ADDR	
<b>Description</b>	Specifies the end address of DSPR for core 0.	
<b>Verification method</b>	The macro is generated based on end address of DSPR for core <x> where x=0 up to maximum number of cores available in hardware.  <i>Note: The macro is not user configurable.</i>	
Example(s)	Action	Generated output
	Generate McalLib_Cfg.h file with <ul style="list-style-type: none"> <li>• DSPR0 end address is 1879293951</li> </ul>	<pre>#define MCAL_DSPR0_GLOBAL_END_ADDR (1879293951) #define MCAL_DSPR1_GLOBAL_END_ADDR (1610858495)</pre>



	<ul style="list-style-type: none"> <li>• DSPR1 end address is 1610858495</li> <li>• DSPR2 is not available</li> <li>• DSPR3 end address is 1073840127</li> <li>• DSPR4 end address is 805404671</li> <li>• DSPR5 end address is 268533759</li> </ul>	<pre>#define MCAL_DSPR2_GLOBAL_END_ADDR (0) #define MCAL_DSPR3_GLOBAL_END_ADDR (1073840127) #define MCAL_DSPR4_GLOBAL_END_ADDR (805404671) #define MCAL_DSPR5_GLOBAL_END_ADDR (268533759)</pre>
	<ul style="list-style-type: none"> <li>• DSPR0 end address is 1610858495</li> <li>• DSPR1 is not available</li> <li>• DSPR2 end address is 1342275583</li> <li>• DSPR3 is not available</li> <li>• DSPR4 is not available</li> <li>• DSPR5 is not available</li> </ul>	<pre>#define MCAL_DSPR0_GLOBAL_END_ADDR (1610858495) #define MCAL_DSPR1_GLOBAL_END_ADDR (0) #define MCAL_DSPR2_GLOBAL_END_ADDR (1342275583) #define MCAL_DSPR3_GLOBAL_END_ADDR (0) #define MCAL_DSPR4_GLOBAL_END_ADDR (0) #define MCAL_DSPR5_GLOBAL_END_ADDR (0)</pre>

### 1.1.14 Macro: MCAL\_MASTER\_COREID

**Table 14** MCAL\_MASTER\_COREID

<b>Name</b>	MCAL_MASTER_COREID	
<b>Description</b>	Indicates the CPU ID which is considered as the master core.	
<b>Verification method</b>	The macro is generated based on 'ResourceMMcalConfig/ResourceMMasterCore'.	
<b>Example(s)</b>	<b>Action</b>	<b>Generated output</b>
	Generate McalLib_Cfg.h file with ResourceMMasterCore = 1	<pre>#define MCAL_MASTER_COREID (1)</pre>
	Generate McalLib_Cfg.h file with ResourceMMasterCore = 5	<pre>#define MCAL_MASTER_COREID (5)</pre>

## Revision history

## Revision history

## Major changes since the last revision

Date	Version	Description
2020-10-27	3.0	MCALLIB driver chapter moved from MC-ISAR_TC3xx_Config_Verification_Manual_BASIC.pdf to this document
2019-07-19	2.0	Updated revision history
2019-02-20	1.10.0_1.0	Initial Release

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**Do you have a question about this document?**

**Email:** [erratum@infineon.com](mailto:erratum@infineon.com)

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