

# Release Notes Appendix for Tasking compiler

## Release Notes

Product name: Refer to Package\* release notes

Release number: 2.0.0

Type of release: Refer to Package\* release notes

Release method: via Release Area

AUTOSAR specification: Refer to Package\* release notes

Compiler support: Tasking 6.3r1p2

Processor platform: Refer to Package\* release notes

Date: 2021-04-09

Previous release number: Refer to Package\* release notes

## About this document

### Scope and purpose

This document, provides information of compiler and compiler options intended to use for 2.0.0 delivery.

*Note:           \*Here Package refers to BASIC/CD/COM-E/ DEMO package.*

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**Table of contents****Table of contents**

	<b>About this document</b> .....	1
	<b>Table of contents</b> .....	2
<b>1</b>	<b>Tool information</b> .....	3
1.1	Compiler options .....	3
<b>2</b>	<b>Known issues</b> .....	3
	<b>Disclaimer</b> .....	5

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**Tool information**

## 1 Tool information

**Table 1 Tool information**

Tool description	Version details	
Compiler	<b>Version:</b> 6.3r1p2	<b>Build number:</b> Build 20061357 Copyright 2002-2020 TASKING BV

### 1.1 Compiler options

**Table 2 TASKING compiler options used**

Options	Description
Compiler options	--core=tc1.6.2 --iso=99 -O2 --eabi=BCFHNSW -AGKpvX --tradeoff=2 --fp-model=1 --switch=auto --integer-enumeration --default-near-size=0 --global-type-checking
Assembler options	--core=tc1.6.2 --list-format=1 --optimize=gs
Linker options	-OcLtXY --core=mpe:vtc --global-type-checking

**Note:** Compiler options which influence code generation and are not listed, should be left to the default compiler settings. All the above-listed compiler options are mandatory.

**Attention:** If the compiler options are changed by the user, and if the generated binary output is different than the one generated by the usage of the mandatory compiler options, the functionality and reliability of the drivers cannot be ensured.

## 2 Known issues

This chapter describes the prescribed workarounds for all the open issues identified.

**Table 3 Known issues**

Module	Issue number	Description
Generic	0000053912-13111	Description: Tasking compiler errata TCVX-44325 Impact: Due to code compaction optimization (reverse in-lining), possibility to trigger a run-time stack pointer alignment error if stack pointer alignment protection is enabled on the hardware. Workaround: 1) Do not disable the Interrupt Stack in hardware, OR 2) Do not enable stack pointer check in hardware.
	0000053912-15836	Description: Compiler errata TCVX-44339 and TCVX-44387 analysis. Impact: Compiler errata issues not seen/reproduced during internal testing of MCAL with the published compiler options. Customer shall analyze the impact of compiler errata TCVX-44339 and TCVX-44387 for their application with MCAL. Workaround: No impact seen in MCAL. Refer workarounds in compiler errata if customer application is affected.

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**Known issues****Table 3**      **Known issues (continued)**

Module	Issue number	Description
	0000053912-16537	Description: Compiler errata TCVX-44453 analysis. Impact: Compiler errata issues not seen/reproduced during internal testing of MCAL with the published compiler options. Customer shall analyze the impact of compiler errata TCVX-44453 for their application with MCAL. Workaround: No impact seen in MCAL. Refer workarounds in compiler errata if customer application is affected.
	0000053912-11963	Description: Compiler errata TCVX-44257 analysis. Impact: Issue not seen/reproduced during internal testing of MCAL with the published compiler options. Customer shall analyze the impact of compiler erratas TCVX-44257 for their application with MCAL.. Workaround: None.

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**aspect of this document?**  
**Email: [erratum@infineon.com](mailto:erratum@infineon.com)**

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