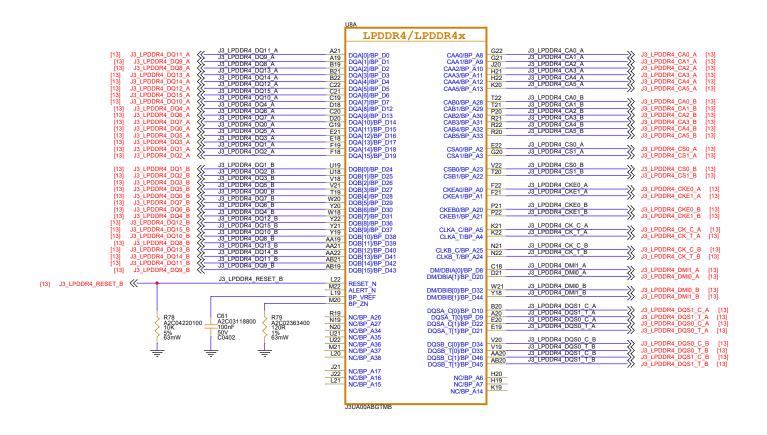
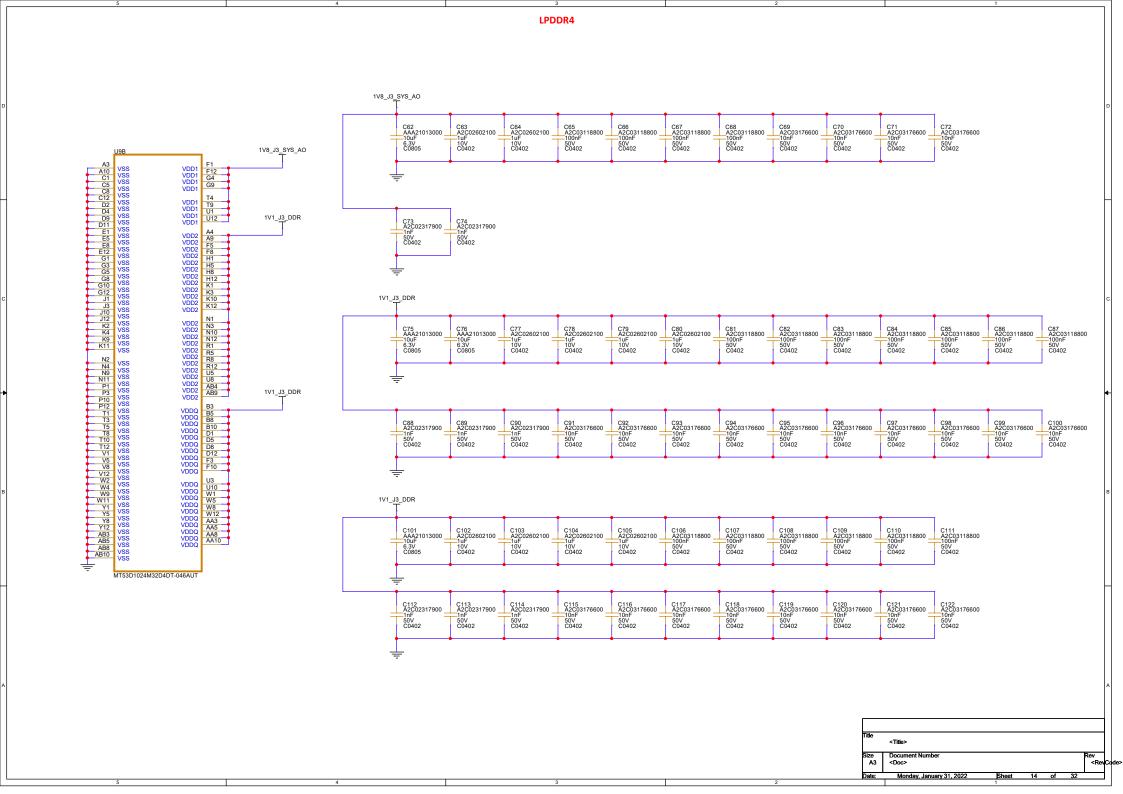


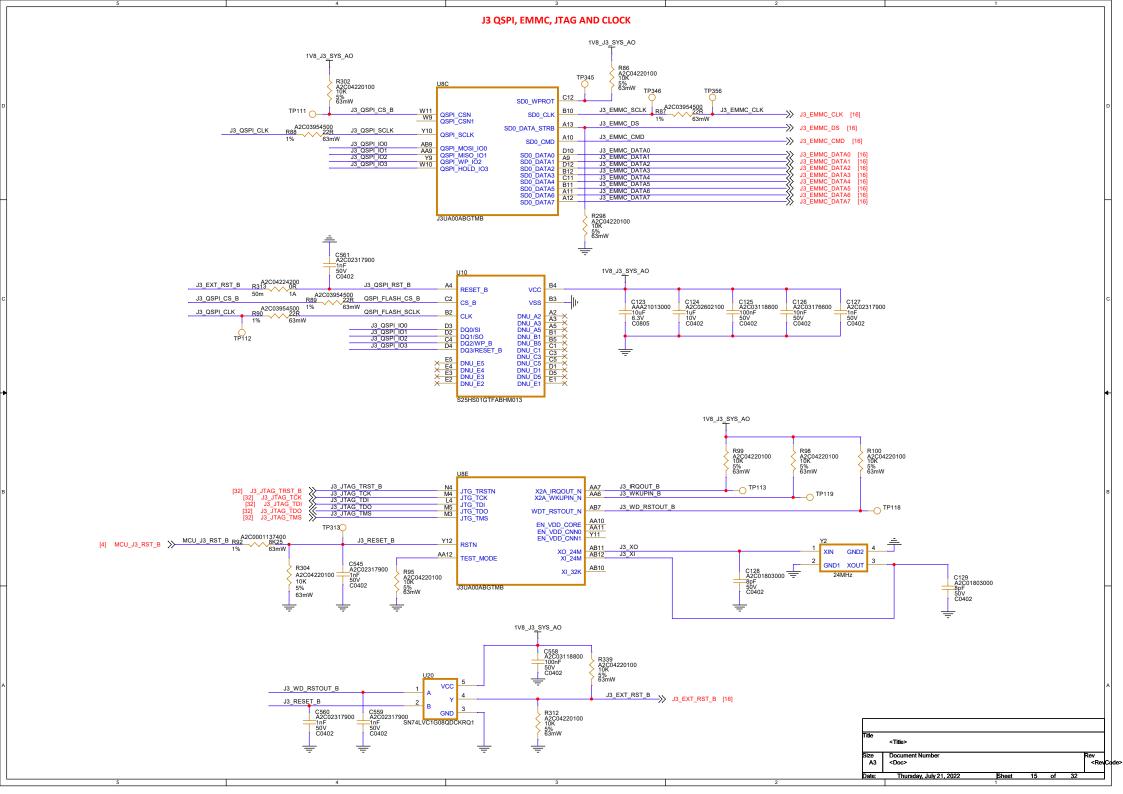
J3 LPDDR4 CONTROLLER



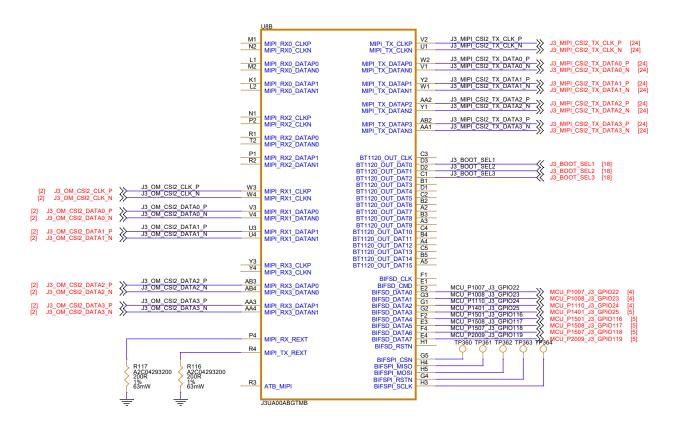
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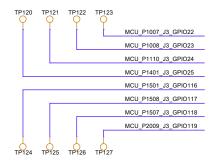
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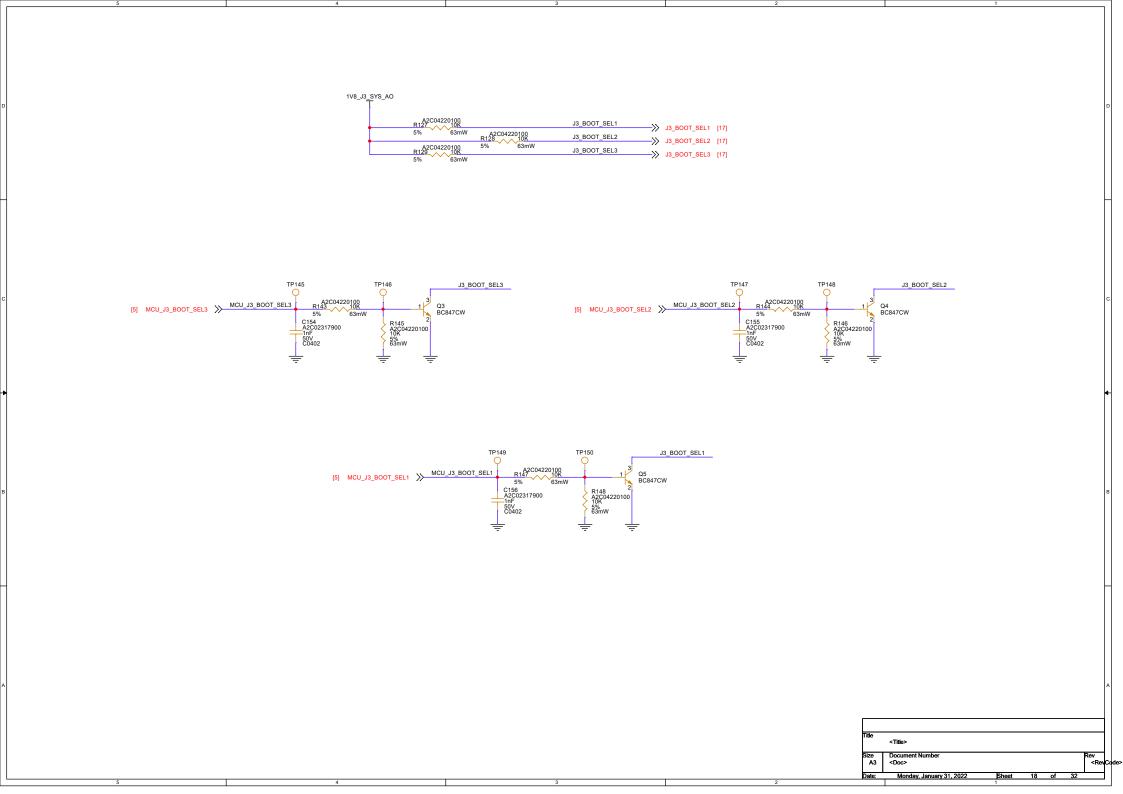




J3 MIPI CSI2 TX, RX BOOT CONFIG







J3 GEMAC, I2C, SPI, UART AND GPIO D14 MDCK MDIO J3_GEMAC_MDC J3_I2C0_SCL [23] J3_GEMAC_MDC [23] J3_GEMAC_MDIO → J3_I2C0_SCL [2] → J3_I2C0_SDA [2] I2C0_SCL I2C0_SDA J3 GEMAC MDIO J3 I2C0 SDA J3 GEMAC RGMII TXCLK J3 J2C1 SCI RGMII TX CLK J3_I2C1_SCL [24] J3_I2C1_SDA [24] J3 GEMAC RGMII TXCTL J3 GEMAC RGMII TXD0 J3 GEMAC RGMII TXD1 J3_I2C1_SDA RGMII_TX_EN RGMII_TXD0 I2C1_SDA MCU_M_J3_S_SPI_MOSI MCU_M_J3_S_SPI_MISO MCU_M_J3_S_SPI_MOSI [4] MCU_M_J3_S_SPI_MISO [4] RGMII_TXD1 J3 GEMAC RGMII TXD2 J3 GEMAC RGMII TXD3 RGMII TXD2 I2C2_SDA RGMII_TXD3 MCU_M_J3_S_SPI_SCLK MCU_M_J3_S_SPI_CS_B MCU_M_J3_S_SPI_SCLK [4] MCU_M_J3_S_SPI_CS_B [4] I2C3_SCL I2C3_SDA J3_GEPHY_RGMI_RXCLI J3_GEPHY_RGMI_RXD1 J3_GEPHY_RGMI_RXD0 J3_GEPHY_RGMI_RXD2 J3_GEPHY_RGMI_RXD2 J3_GEPHY_RGMI_RXD2 J3_GEPHY_RGMII_RXCLK H2 J3_GEPHY_RGMII_RXCLK J3_GEPHY_RGMII_RXCTL RGMII_RX_CLK J3_GEPHY_RGMI_RXCTL J3_GEPHY_RGMI_RXD0 RGMII_RX_DV RGMII_RXD0 J3 UARTO TXD J3_UART0_TXD [32] J3_UART0_RXD [32] J3_GEPHY_RGMII_RXD0 J3_GEPHY_RGMII_RXD1 UARTO TXD J3_UART0_RXD RGMII RXD1 UARTO_RXD RGMII_RXD2 UART1_TXD UART1_RXD UART1_RTSN J3 GEPHY RGMII RXD3 RGMII_RXD3 W7 W8 Y7 Y8 MCU_S_J3_M_SPI_LS_OE J3_OM_VMERRB MCU_S_J3_M_SPI_LS_OE [5] J3_OM_VMERRB [2] I2S0_MCLK UART1_CTSN 12S0 LRCK I2S0_BCLK MCU_S_J3_M_SPI_CS_B MCU_S_J3_M_SPI_SCLK MCU_S_J3_M_SPI_CS_B [5] MCU_S_J3_M_SPI_SCLK [5] MCU_S_J3_M_SPI_MOSI [5] MCU_S_J3_M_SPI_MISO [5] SPI0_CSN SPI0_SCLK SPI0_MOSI SPI0_MISO 12S0_SDIO AA5 V5 Y6 W6 W5 MCU S J3 M SPI MOSI MCU S J3 M SPI MISO I2S1 MCLK I2S1 LRCK I2S1 BCLK I2S1_SDIO AB14 II AA14 USB_REF_PAD_CLK_P USB_REF_PAD_CLK_M J3_USB_DP J3_USB_DM J3_USB_TX_P J3_USB_RX_P J3_USB_RX_N J3_USB_DP J3_USB_DM J3_USB_TX_P J3_USB_TX_N USB_DP USB_DM SD1_DATA3 J3_OM_RST_B AA16 AB16 AA17 USB_TX_P USB_TX_M USB_RX_P J3_OM_RST_B [2] J3_SER_PWDN_B [24] J3_SER_ERRB [24] J3_SER_LOCK [24] J3_USB_TX_N J3_USB_RX_P J3_SER_PWDN_B J3_SER_ERRB SD2_CMD SD2_DATA0 SD2_DATA1 J3_USB_RX_N AB17 USB RX M SD2_DATA2 Y14 AA13 W14 J3_USB_VBUS_DET USB_VBUS J3_USB_VBUS_DET [32] SD2_DATA3 USB_ID USB_RESREF USB_RESREF J3_OM_ERRB SENSOR0_MCLK SENSOR1 MCLK J3_OM_ERRB [2] J3_OM_VSYNC [2] J3 OM VSYNC R149 A2C04293200 200R 1% 63mW V13 V12 W13 PVT_AN_IO0 PVT_AN_IO1 SENSOR2_MCLK SENSOR3_MCLK J3_GEPHY_INT_B [23] J3_GEPHY_RST_B [23] J3_GEPHY_RST_B PVT_VIN_HI PVT_VIN_LO W12 C14 × EPHY_CLK B13 × SD0_DET_N ÷ J3UA00ABGTMB 1V8 R150 A2C04220400 4K7 R153 A2C04220400 4K7 R152 A2C04220400 4K7 R151 A2C04220400 4K7 5% 63mW 5% 63mW 5% 63mW J3_I2C0_SCL J3_I2C0_SDA J3_I2C1_SCL J3_I2C1_SDA J3_GEMAC_RGMII_TXCLK J3_GEPHY_RGMII_TXCLK J3_GEPHY_RGMII_TXCLK [23] A2C03954500 R161 22R J3 GEPHY_RGMII_TXCTL J3 GEMAC RGMII TXCTL J3_GEPHY_RGMII_TXCTL [23] 1% J3_GEMAC_RGMII_TXD0 J3_GEPHY_RGMII_TXD0 J3_GEPHY_RGMII_TXD0 [23] A2C03954500 R163 22R 1% 63mW J3_GEMAC_RGMII_TXD1 J3_GEPHY_RGMII_TXD1 → J3_GEPHY_RGMII_TXD1 [23] A2C03954500 R164 22R 1% 63mW J3_GEMAC_RGMII_TXD2 J3_GEPHY_RGMII_TXD2 J3_GEPHY_RGMII_TXD2 [23] A2C03954500 R165 22R J3_GEMAC_RGMII_TXD3 J3_GEPHY_RGMII_TXD3 J3_GEPHY_RGMII_TXD3 [23] 63mW <Title> Document Number A3 <Doc> <RevCo Monday, January 31, 2022 Sheet 19 of

