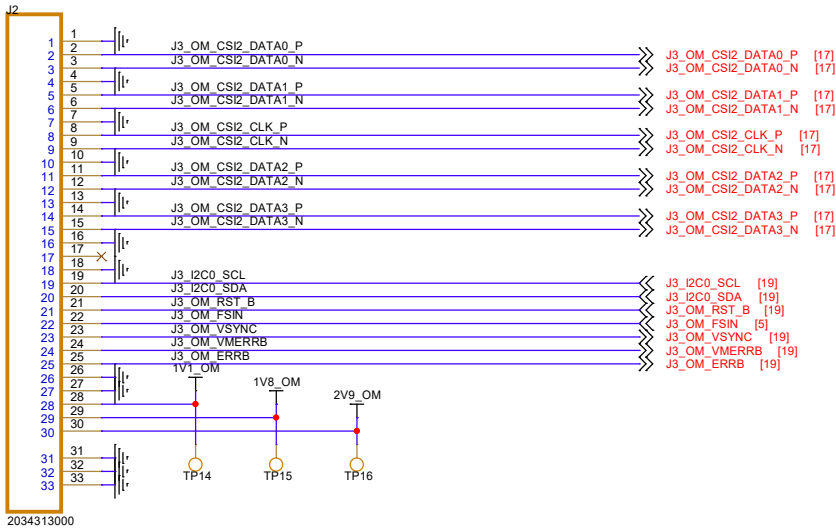
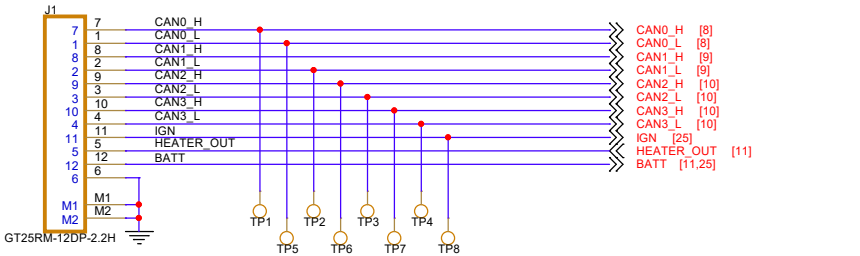


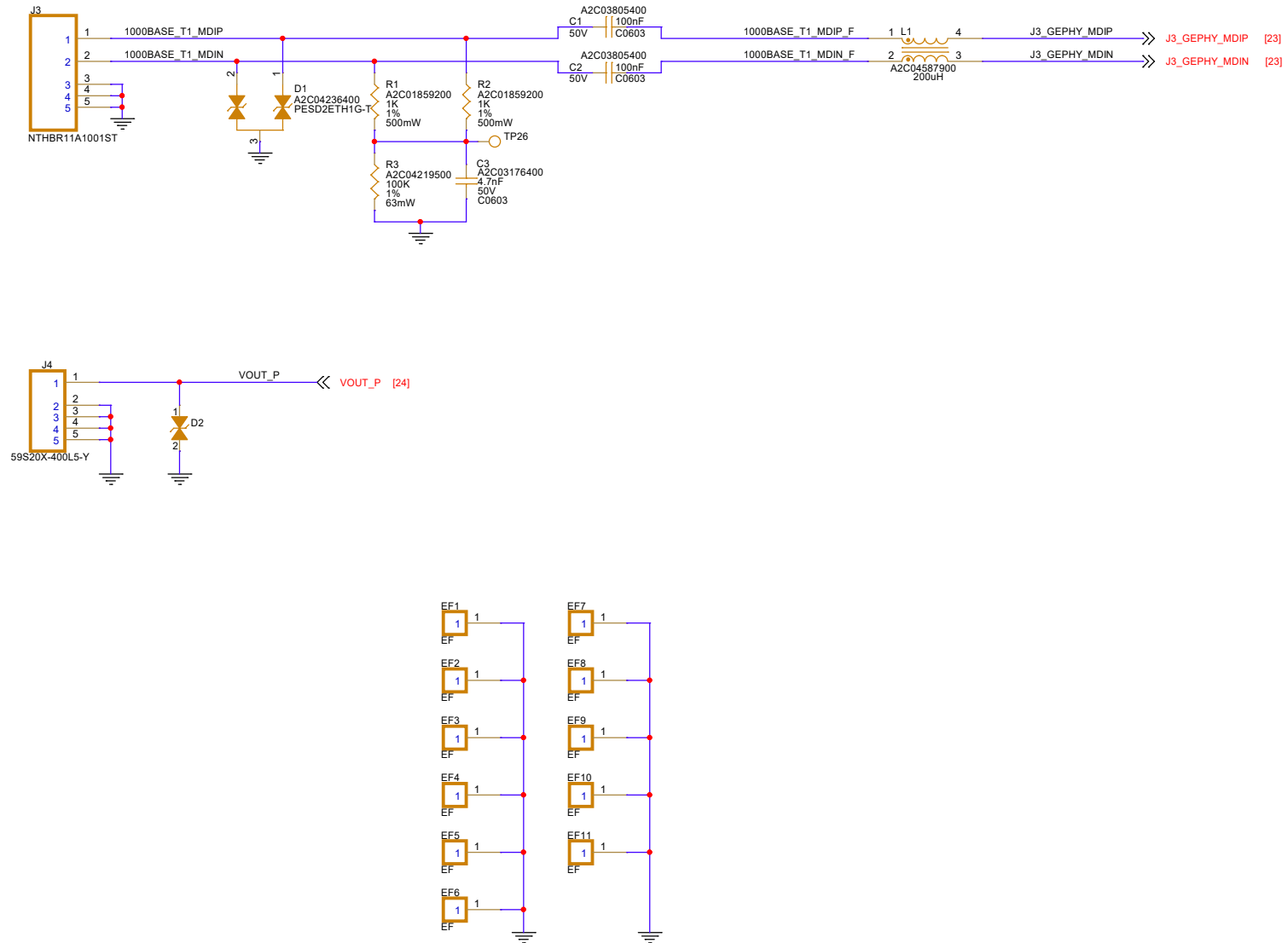
BLOCK DIAGRAM

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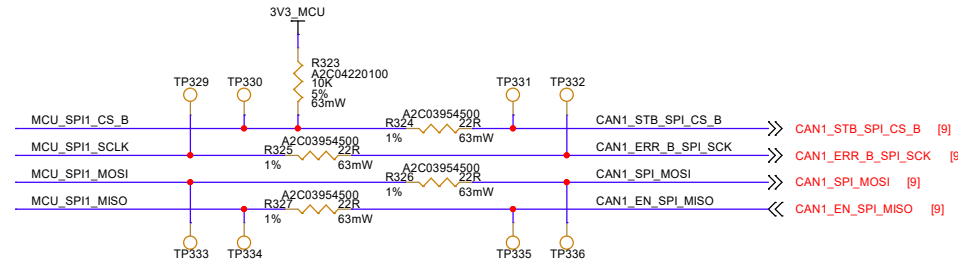
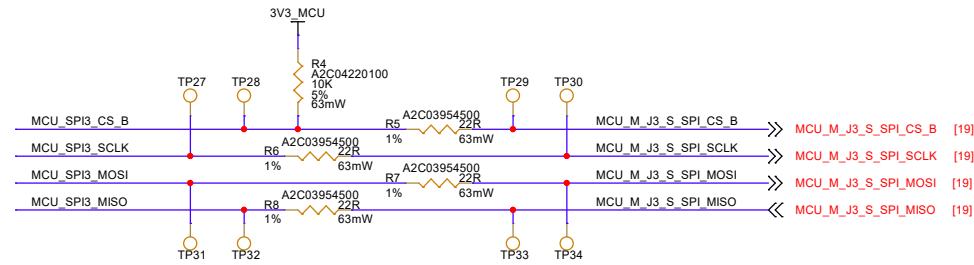
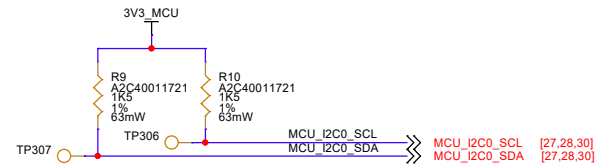
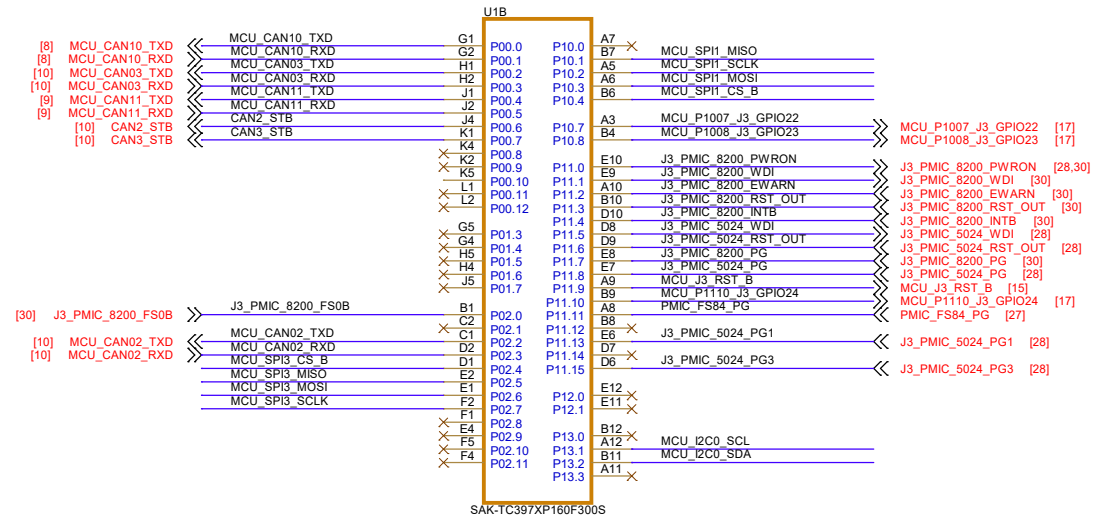
VEHICLE INTERFACE: MAIN CON AND OM CON



# VEHICLE INTERFACE: 1000BASE-T1 AND VIDEO OUTPUT

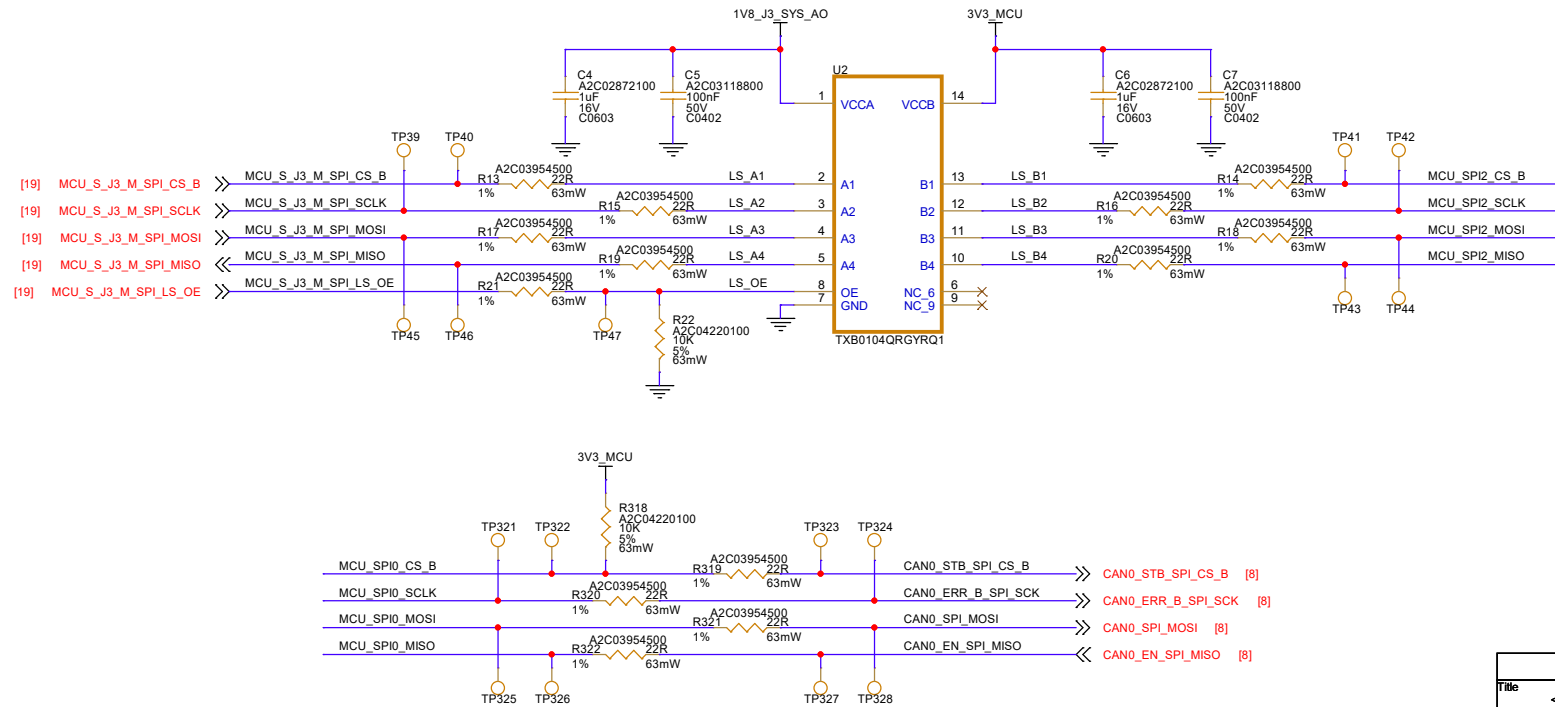
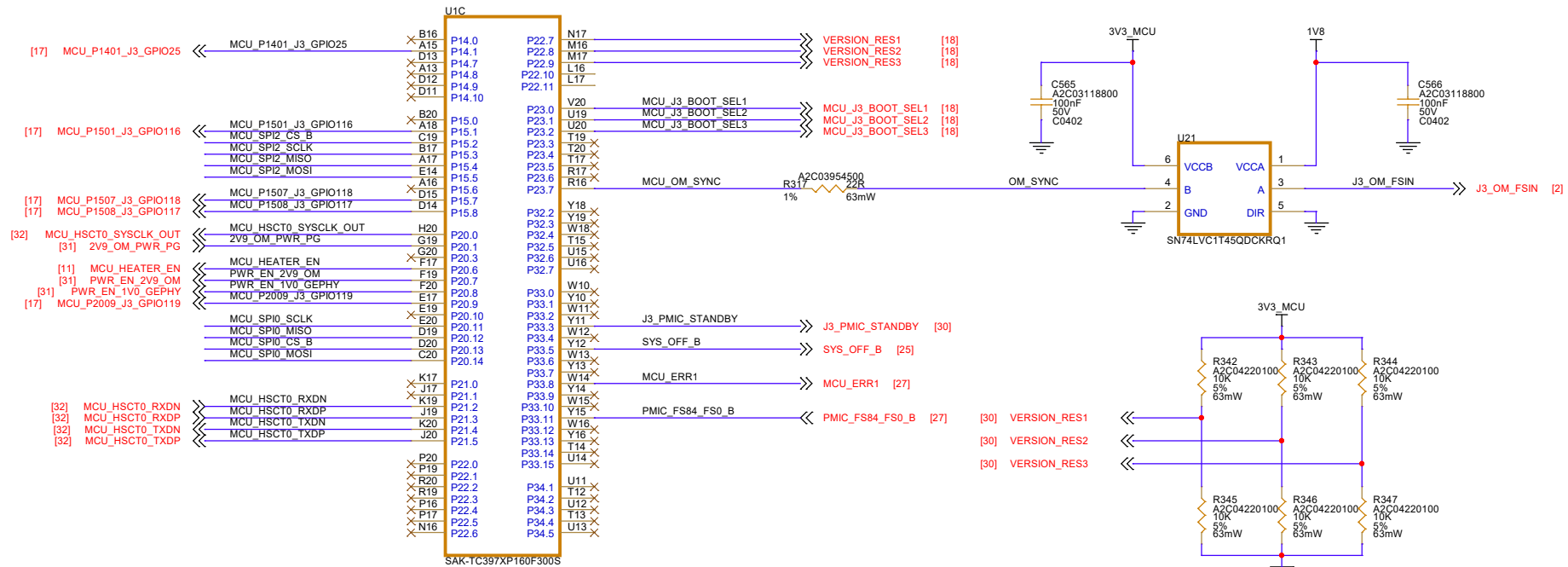


## SAFETY MCU



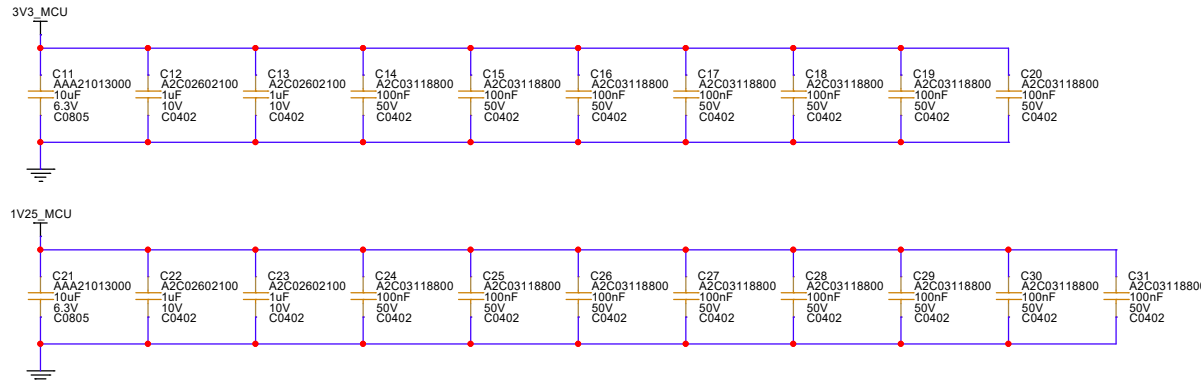
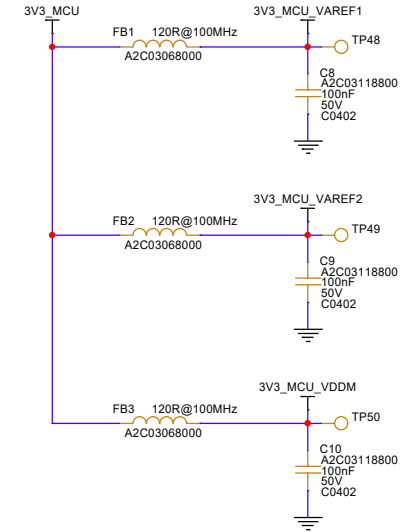
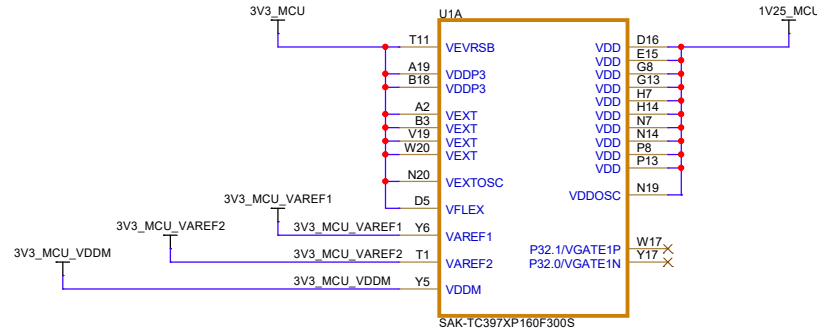
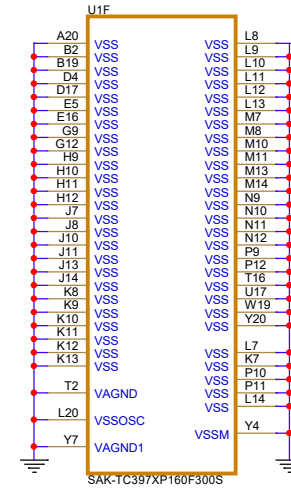
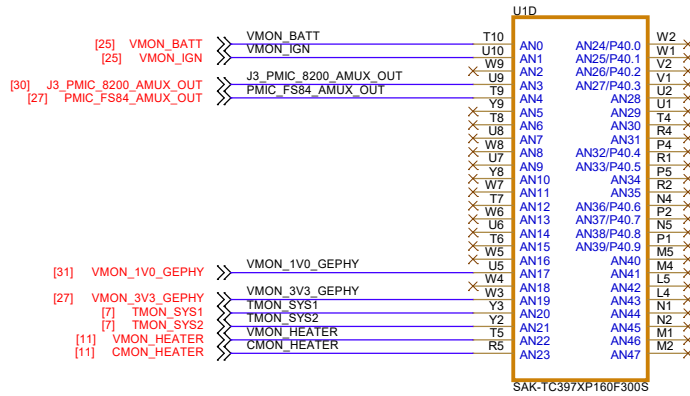
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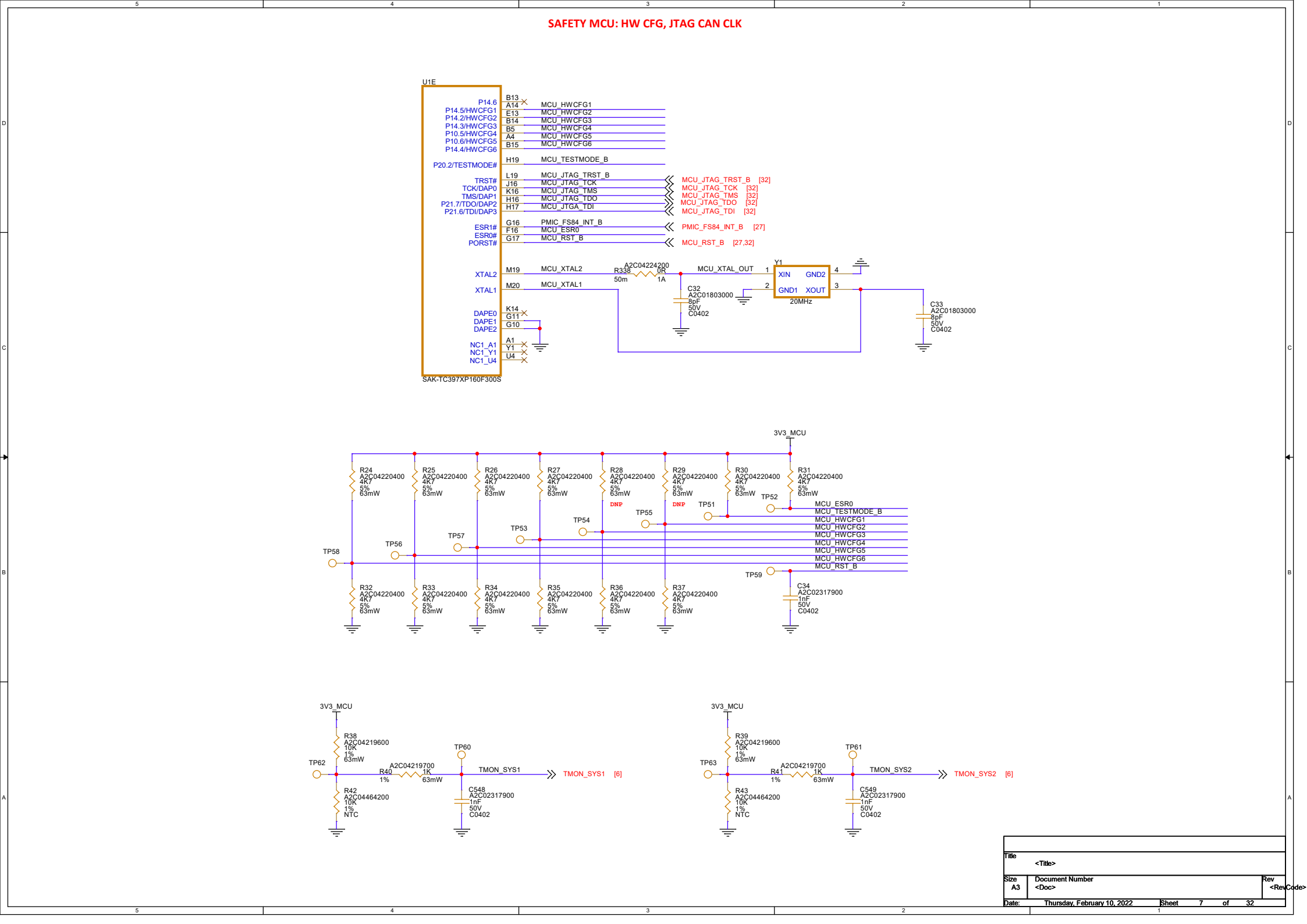
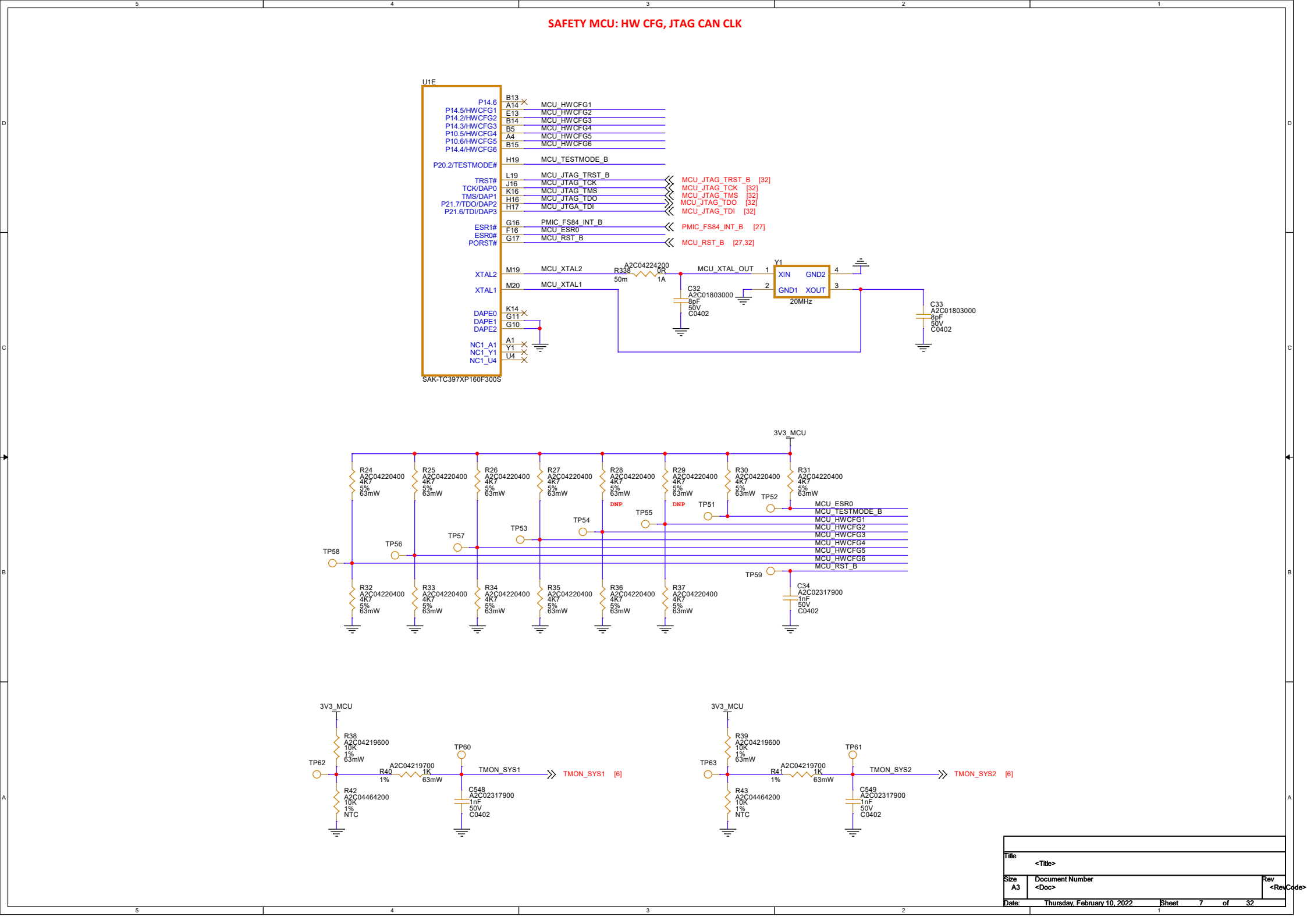
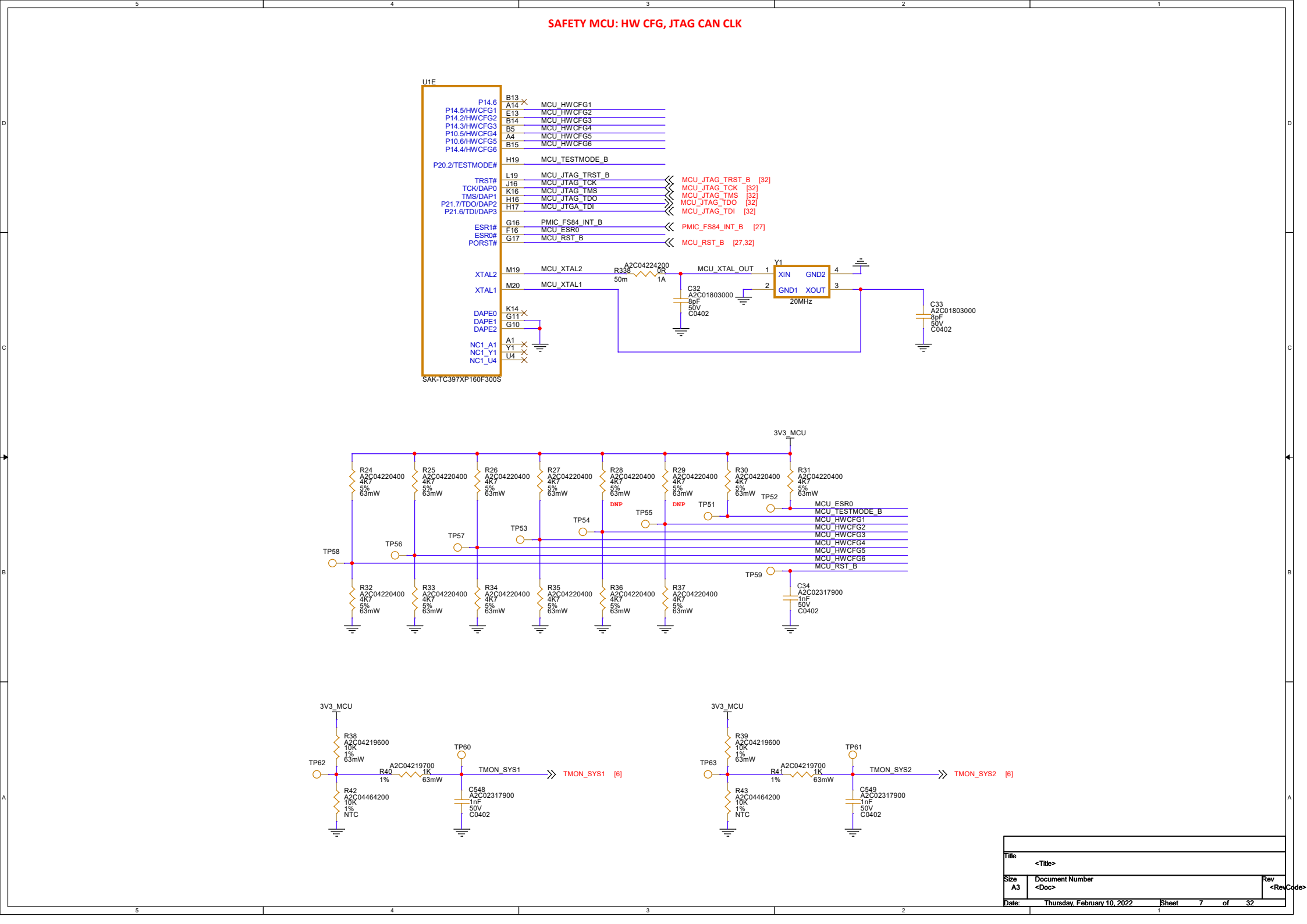
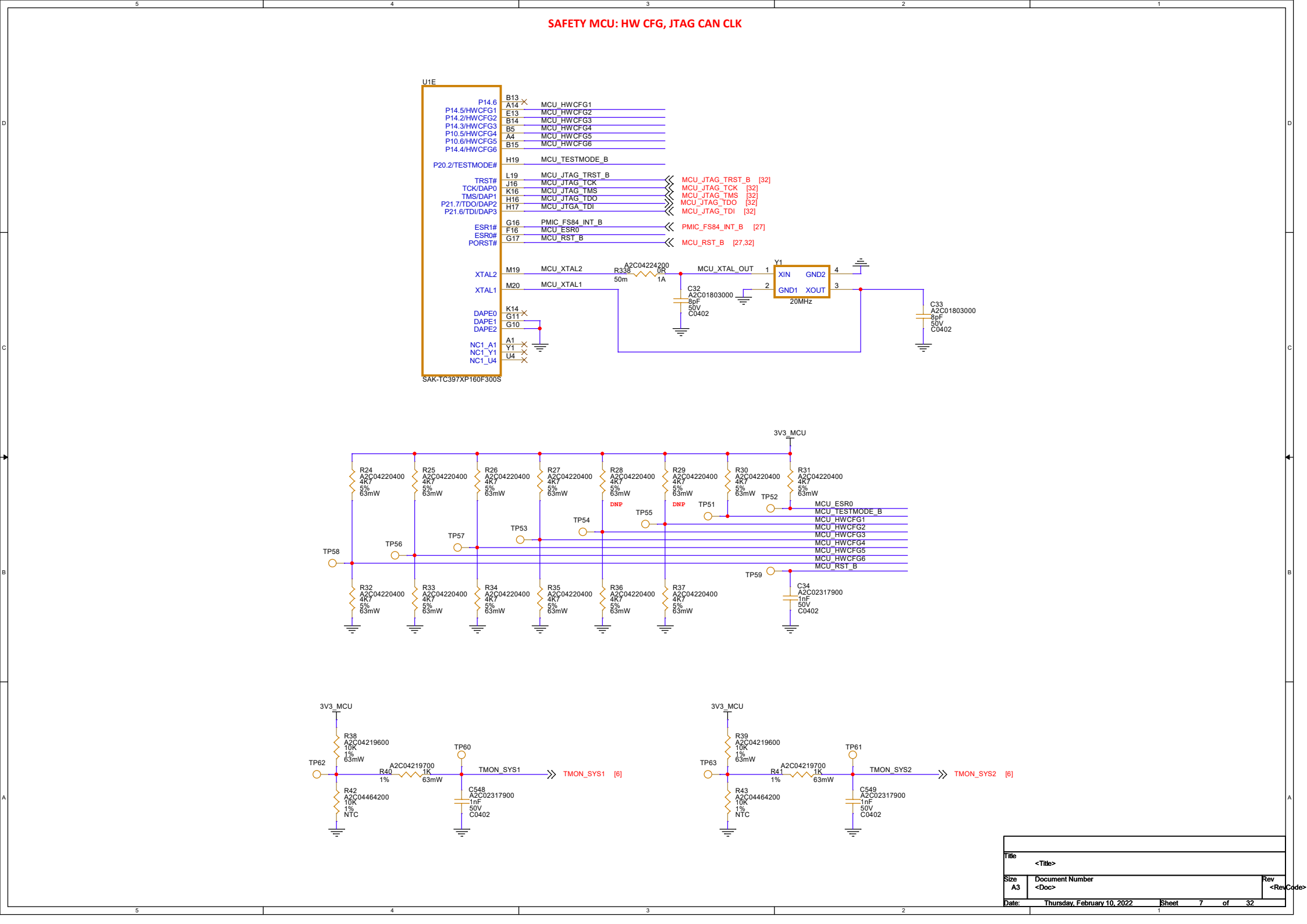
## SAFETY MCU



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## SAFETY MCU: ADC, POWER AND GND



[illegible]

**SAFETY MCU: HW CFG, JTAG CAN CLK**

**U1E**

P14.6 B13 X MCU\_HWCFG1  
P14.5/HWCFG1 A14 X MCU\_HWCFG2  
P14.2/HWCFG2 E13 X MCU\_HWCFG3  
P14.3/HWCFG3 B5 X MCU\_HWCFG4  
P10.5/HWCFG4 A4 X MCU\_HWCFG5  
P10.6/HWCFG5 B15 X MCU\_HWCFG6  
P14.4/HWCFG6 H19 X MCU\_TESTMODE\_B  
P20.2/TESTMODE# L19 X MCU\_JTAG\_TRST\_B [32]  
TRST# J16 X MCU\_JTAG\_TCK [32]  
TCK/DAPO K16 X MCU\_JTAG\_TMS [32]  
TMS/DAPI H16 X MCU\_JTAG\_TDO [32]  
P21.7/TDO/DAPI H17 X MCU\_JTAG\_TDI [32]  
P21.6/TDI/DAP3 G16 X PMIC\_FS84\_INT\_B [27]  
ESR1# F16 X MCU\_ESR0  
ESR0# G17 X MCU\_RST\_B [27,32]  
PORST#

MCU\_XTAL2 M19 X R338 A2C04224200 0R 1A  
MCU\_XTAL1 M20 X C32 A2C01803000 8pF 50V C0402  
DAPE0 K14 X  
DAPE1 G11 X  
DAPE2 G10 X  
NC1\_A1 A1 X  
NC1\_Y1 Y1 X  
NC1\_U4 U4 X

MCU\_XTAL\_OUT Y1 XIN GND2 4  
GND1 XOUT 3

20MHz

C33 A2C01803000 8pF 50V C0402

SAK-TC397XP160F300S

3V3\_MCU

R24 A2C04220400 4K7 5% 63mW  
R25 A2C04220400 4K7 5% 63mW  
R26 A2C04220400 4K7 5% 63mW  
R27 A2C04220400 4K7 5% 63mW  
R28 A2C04220400 4K7 5% 63mW  
R29 A2C04220400 4K7 5% 63mW  
R30 A2C04220400 4K7 5% 63mW  
R31 A2C04220400 4K7 5% 63mW  
R32 A2C04220400 4K7 5% 63mW  
R33 A2C04220400 4K7 5% 63mW  
R34 A2C04220400 4K7 5% 63mW  
R35 A2C04220400 4K7 5% 63mW  
R36 A2C04220400 4K7 5% 63mW  
R37 A2C04220400 4K7 5% 63mW

TP58 TP56 TP57 TP53 TP54 TP55 TP51 TP52 TP59

MCU\_ESR0  
MCU\_TESTMODE\_B  
MCU\_HWCFG1  
MCU\_HWCFG2  
MCU\_HWCFG3  
MCU\_HWCFG4  
MCU\_HWCFG5  
MCU\_HWCFG6  
MCU\_RST\_B

C34 A2C02317900 1nF 50V C0402

3V3\_MCU

R38 A2C04219600 10K 1% 63mW  
R40 A2C04219700 1K 1% 63mW  
R42 A2C04464200 10K 1% NTC  
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TP62 TP60 TMON\_SYS1 TMON\_SYS1 [6]

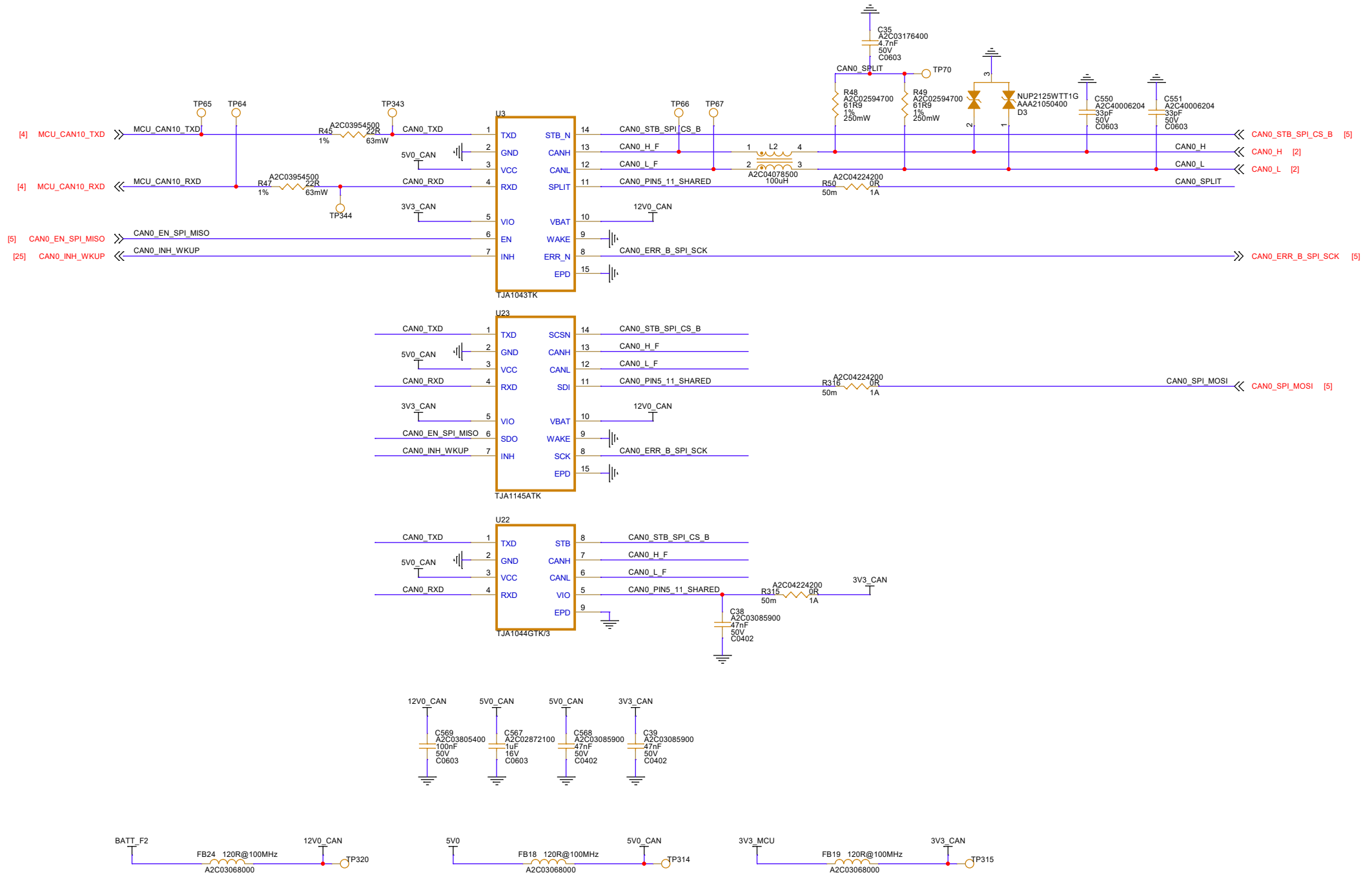
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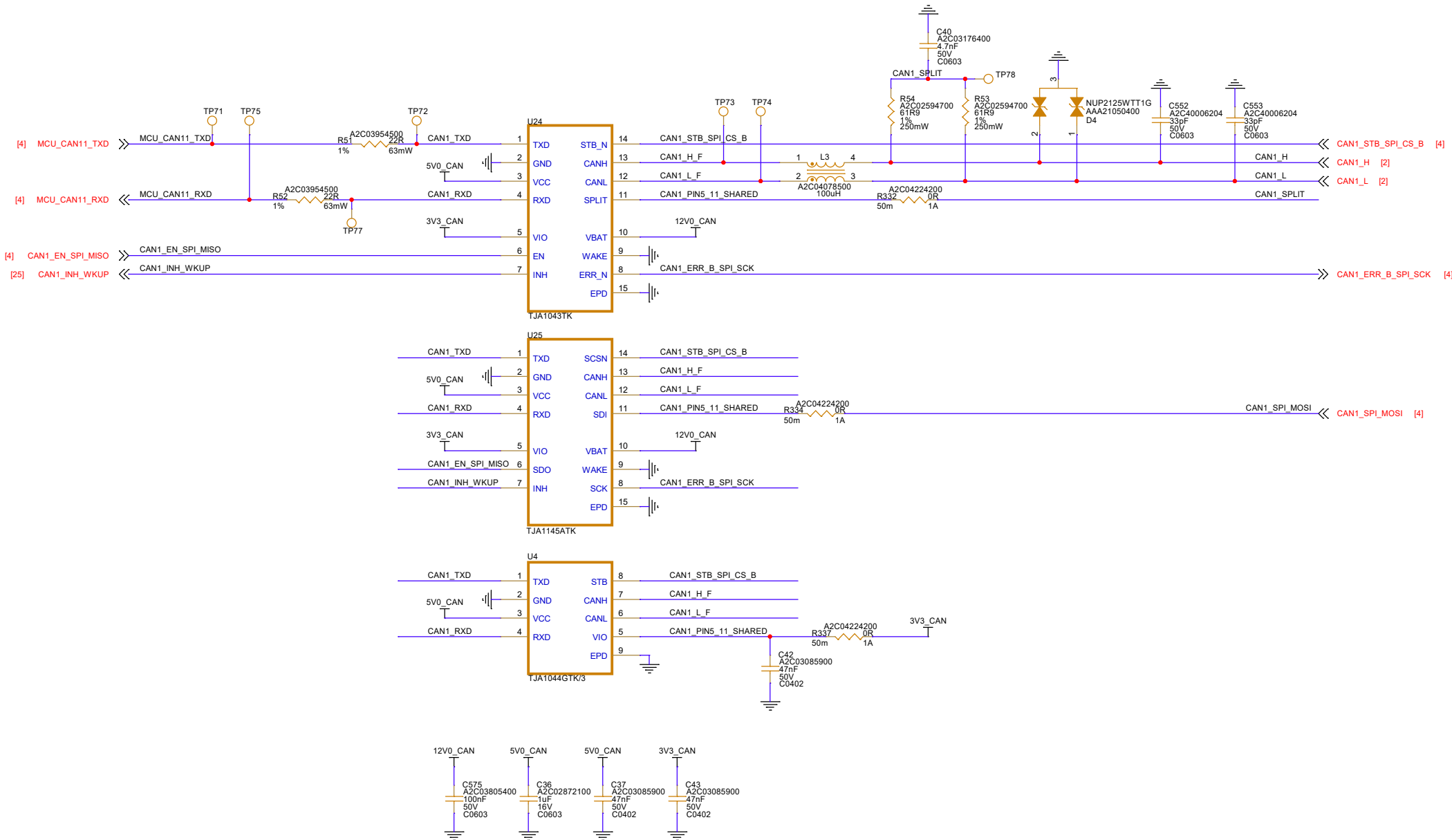
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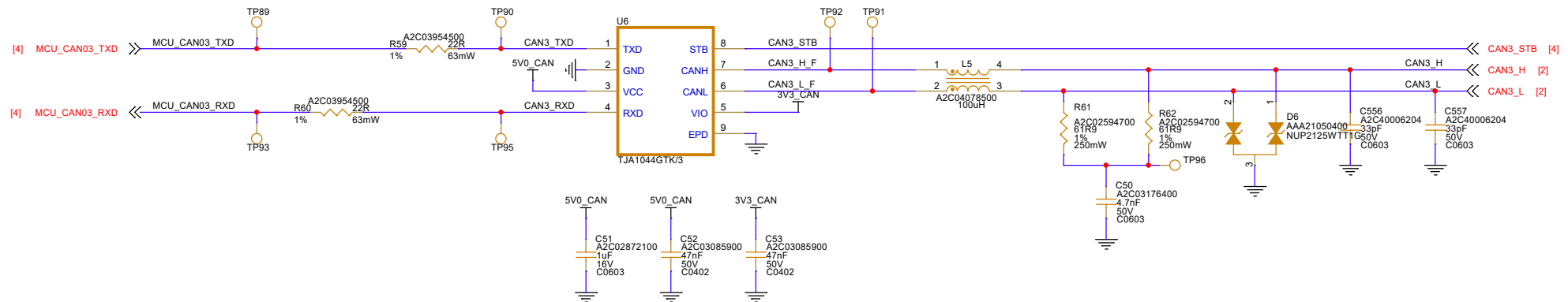
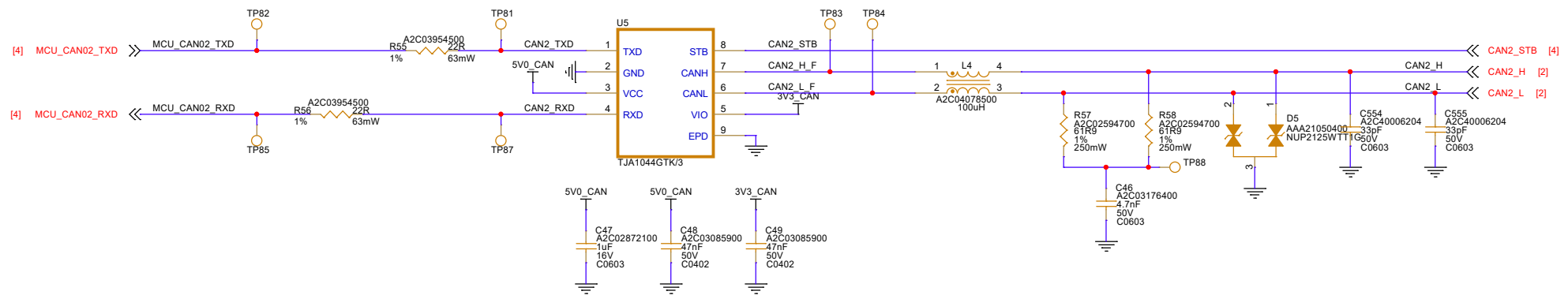
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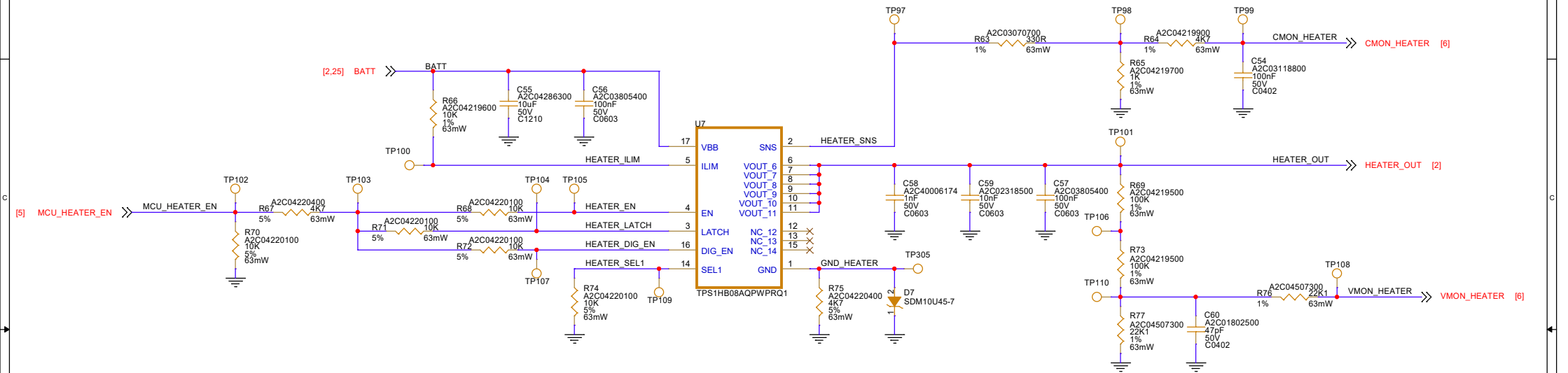


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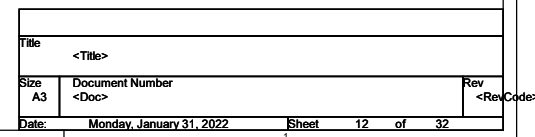
## HEATER



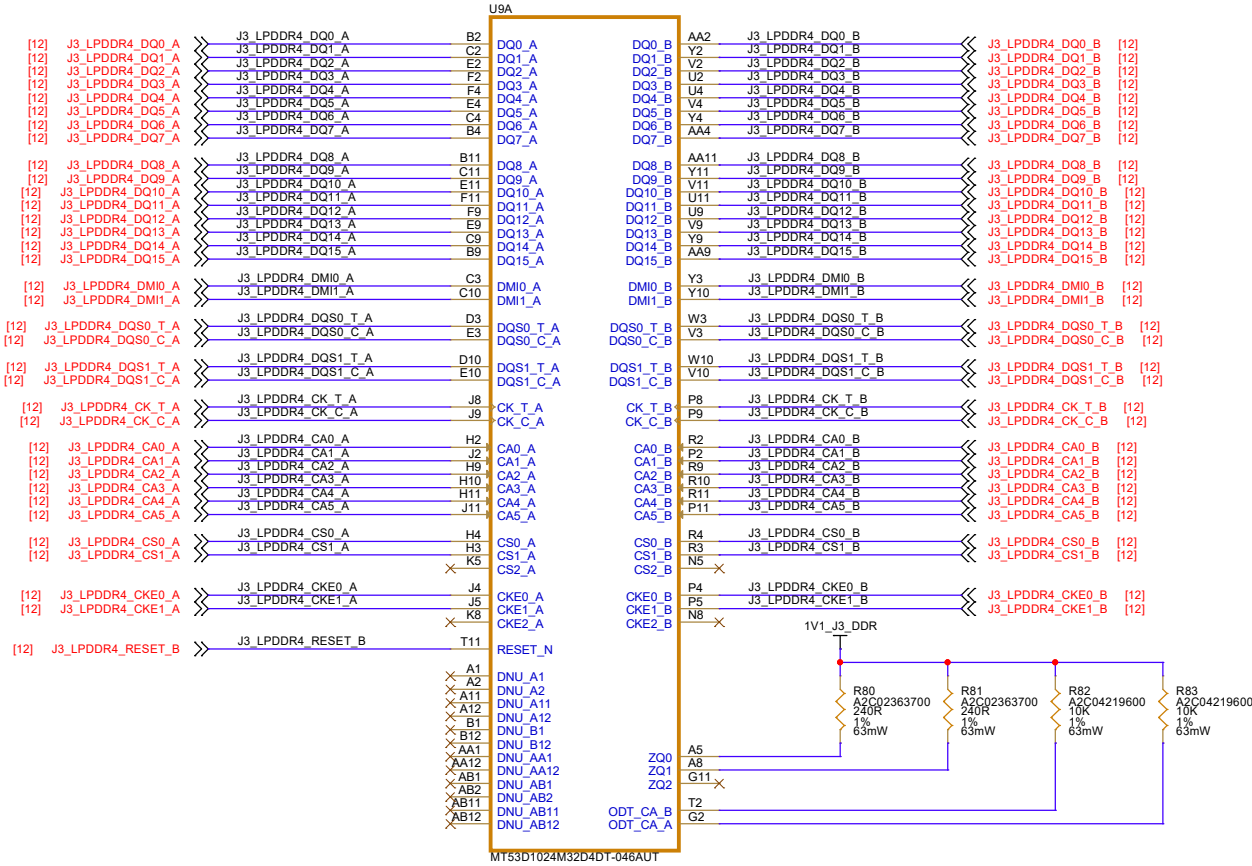
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## U8A

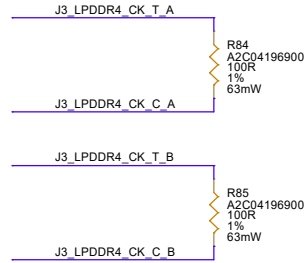
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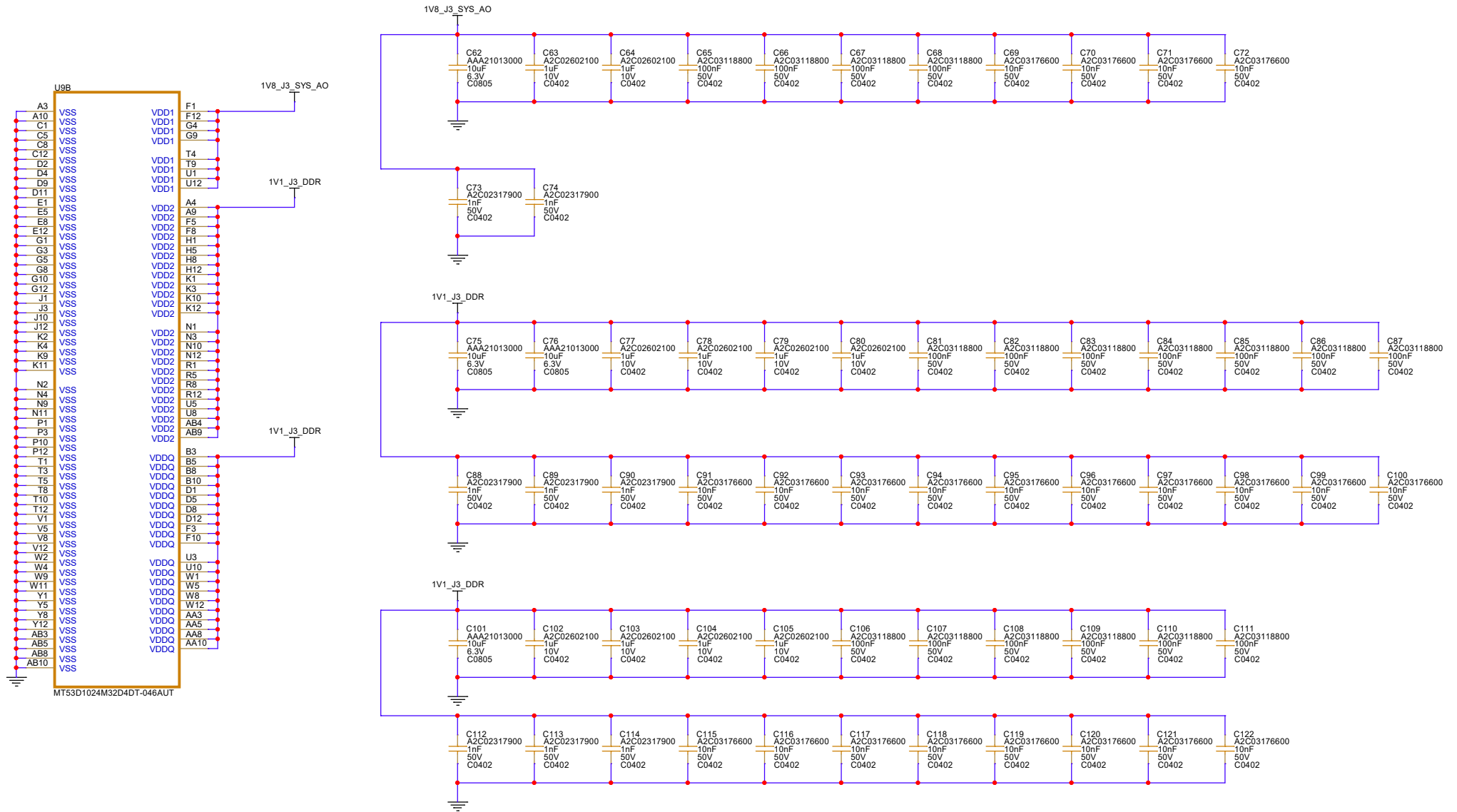


MT53D1024M32B4DT-048AUT



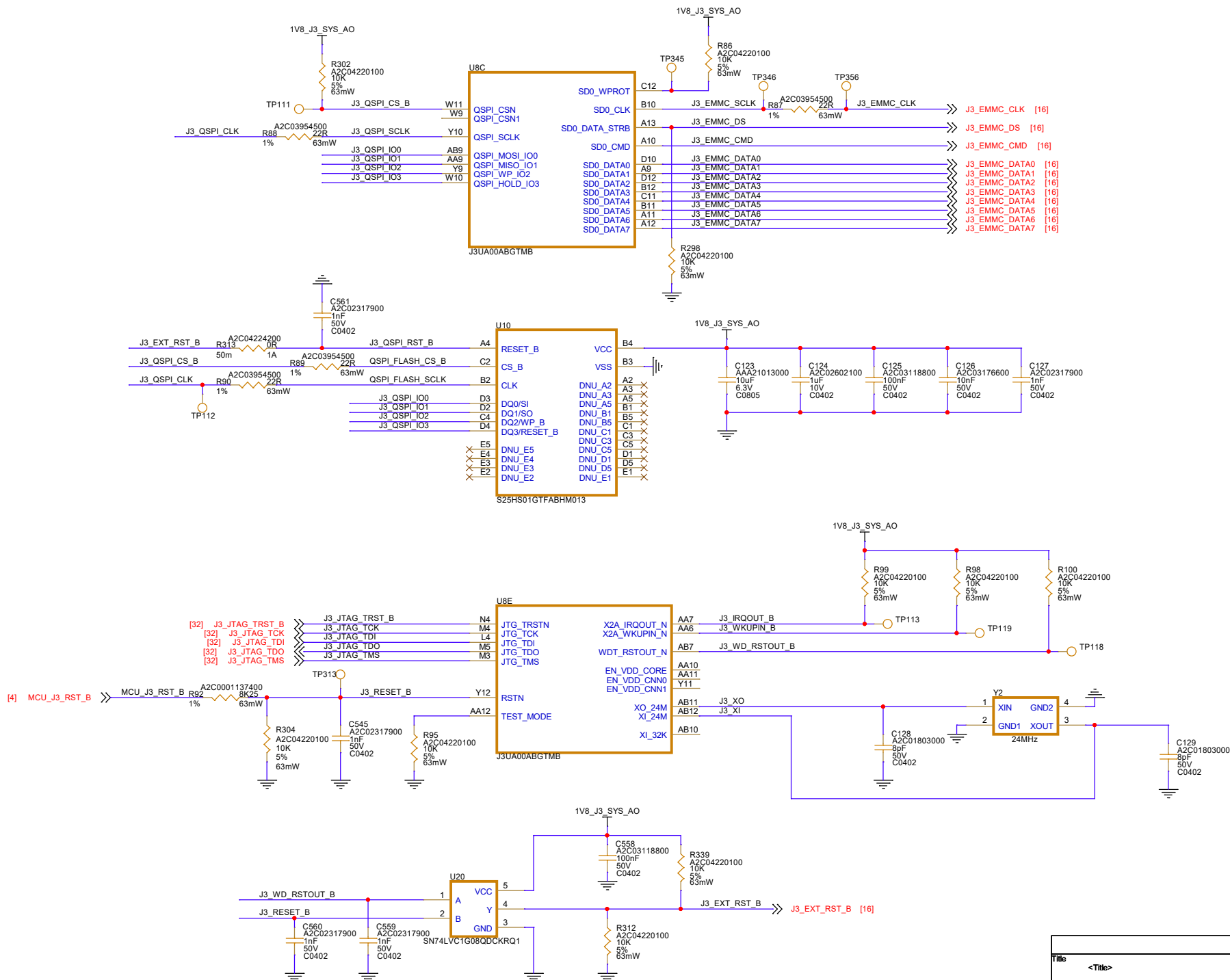
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LPDDR4



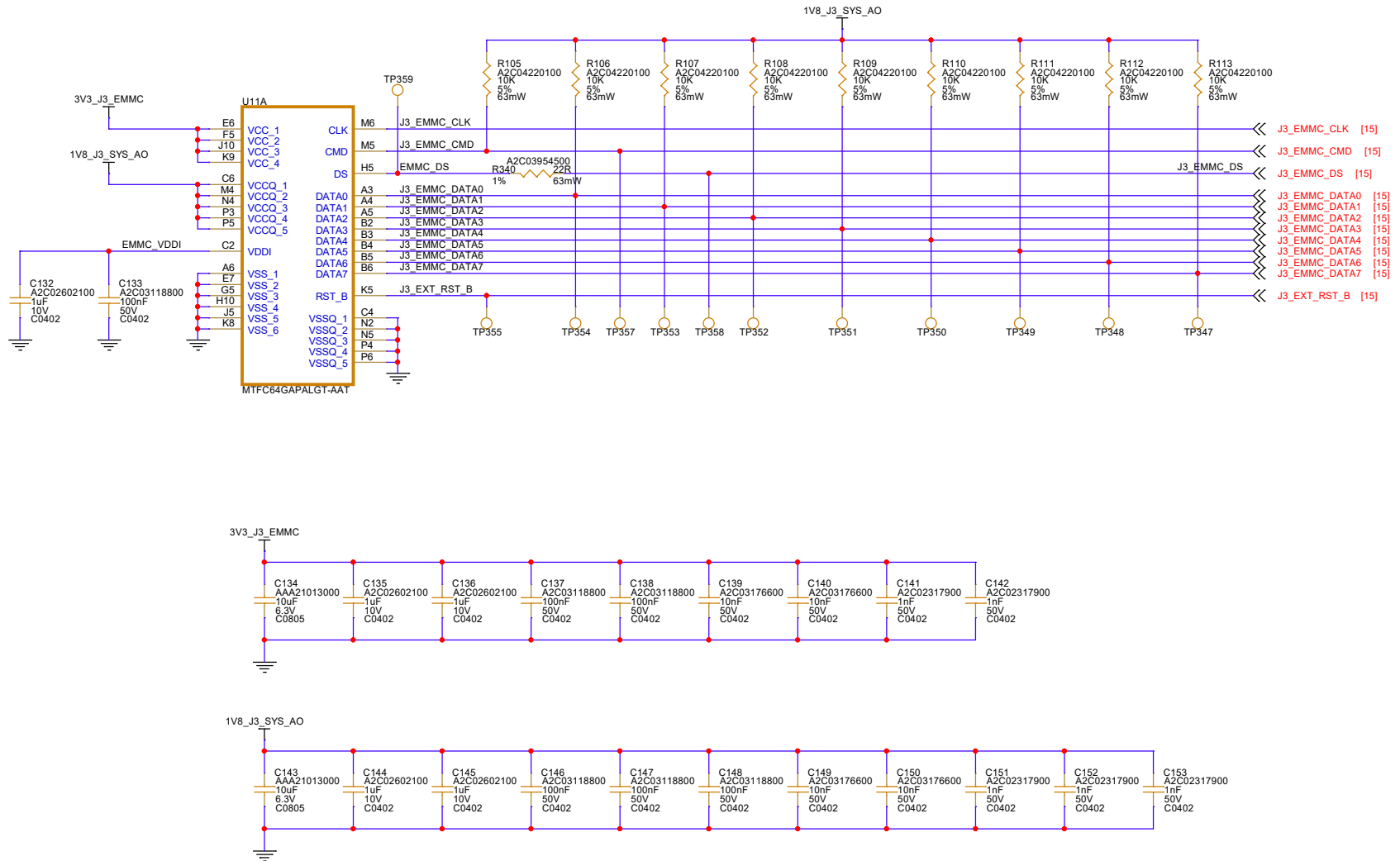
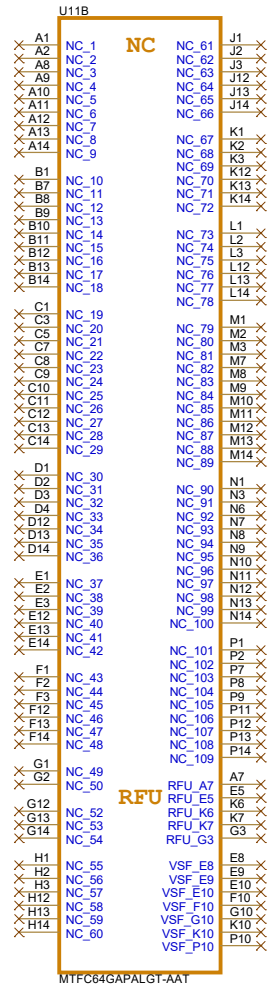
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### J3 QSPI, EMMC, JTAG AND CLOCK



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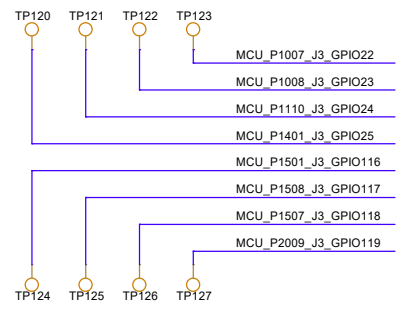
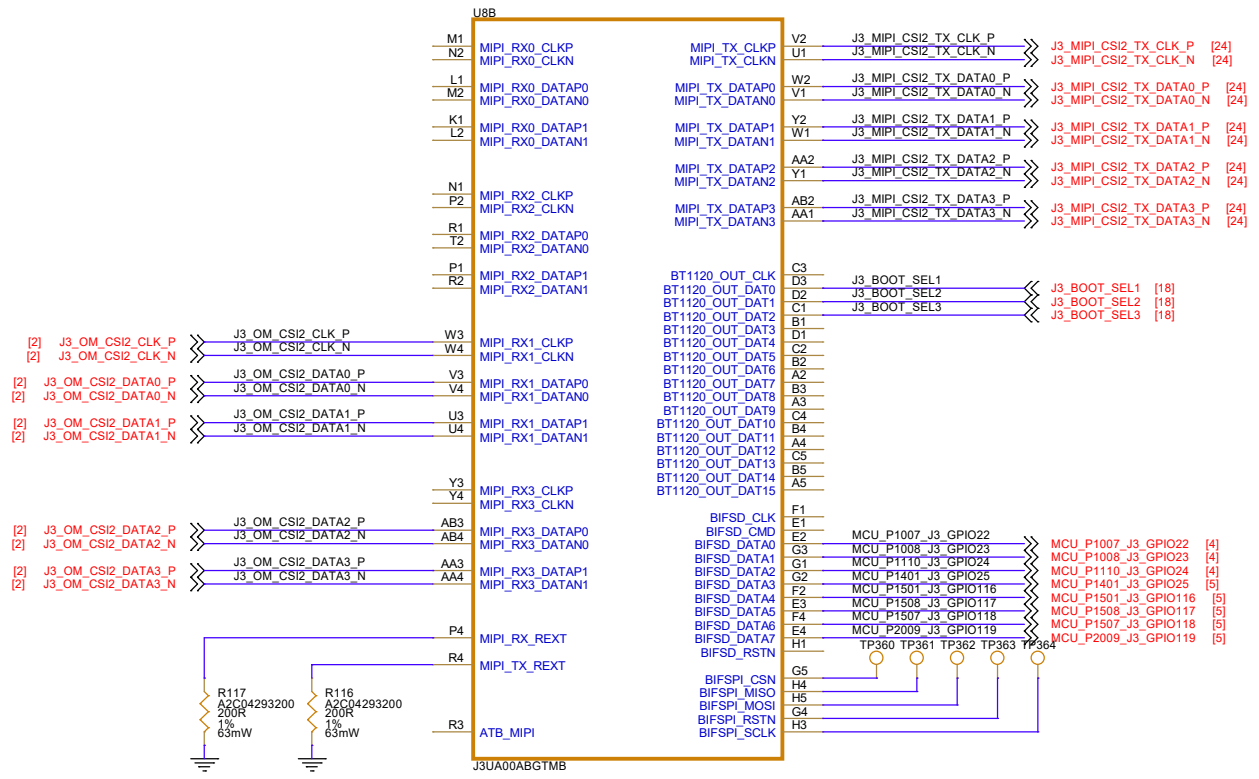
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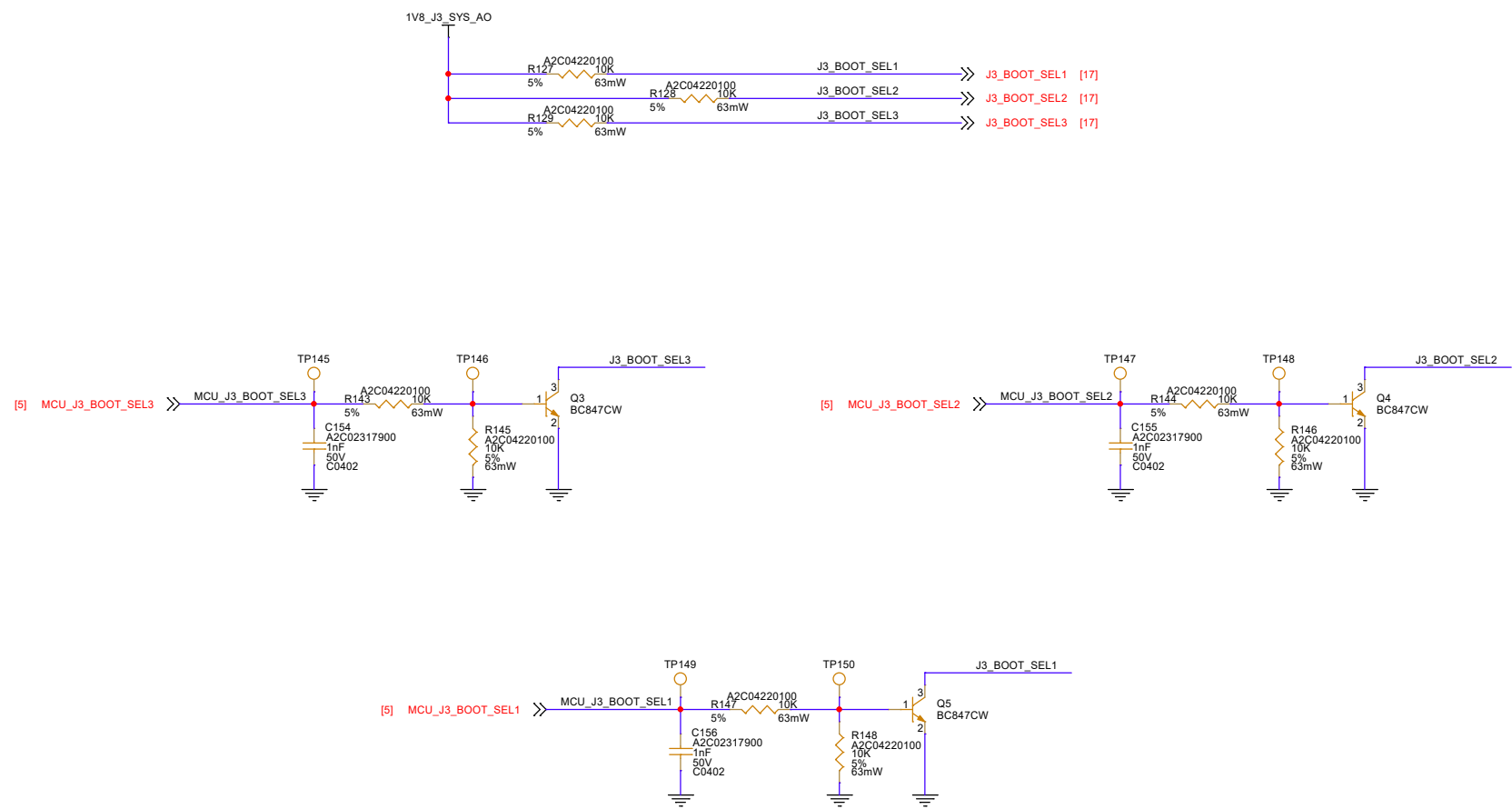


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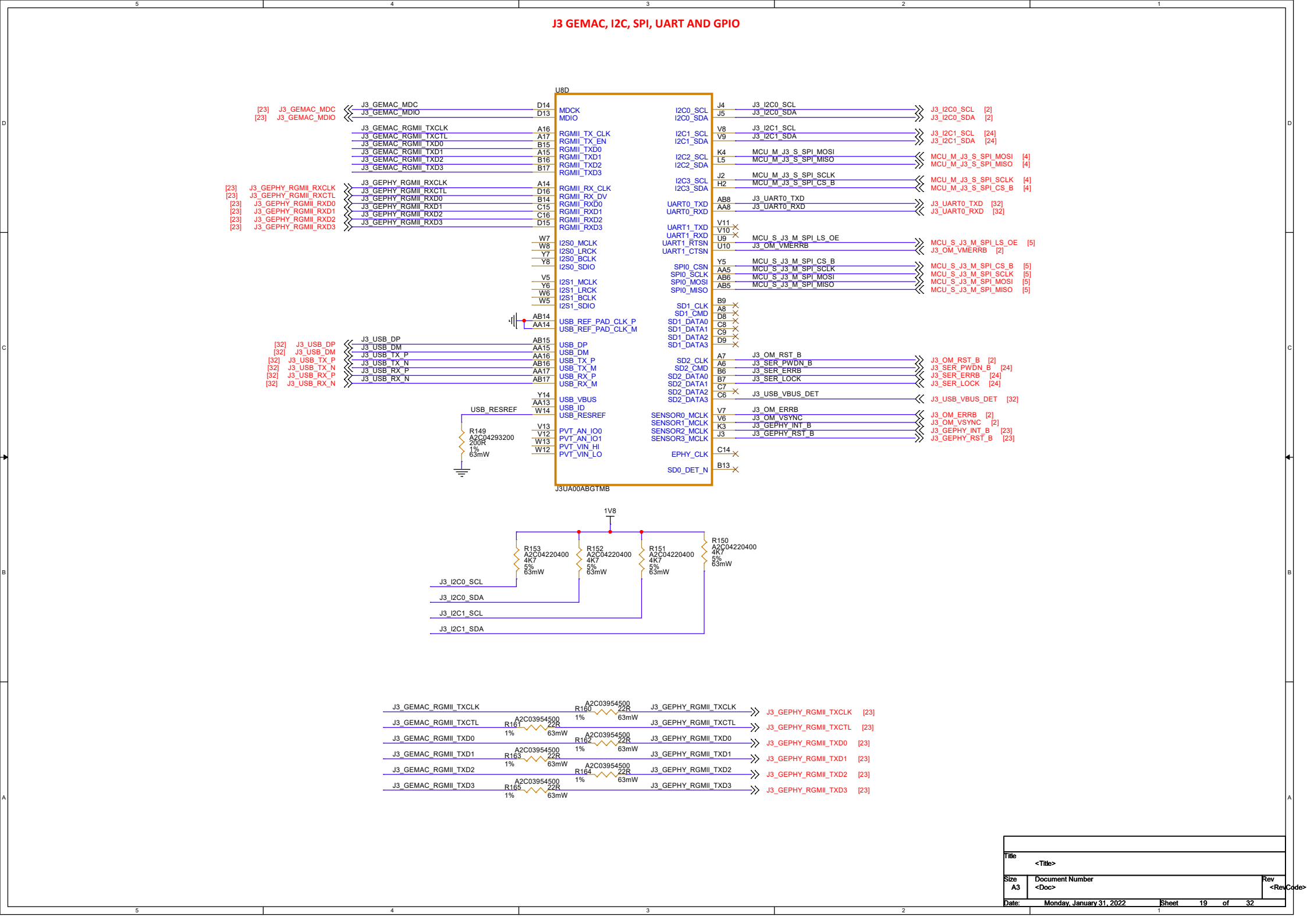
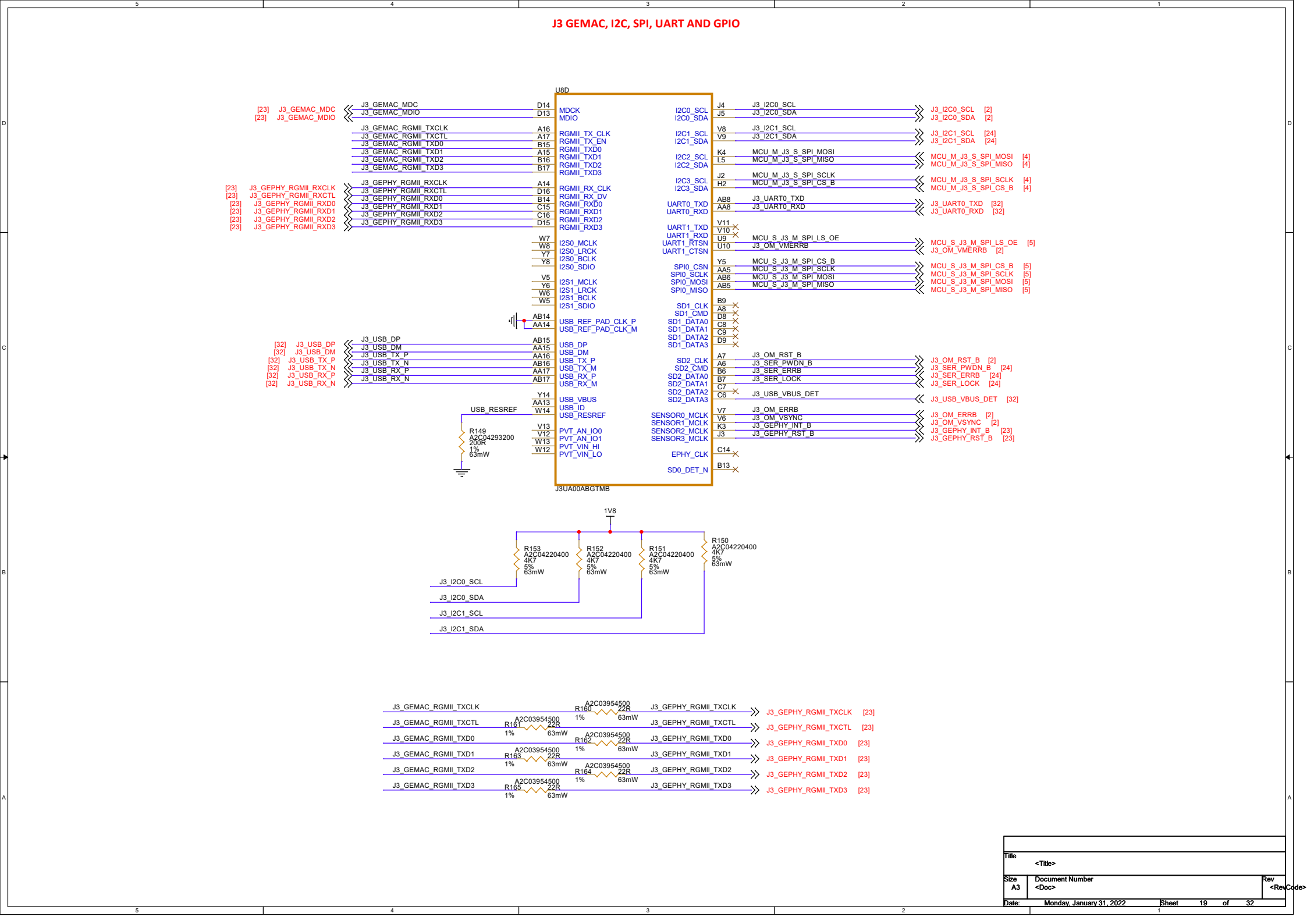


J3 MIPI CSI2 TX, RX BOOT CONFIG

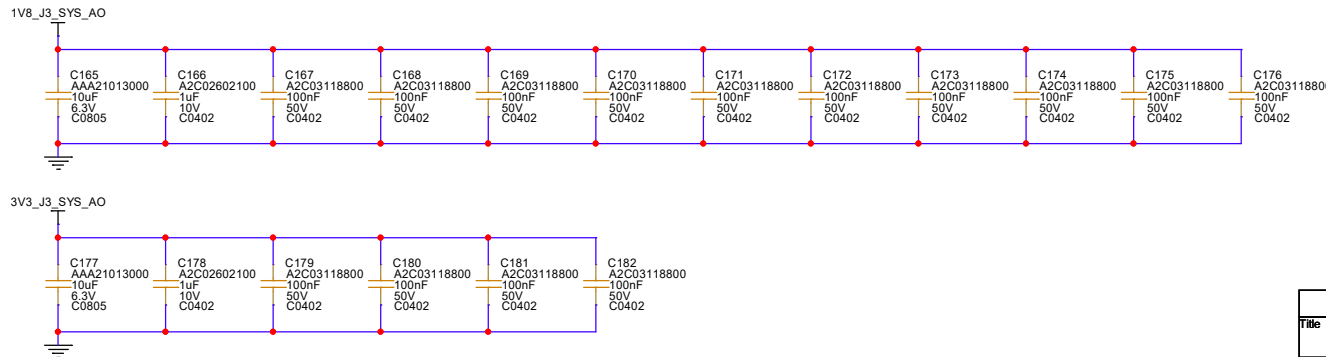
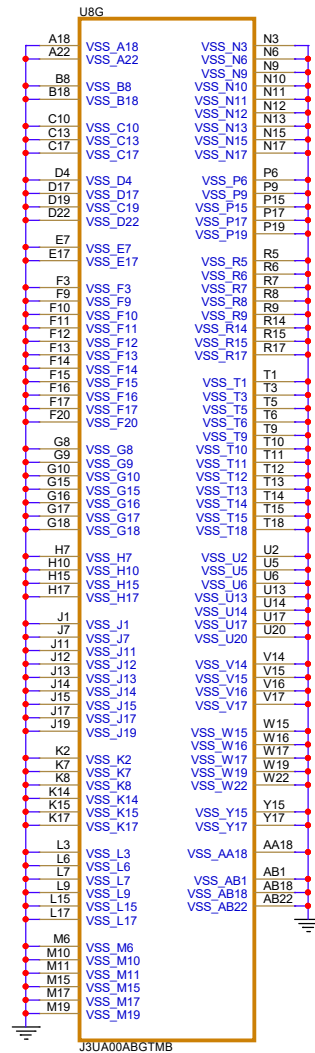
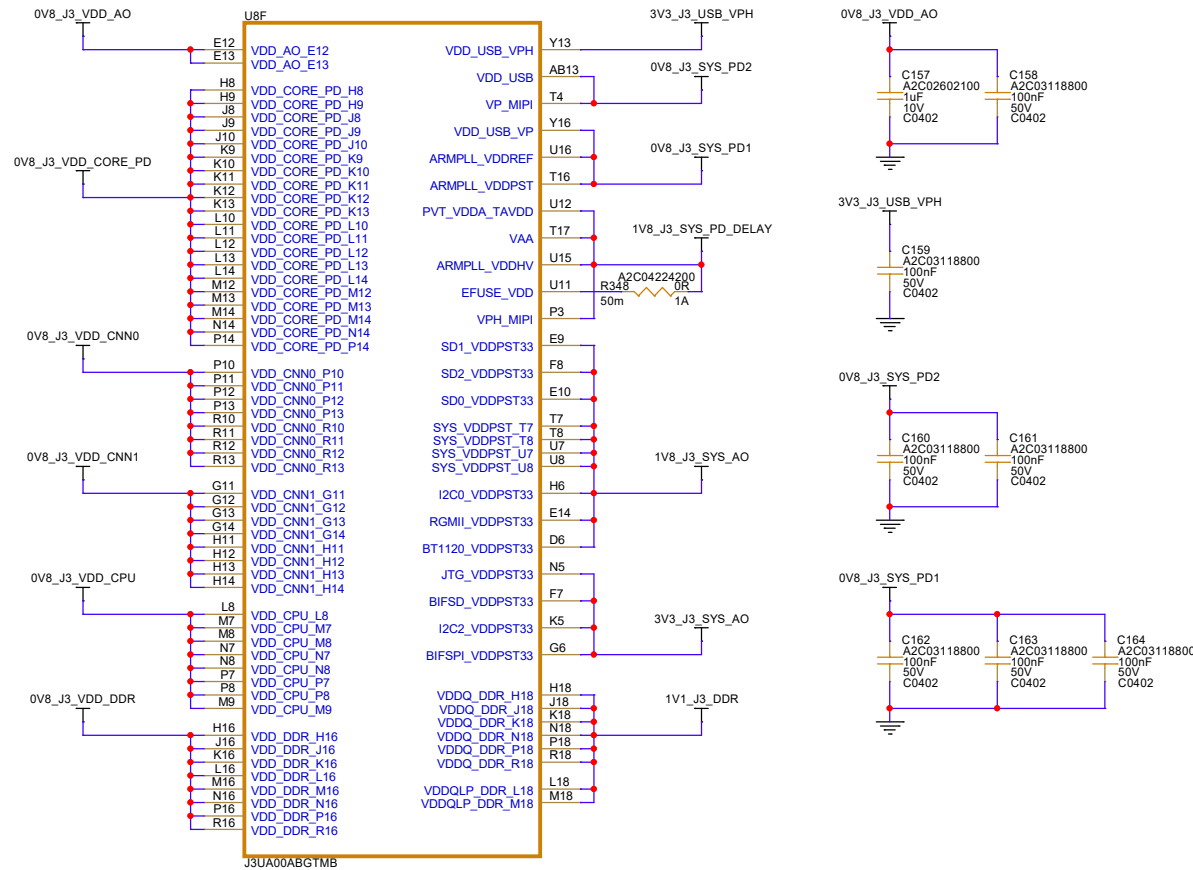




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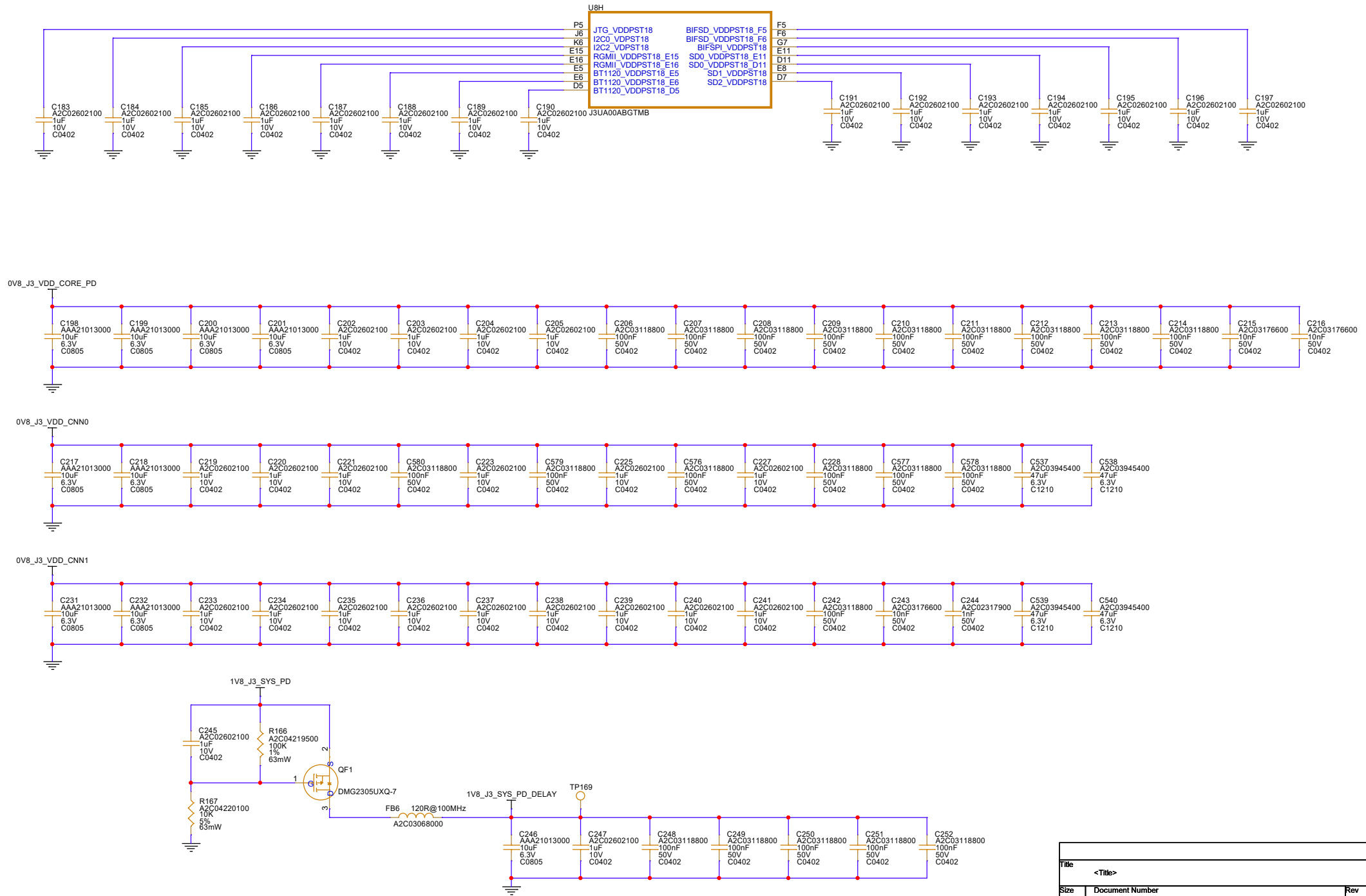
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J3 POWER



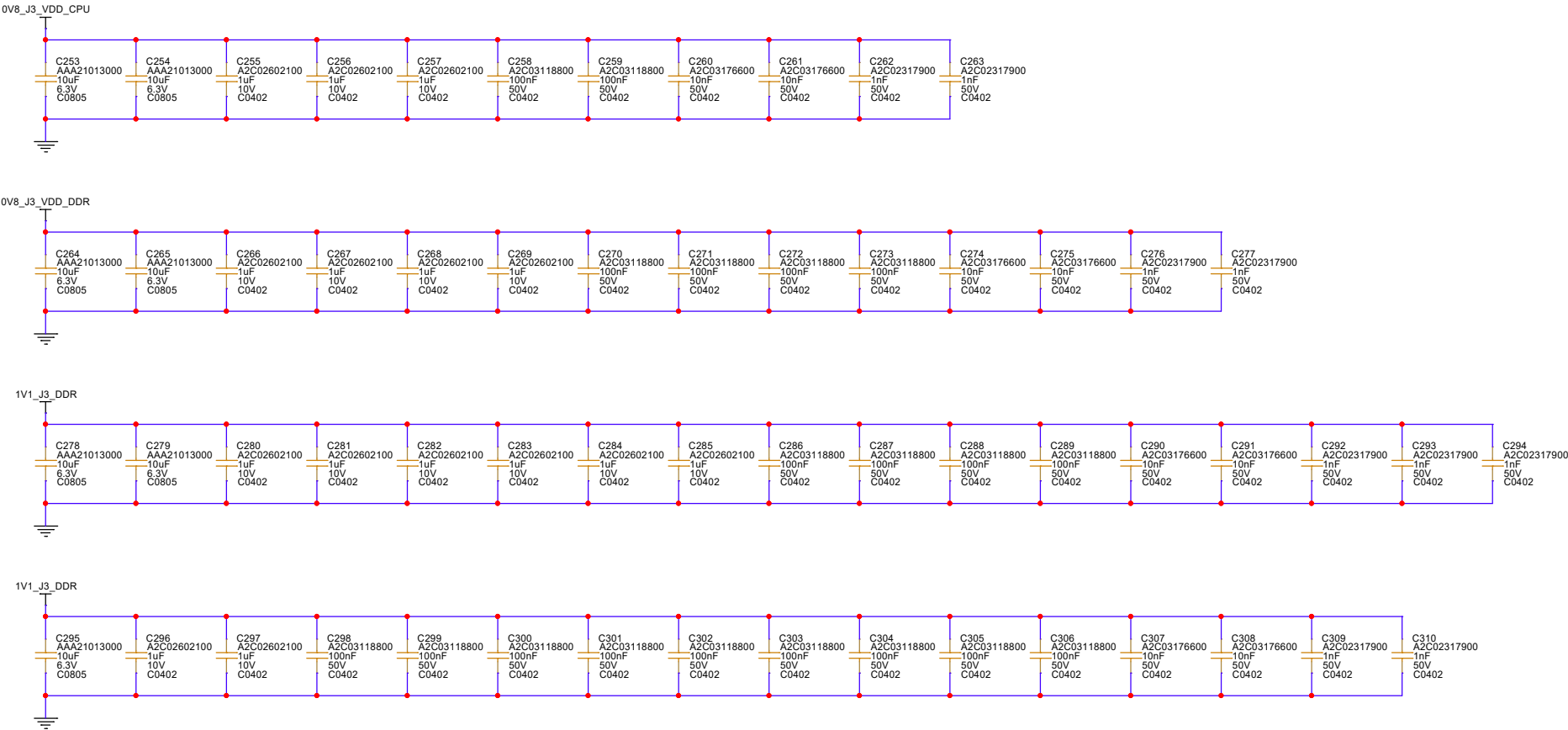
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### J3 POWER

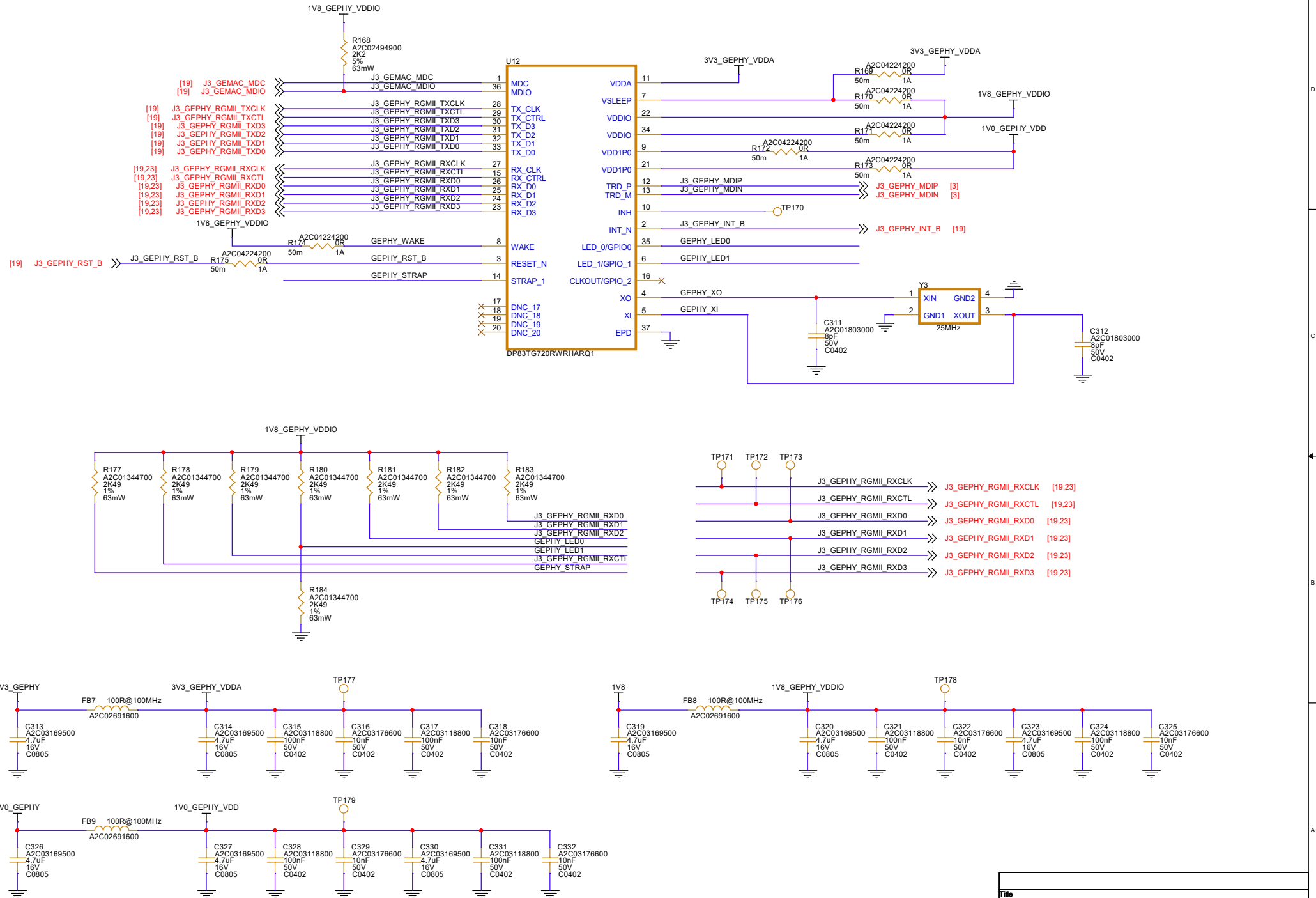


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J3 POWER

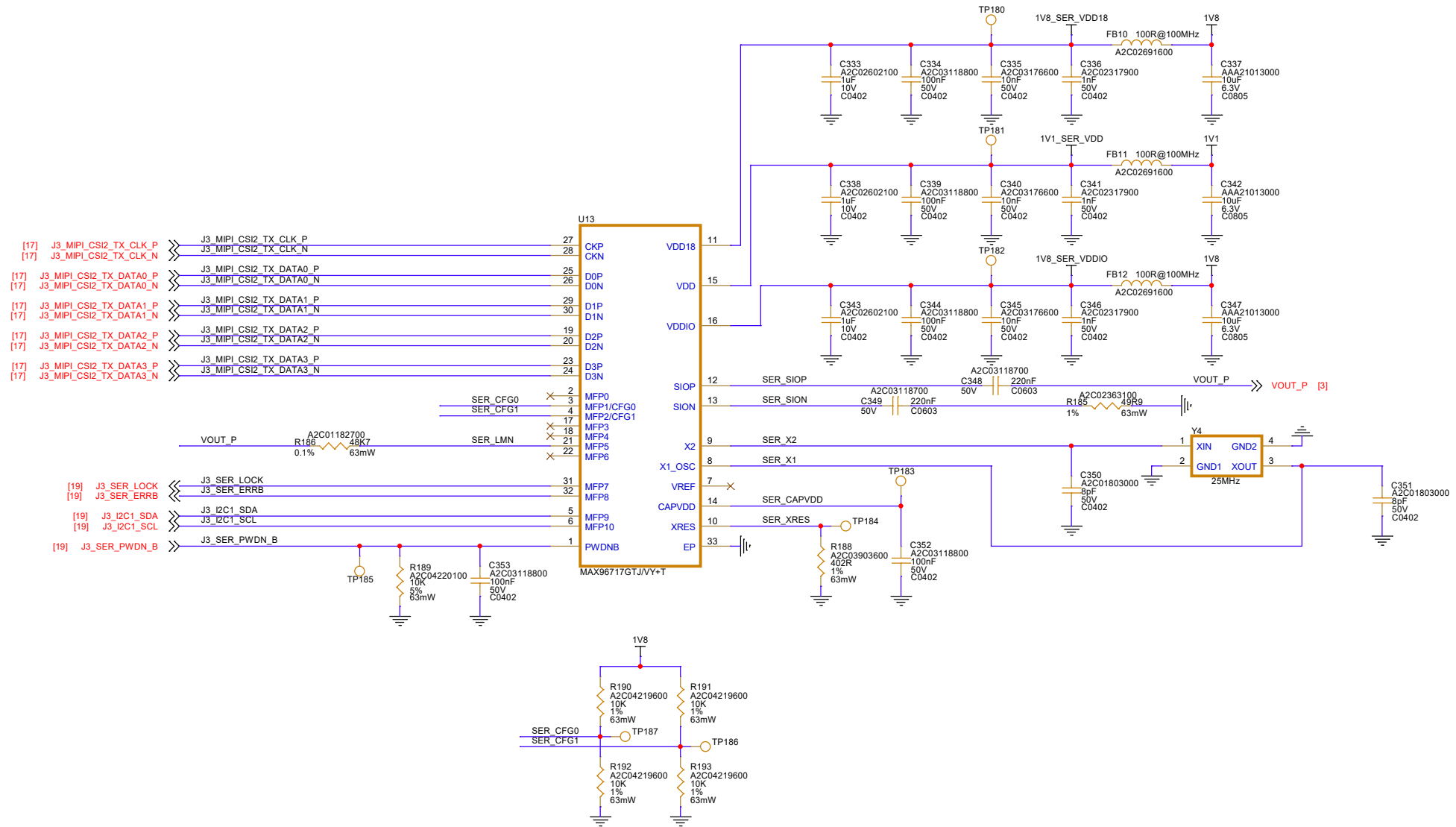


# 1000BASE-T1 PHY



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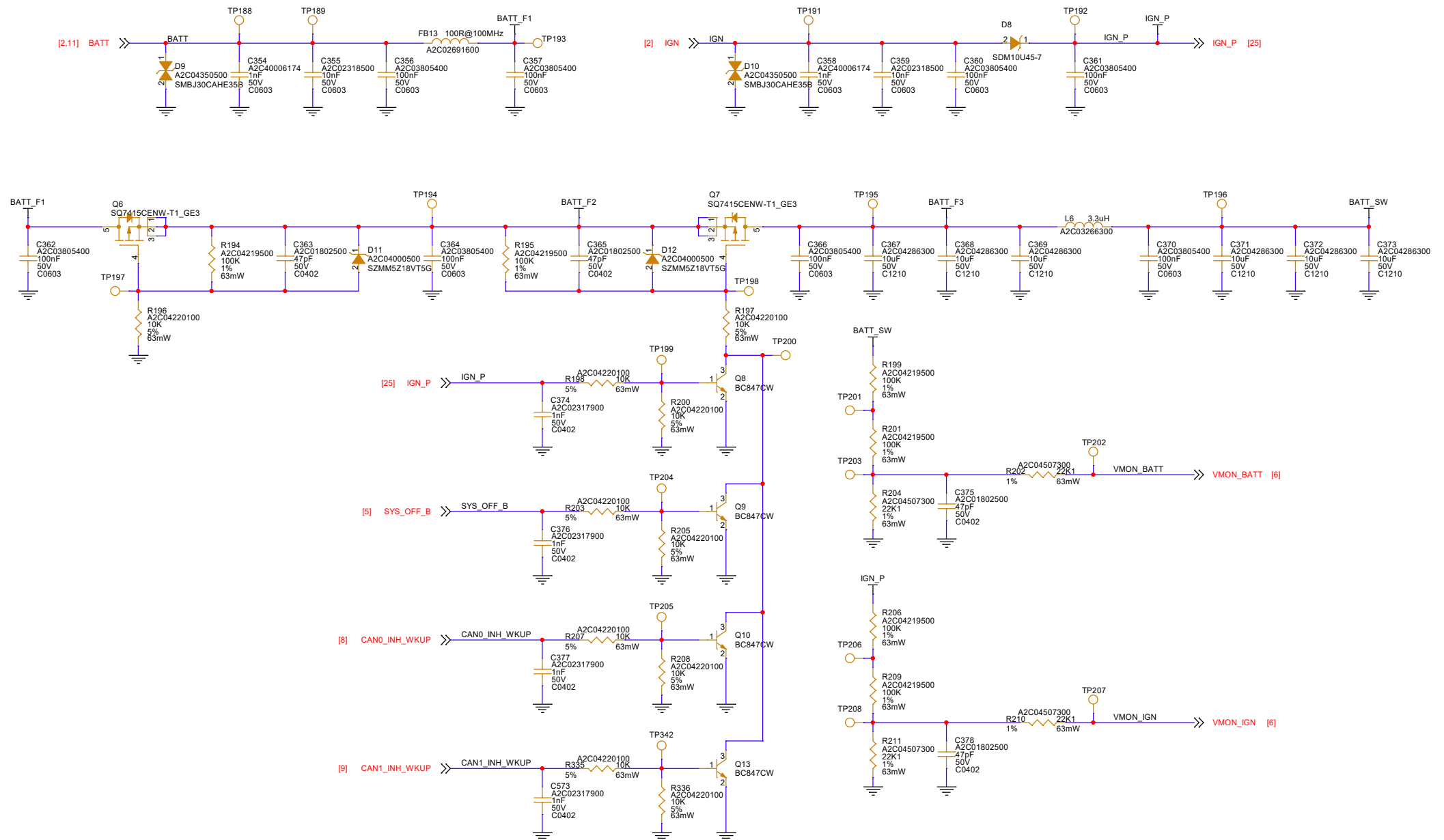
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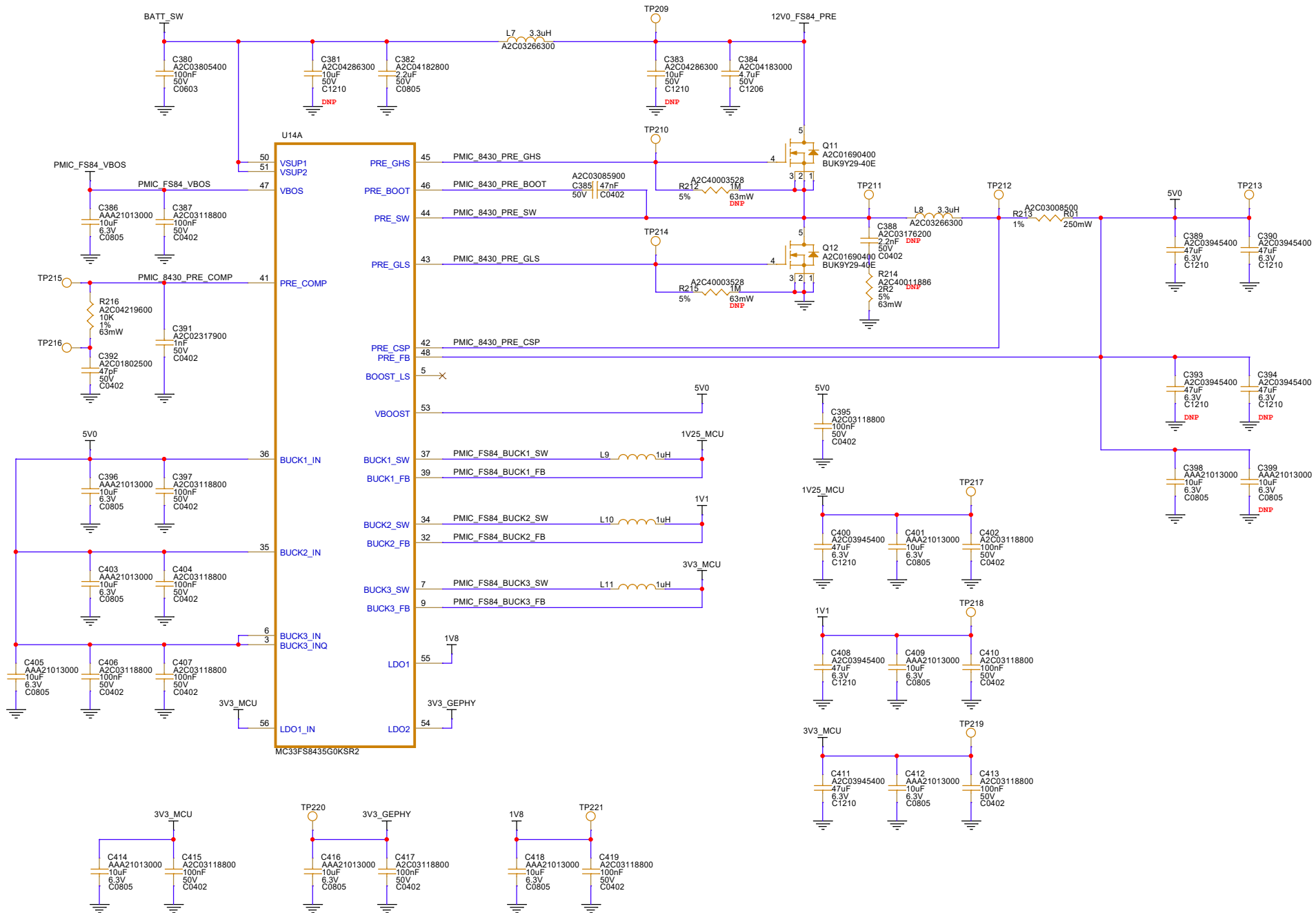


## BATT FILTER AND CONTROL



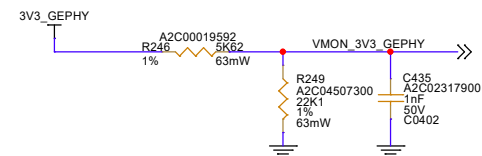
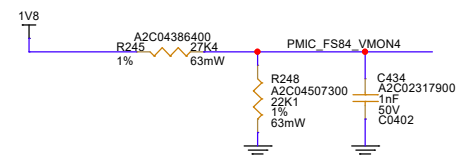
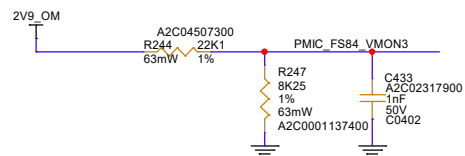
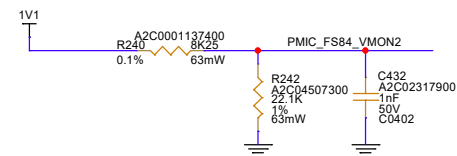
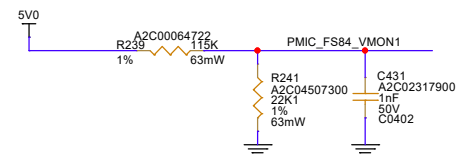
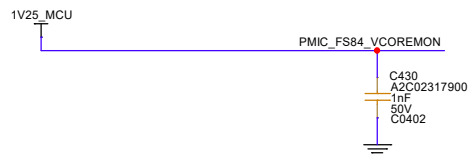
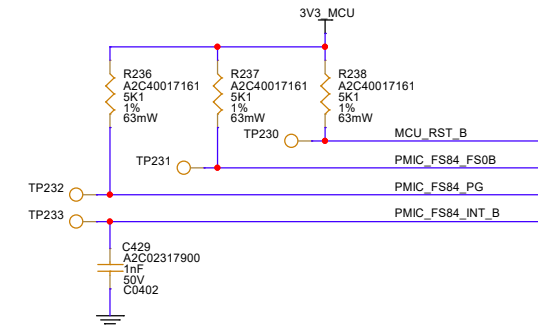
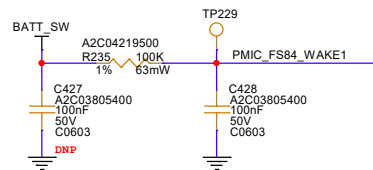
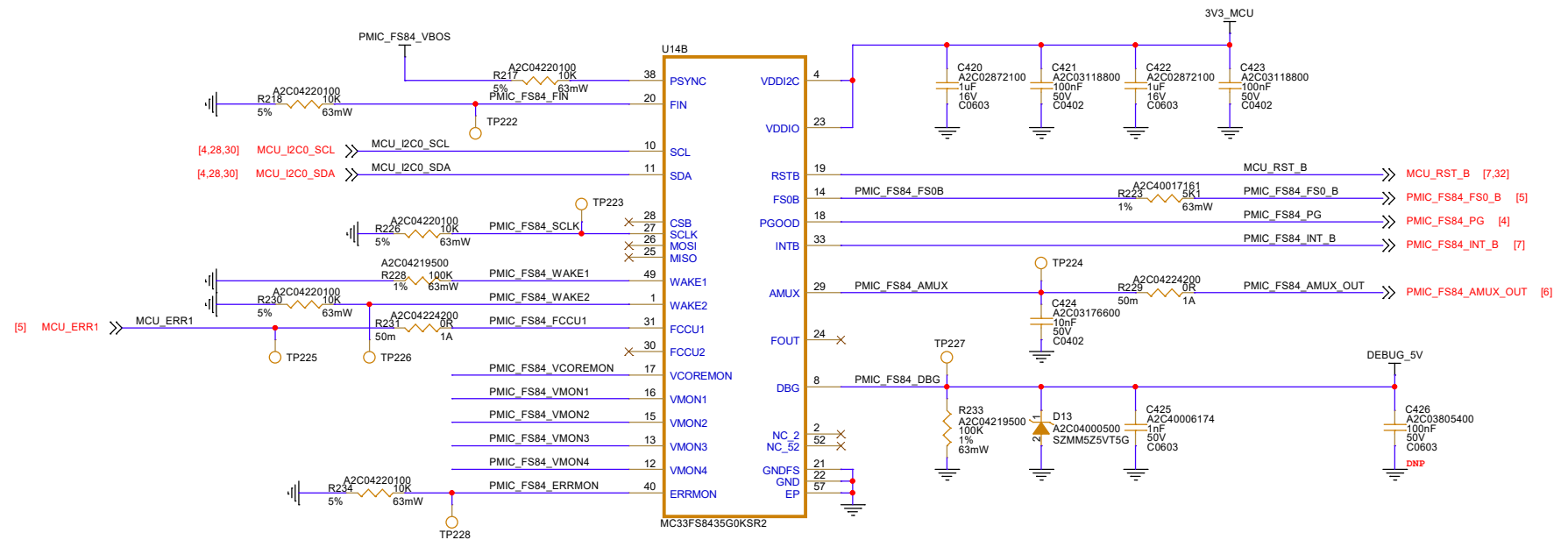
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# 1ST PWR FS84



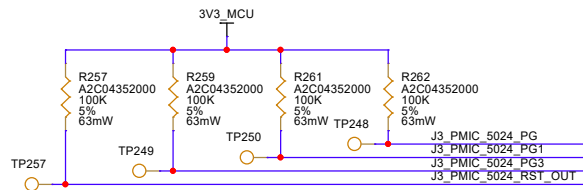
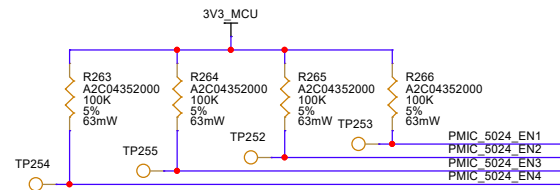
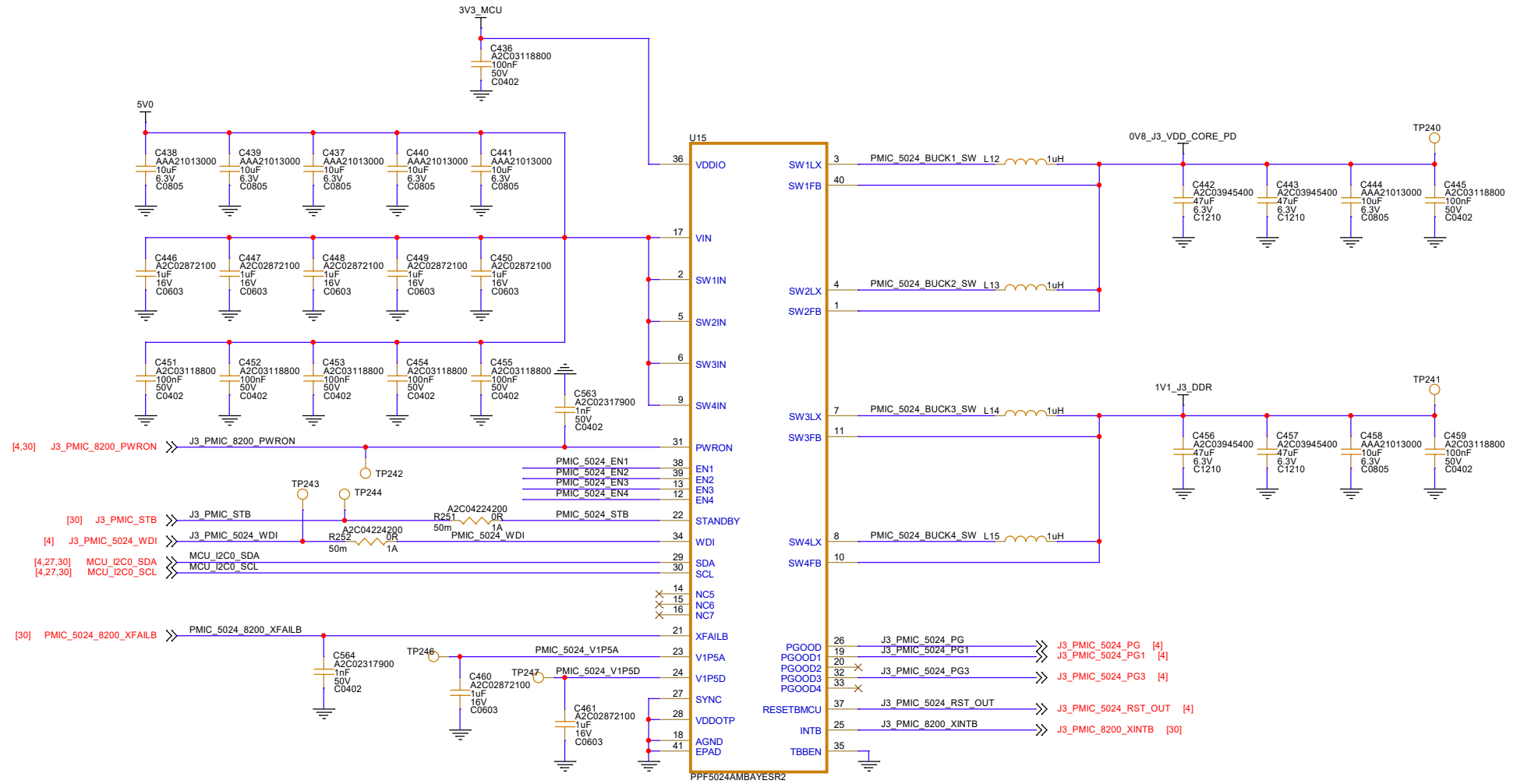
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## 1ST PWR FS84



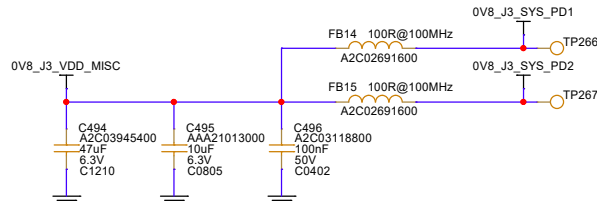
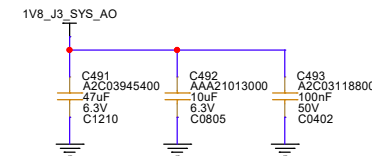
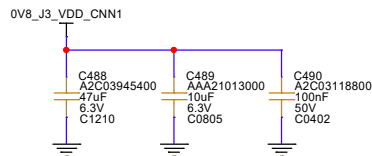
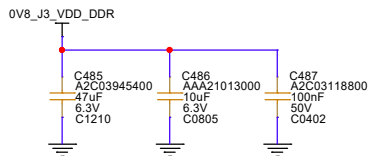
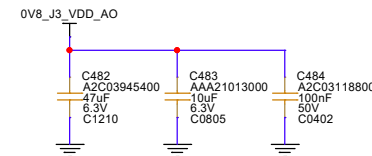
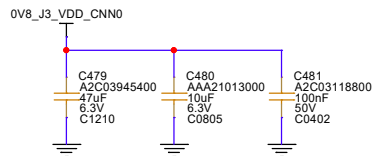
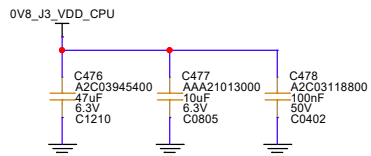
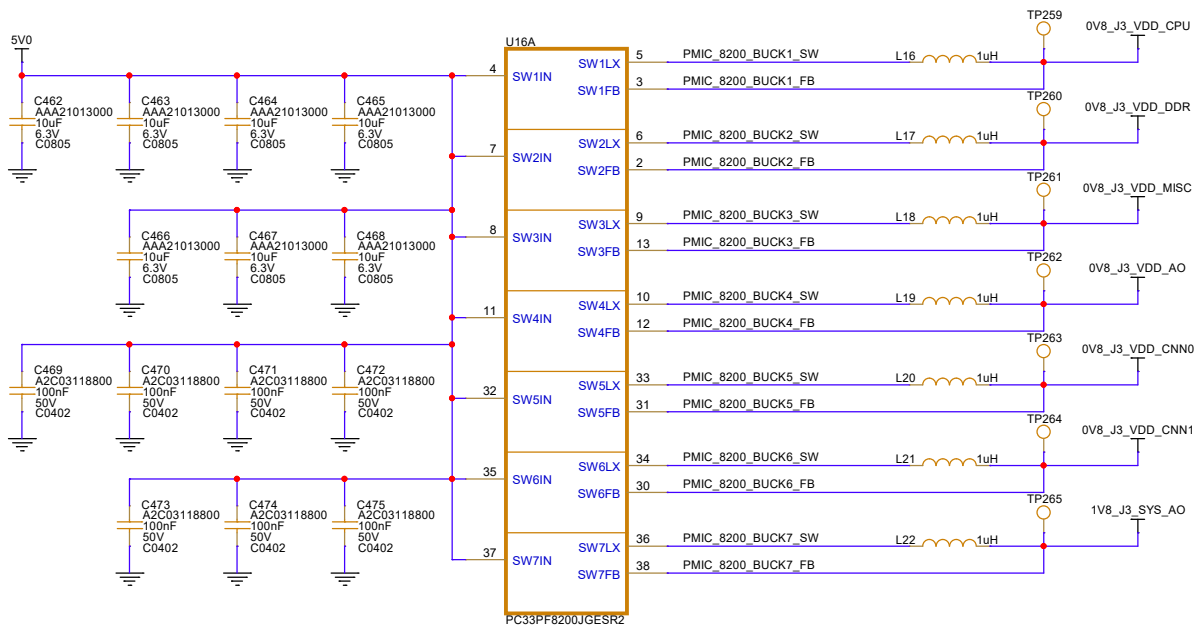
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## 2ND STAGE POWER SOURCES PF5024



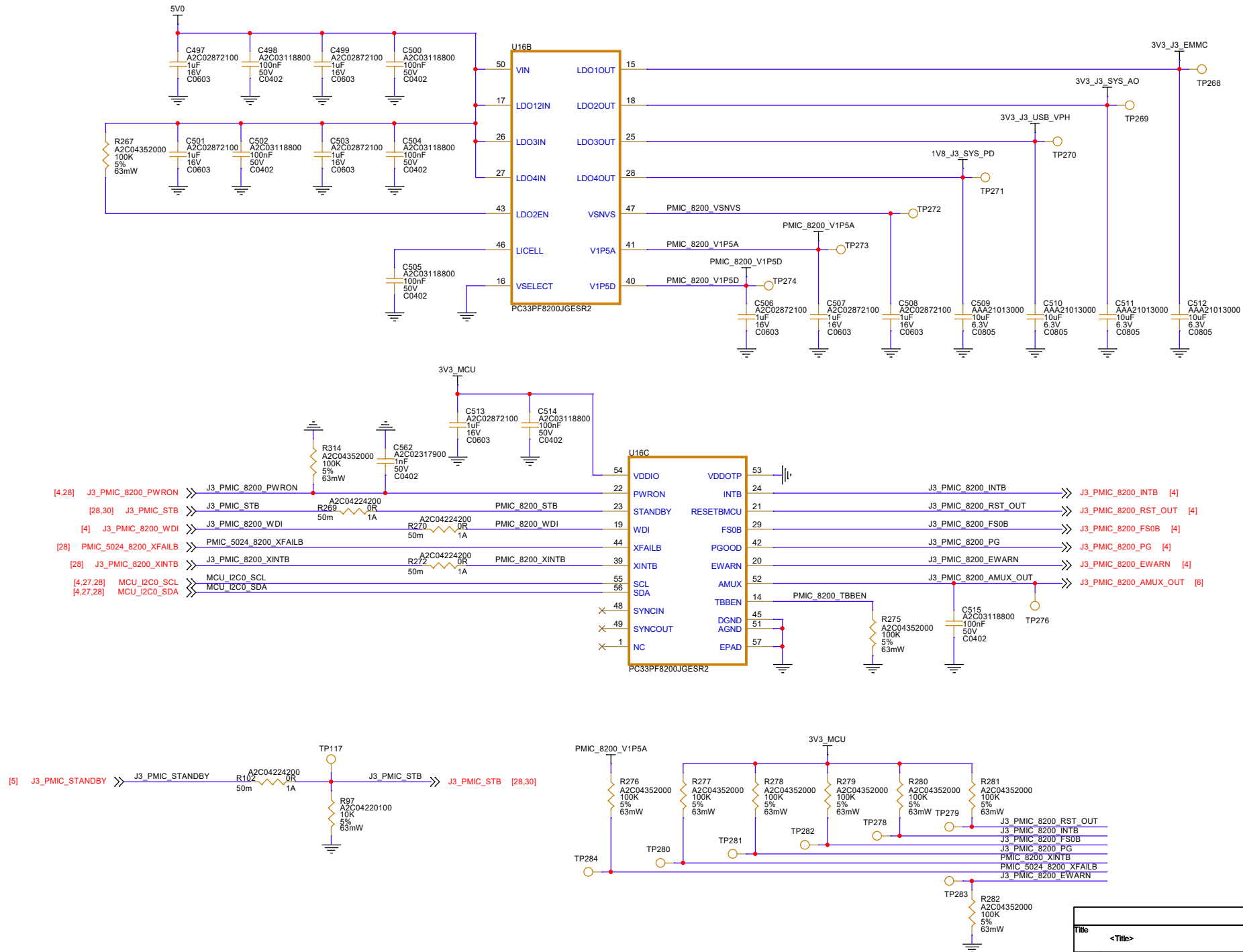
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2ND STAGE POWER SOURCES PF8200



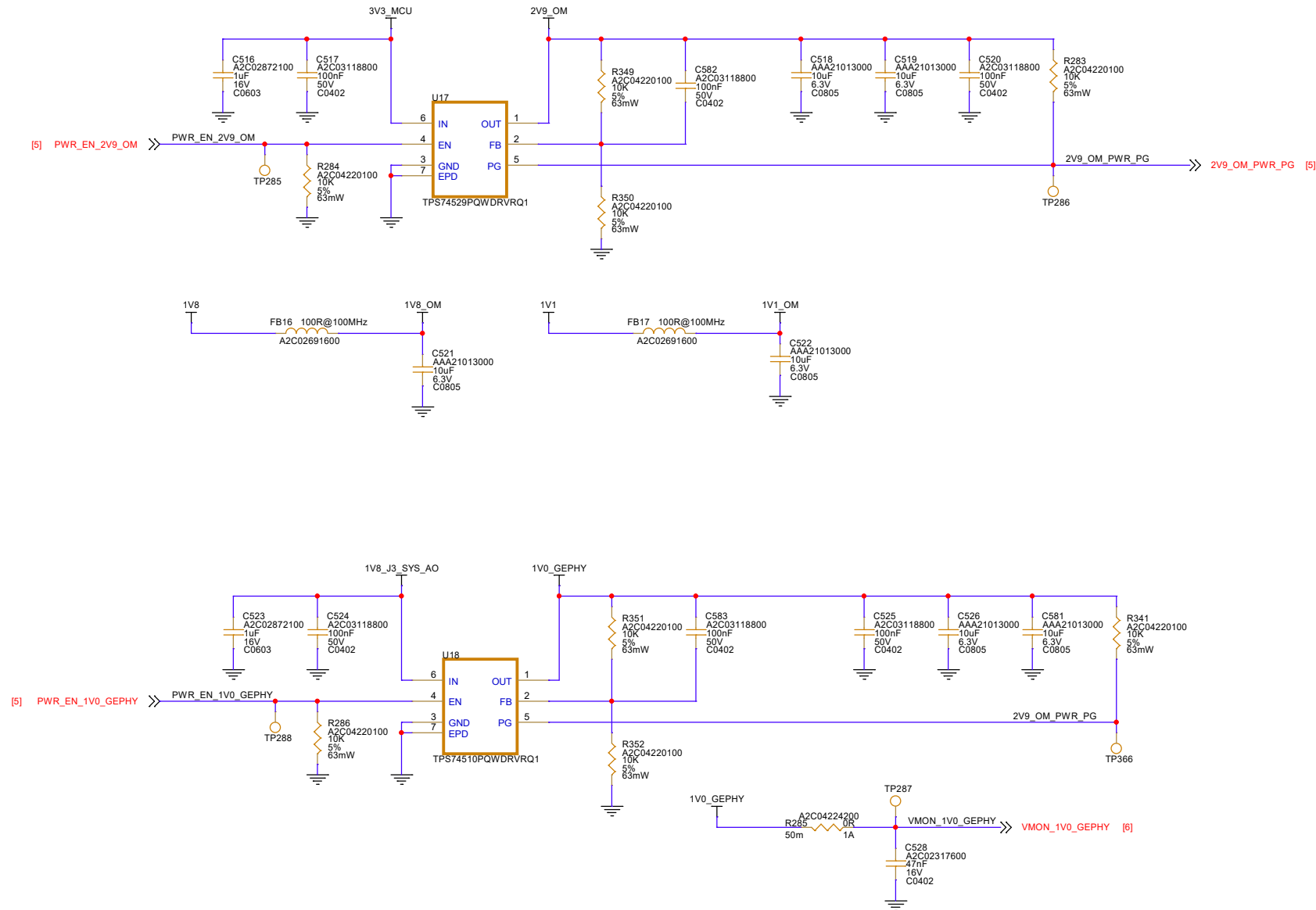
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# 2ND STAGE POWER SOURCES PF8200



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# POWER FOR PERIPHERAL: OM, GEPHY



DEBUG INTERFACE: USB

