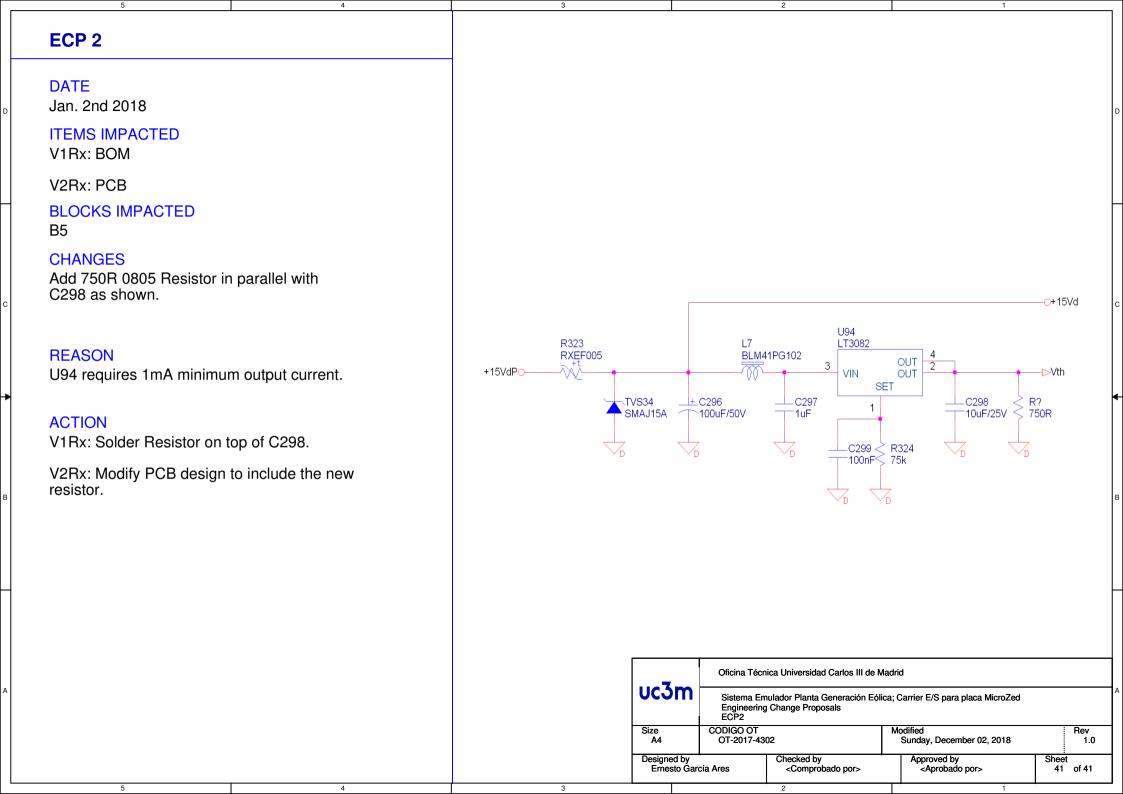


ECP₁ DATE Jan. 2nd 2018 **ITEMS IMPACTED** V1Rx: BOM **BLOCKS IMPACTED** B5, B5B1, B5B2, B5B3, B5B4, B5B5, B5B6, B5B7 +15Vd **CHANGES** TLV1701 Change R324 in B5 to 182K/1%; Change resistor values in B5B1, B5B2, B5B3, B5B4 +15Vd +3V3D B5B5, B5B6 & B5B7 as shown. **REASON** Possible change in driver control signal levels from 0-15V to 0-5V. **ACTION** +3V3D Use original component values for 0-15V driver control signals. +15Vd Use new values proposed herein for 0-5V driver control signals. W TLV1701 DRIVER DOWN 15V +15Vd +3V3D Oficina Técnica Universidad Carlos III de Madrid uc3m Sistema Emulador Planta Generación Eólica; Carrier E/S para placa MicroZed Engineering Change Proposals ECP1 CODIGO OT Size A4 OT-2017-4302 Sunday, December 02, 2018 Designed by Ernesto García Ares Checked by Approved by Sheet <Comprobado por> <Aprobado por> 41 of 41



ECP 3 DATE Jan. 2nd 2018 **ITEMS IMPACTED** V2Rx: PCB **BLOCKS IMPACTED** B3B1, B3B2, B3B3, B3B4 **CHANGES** Modify footprint for U10, U12, U14, U24. **REASON** Easily available version is 220mil wide. 180 mil package is not readily available. **ACTION** V1Rx: Bend pins inwaard to fit 180mil footprint. V2Rx: Modify PCB design to include 220 mil footprint Oficina Técnica Universidad Carlos III de Madrid uc3m Sistema Emulador Planta Generación Eólica; Carrier E/S para placa MicroZed Engineering Change Proposals ECP3 CODIGO OT Size A4 OT-2017-4302 Sunday, December 02, 2018 Designed by Ernesto García Ares Checked by Approved by Sheet <Comprobado por> <Aprobado por> 41 of 41