Vector No.	Mnemonic	Description	Source	Type	Error code(Y|N)
0	#DE	Divide Error	DIV and IDIV instructions.	Fault	N
1	#DB	Debug	Any code or data reference.	Fault/Trap	N
2	/	NMI Interrupt	Non-maskable external interrupt.	Interrupt	N
3	#BP	Breakpoint	INT3 instruction.	Trap	N
4	#OF	Overflow	INTO instruction.	Trap	N
5	#BR	BOUND Range Exceeded	BOUND instruction.	Fault	N
6	#UD	Invalid Opcode (UnDefined Opcode)	UD2 instruction or reserved opcode.1	Fault	N
7	#NM	Device Not Available (No Math Coprocessor)	Floating-point or WAIT/FWAIT instruction.	Fault	N
8	#DF	Double Fault	Any instruction that can generate an exception, an NMI, or an INTR.	Abort	Y(0)
9	#MF	CoProcessor Segment Overrun (reserved)	Floating-point instruction.2	Fault	N
10	#TS	Invalid TSS	Task switch or TSS access.	Fault	Y
11	#NP	Segment Not Present	Loading segment registers or accessing system segments.	Fault	Y
12	#SS	Stack Segment Fault	Stack operations and SS register loads.	Fault	Y
13	#GP	General Protection	Any memory reference and other protection checks.	Fault	Y
14	#PF	Page Fault	Any memory reference.	Fault	Y
15		Reserved			
16	#MF	Floating-Point Error (Math Fault)	Floating-point or WAIT/FWAIT instruction.	Fault	N
17	#AC	Alignment Check	Any data reference in memory.3	Fault	Y(0)
18	#MC	Machine Check	Error codes (if any) and source are model dependent.4	Abort	N
19	#XM	SIMD Floating-Point Exception	SIMD Floating-Point Instruction5	Fault	N
20~31		Reserved			
32~255		Maskable Interrupts	External interrupt from INTR pin or INT n instruction.	Interrupt	