**NAME – PRABHAKAR KUMAR**

**ROLL – IRM2017008**

**Assignment 6**

**Que1 –**

XNOR\_Gate.pynb, has the code of the implementation of training and design of XNOR gate using Perceptrons.

**Ques 2 –**

* 3\_INPUT\_AND.pynb
* 3\_INPUT\_OR.pynb
* 3\_INPUT\_NAND.pynb
* 3\_INPUT\_NOR.pynb

The above four files have the implementation of 3 input logic gates as was asked in the question.