UNIVERSITY OF MORATUWA

Faculty of Engineering

Department of Electronic and Telecommunication Engineering

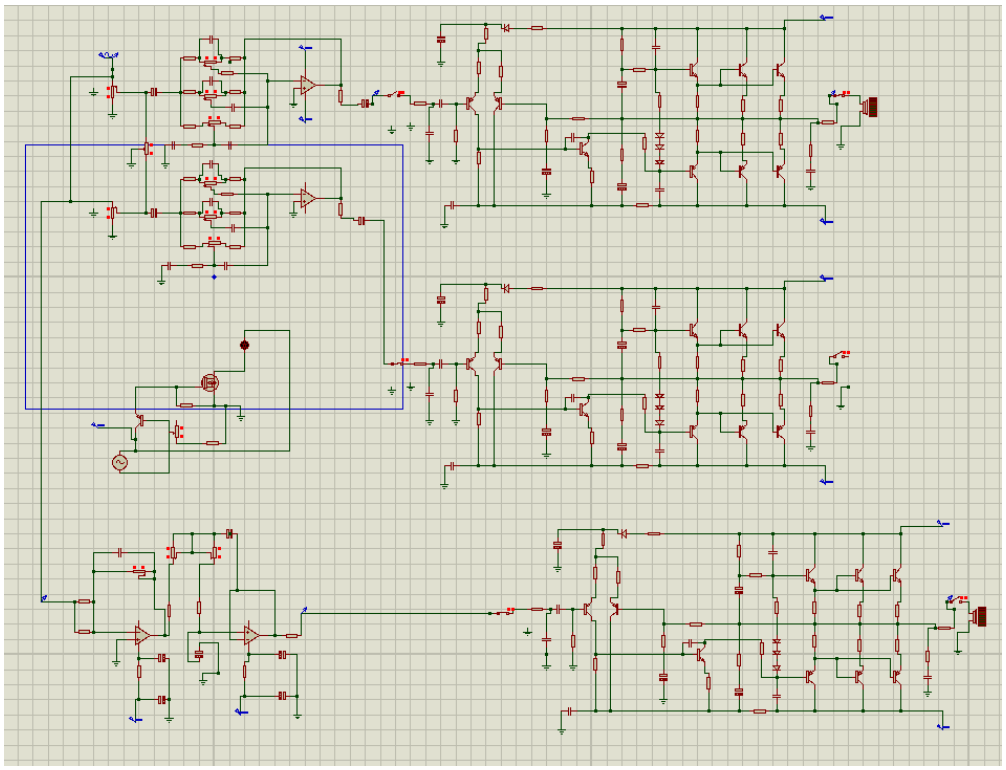
EN2160 - Electronic Design Realization

SIMULATION RESULTS (By Proteus 8 Professional)

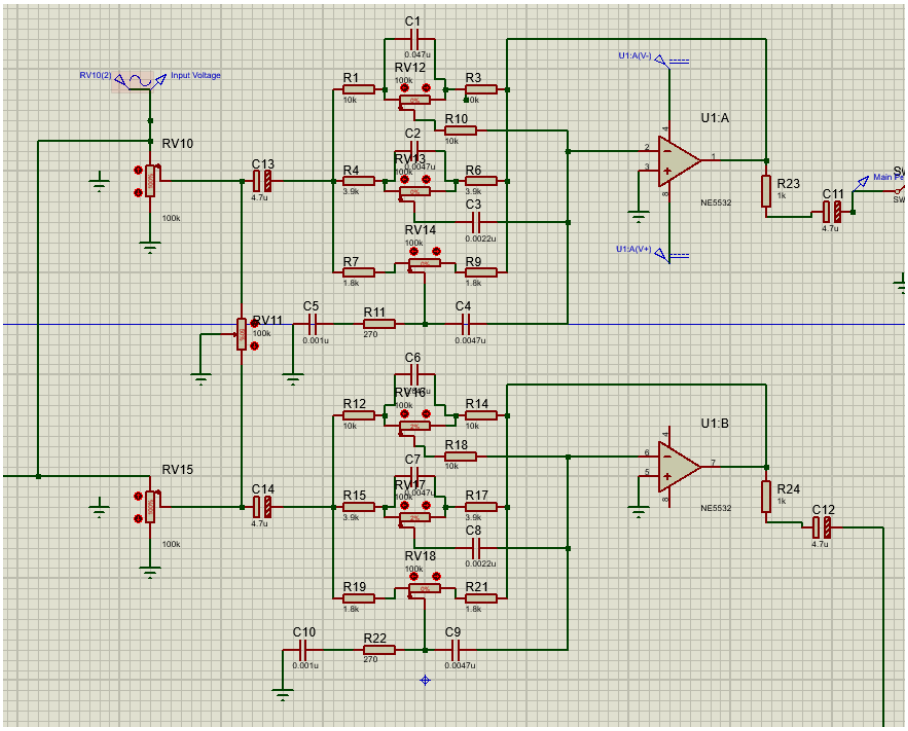
Name: R.D Prabhashana Madhubhasha Ranasinghe

Index No: 200512B

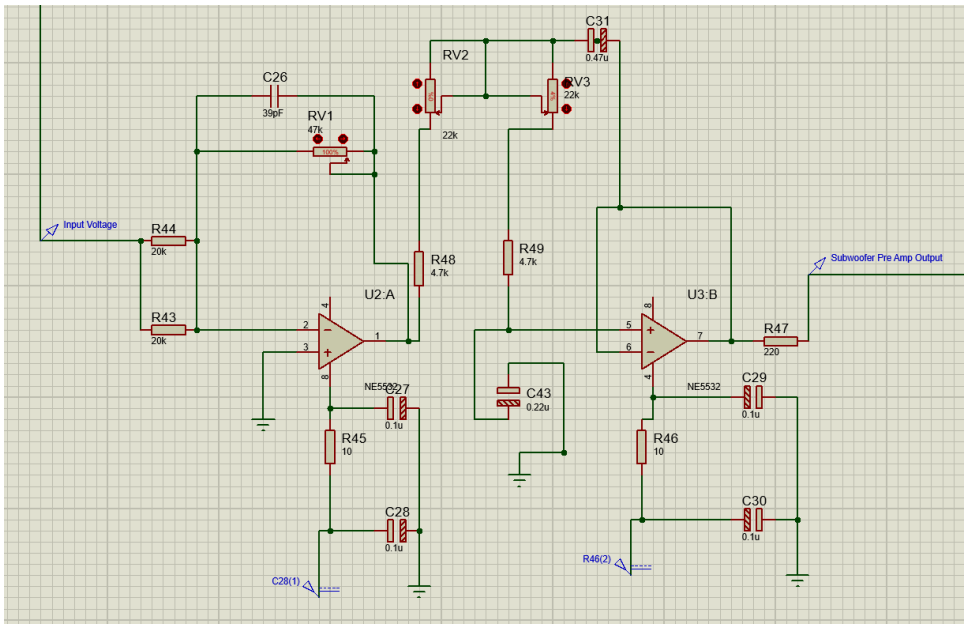
- Schematic Diagram



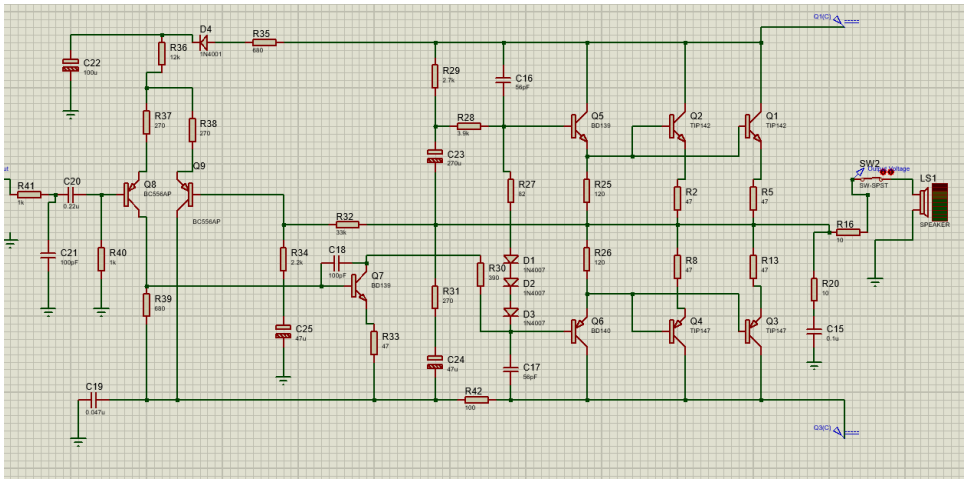
- Main Pre Amplifier



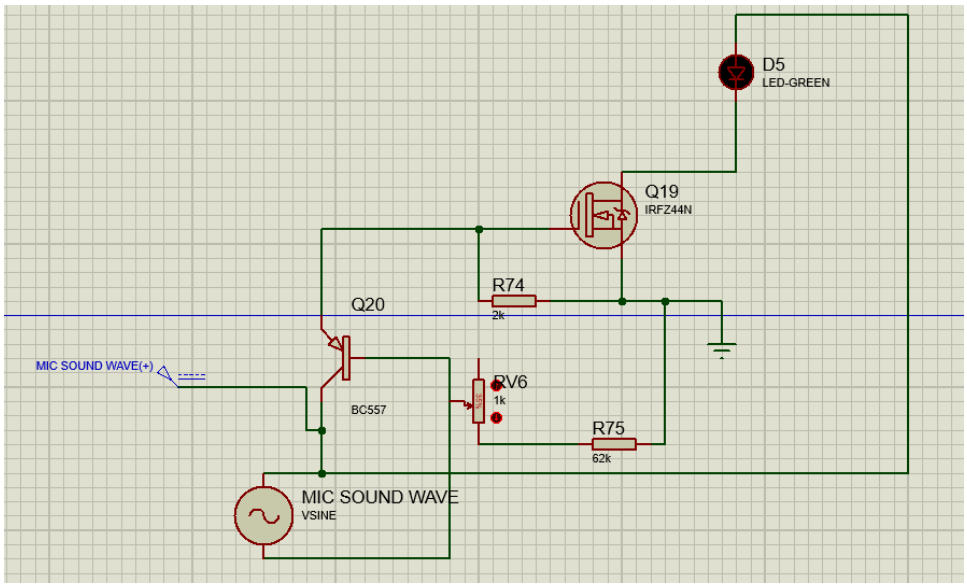
- Subwoofer Pre Amplifier



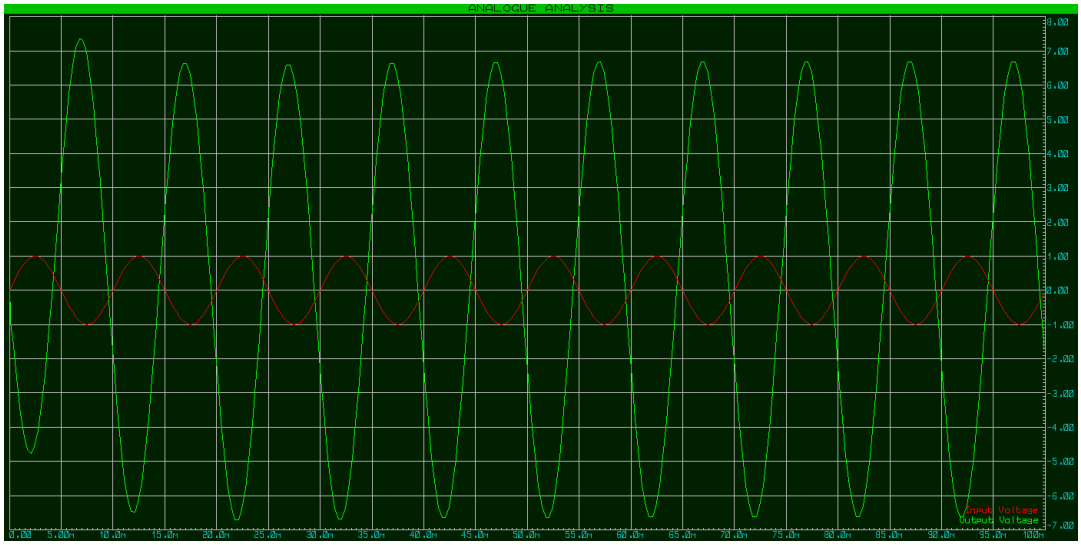
- Power Amplifier



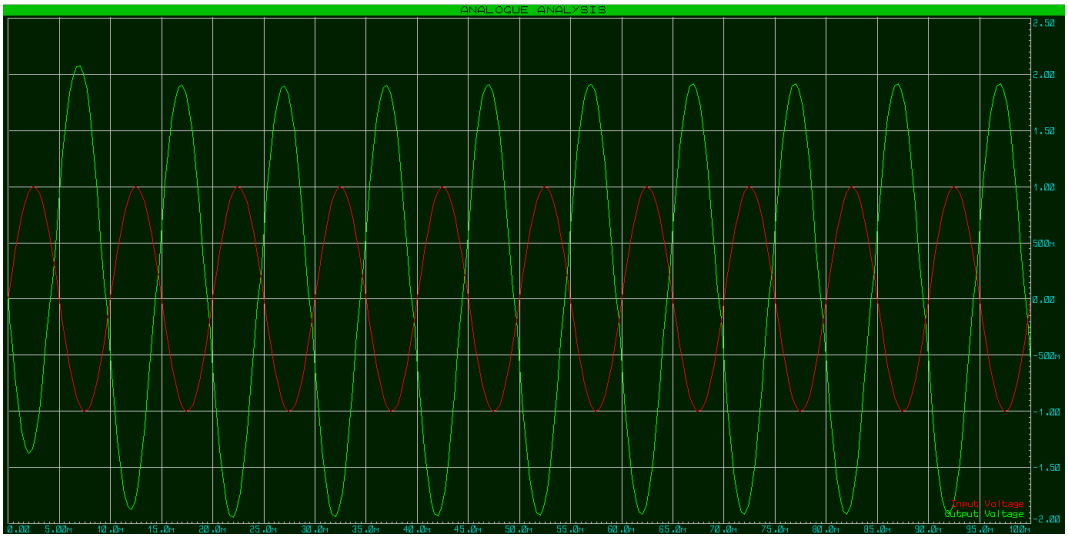
- Sound Reactive Circuit Diagram



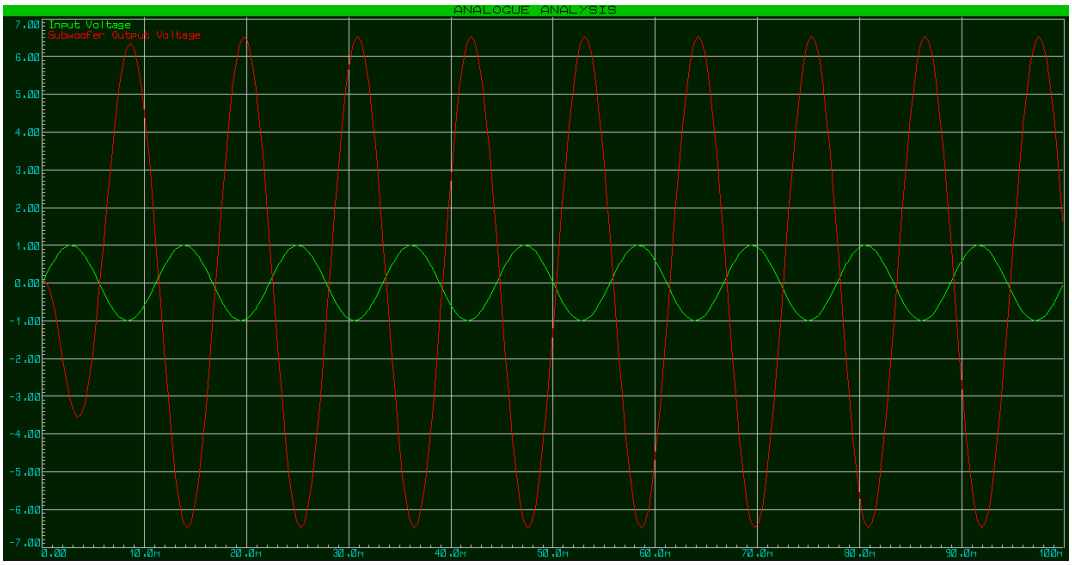
- Input voltage(2v peak-peak/100Hz) and output voltage(open loop) of main amplifier when bass, midrange and treble are fully used



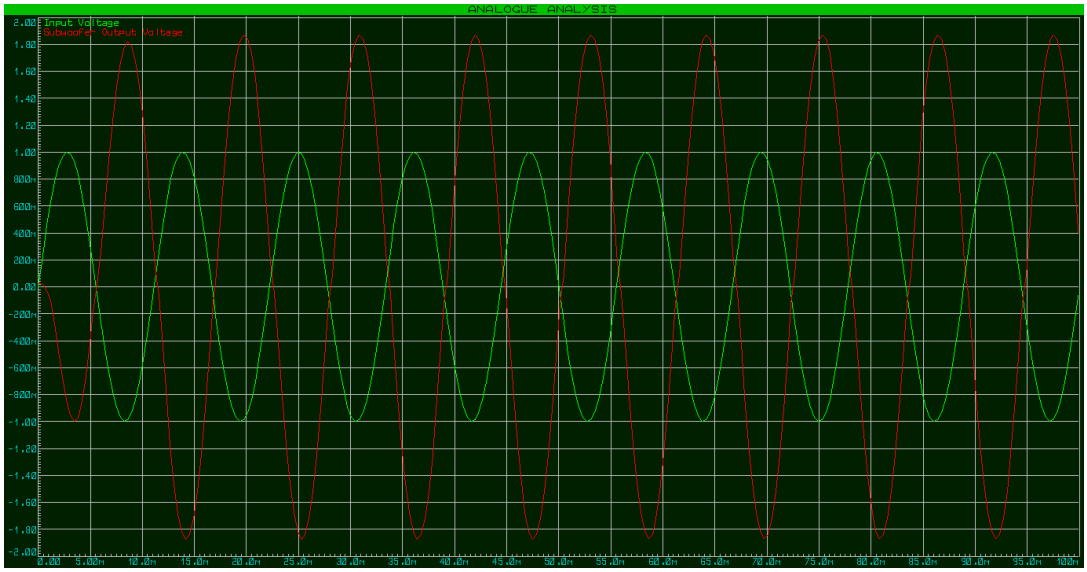
- Input voltage(2v peak-peak/100Hz) and output voltage(closed loop/8ohm speaker) of main amplifier when bass, midrange and treble are fully used



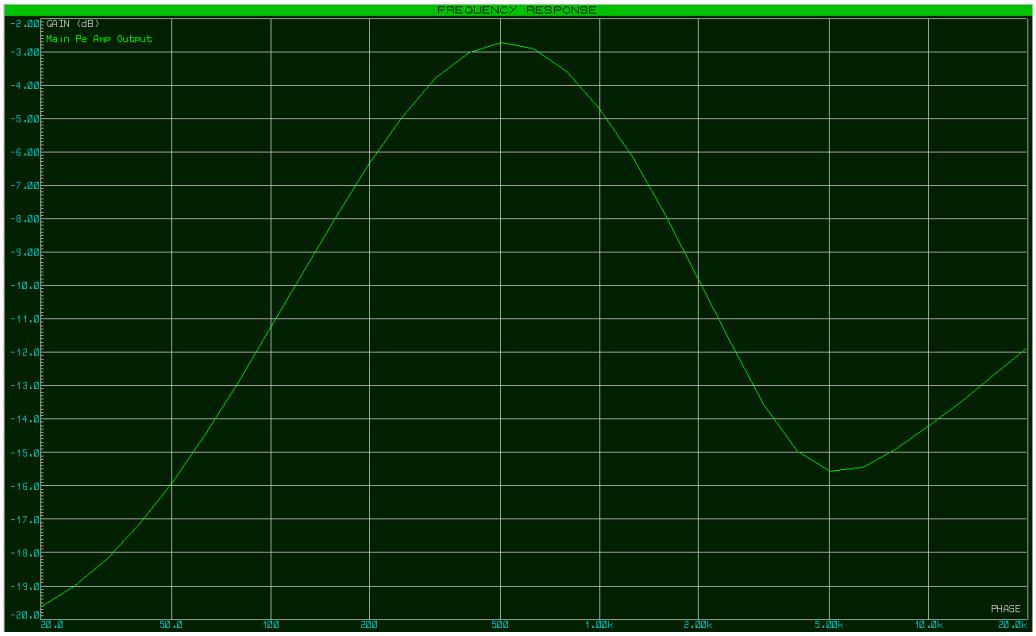
- Input voltage(2v peak-peak/90Hz) and output voltage(open loop) of subwoofer amplifier when bass is fully used



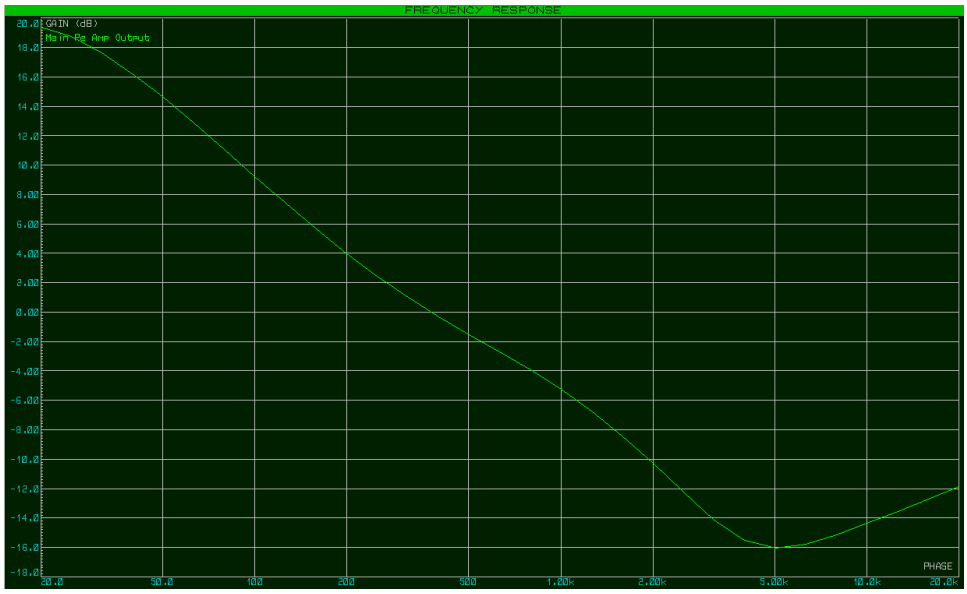
- Input voltage(2v peak-peak/90Hz) and output voltage(closed loop/8 ohm speaker) of subwoofer amplifier when bass is fully used



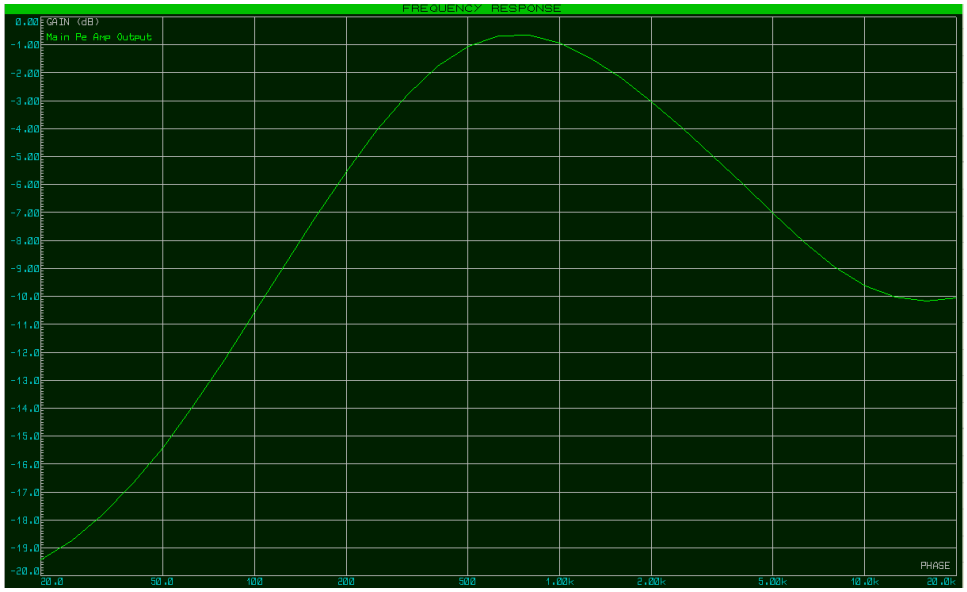
- Frequency vs Magnitude of main pre amplifier when bass, midrange and treble are not used



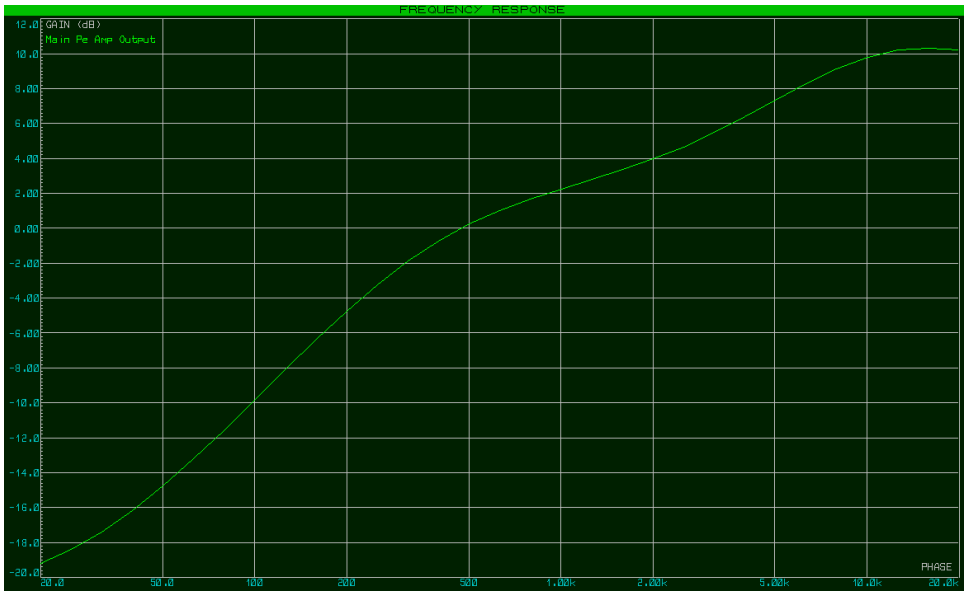
- Frequency vs Magnitude of main pre amplifier when only bass is fully used



- Frequency vs Magnitude of main pre amplifier when only midrange is fully used



- Frequency vs Magnitude of main pre amplifier when only treble is fully used



- Frequency vs Magnitude of subwoofer pre amplifier when only bass is fully used