

## Questions

Most contemporary computers use instructions with two and three operands primarily because ;

1. **It Simplifies the instructions format.**
2. It increases the instructions format complexity.
3. It increases the instruction length.
4. **It reduces the CPU complexity.**

Instructions with a larger size and greater number of addresses tend to ;

1. Simplify the instructions format
2. **Increases the CPU complexity**
3. Reduces the instruction length
4. Decrease the instruction length.

As the number of operands per instruction decreases the instruction tend to become;

1. More complex and advanced
2. Longer and more powerful
3. **Simpler and more primitive**
4. Shorter and more efficient