Accel-Sim-build

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--all_kernels	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--all_kernels	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--all_kernels	l1_lat/__blocks_1___fast--all_kernels
RTX4090-SASS	accelsim-commit-d7f397a_modified_6.0_25-12-21-16-06-16	accelsim-commit-d7f397a_modified_6.0_25-12-21-16-06-16	accelsim-commit-d7f397a_modified_6.0_25-12-21-16-06-16	accelsim-commit-d7f397a_modified_6.0_25-12-21-16

GPGPU-Sim-build

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--all_kernels	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--all_kernels	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--all_kernels	l1_lat/__blocks_1___fast--all_kernels
RTX4090-SASS	gpgpu-sim_git-commit-b18ee397_modified_3.0.	gpgpu-sim_git-commit-b18ee397_modified_3.0.	gpgpu-sim_git-commit-b18ee397_modified_3.0.	gpgpu-sim_git-commit-b18ee397_modified_3.0.

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gpu_tot_sim_insn\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	54931456	2277376	40711168	31108

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gpgpu_simulation_time\s*=.*\(([0-9]+) sec\).*

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	145	73	138	16

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gpu_tot_sim_cycle\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	584513	300763	547209	67150

\s+L2_cache_stats_breakdown\[GLOBAL_ACC_R\]\[HIT\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	0	0	0	0

\s+L2_cache_stats_breakdown\[GLOBAL_ACC_R\]\[TOTAL_ACCESS\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	2060	2056	2048NA	

\s+L2_cache_stats_breakdown\[GLOBAL_ACC_W\]\[HIT\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	12	8	0	6144

\s+L2_cache_stats_breakdown\[GLOBAL_ACC_W\]\[TOTAL_ACCESS\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	640	768	640	8195

\s+Total_core_cache_stats_breakdown\[GLOBAL_ACC_R\]\[TOTAL_ACCESS\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	2099200	133120	2099200	257

\s+Total_core_cache_stats_breakdown\[GLOBAL_ACC_R\]\[HIT\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	2097128	131064	2097152	257

\s+Total_core_cache_stats_breakdown\[GLOBAL_ACC_W\]\[HIT\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	12	8	0	6144

\s+Total_core_cache_stats_breakdown\[GLOBAL_ACC_W\]\[TOTAL_ACCESS\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	640	768	640	8195

\s+Total_core_cache_stats_breakdown\[GLOBAL_ACC_R\]\[MSHR_HIT\]\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	12	0	0	0

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gpgpu_n_tot_w_icount\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	1716672	71232	1272320	31124

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total dram reads\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	2060	2056	2048	0

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total dram writes\s*=\s*(.*)

CFG	l1_bw_32f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_64f/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_bw_128/__tpb_1024___tpsm_1024___blocks_1--_Z5l1_bwPmS_	l1_lat/__blocks_1___fast--_Z6l1_latPjS_PmS0_jj--0
RTX4090-SASS	0	0	0	0