

TUTORIAL-5 (EE 101: Basic Electronics)

DEPARTMENT OF ELECTRONICS & ELECTRICAL ENGINEERING, IIT GUWAHATI

PRE-TUTORIAL ASSIGNMENT- PROBLEMS *(To be solved in the space provided)*

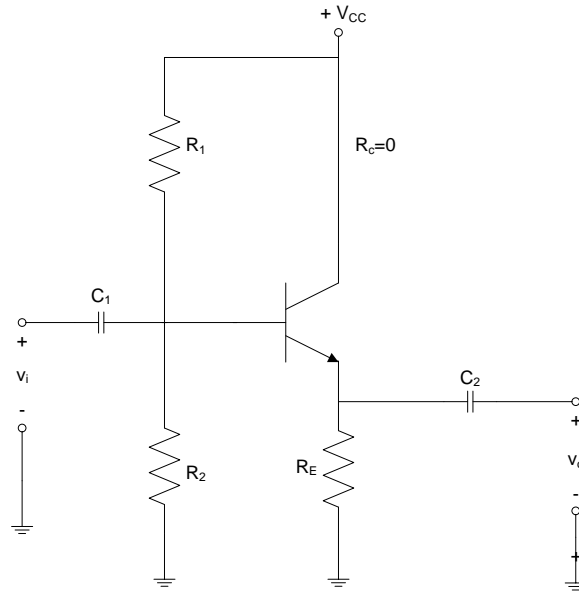
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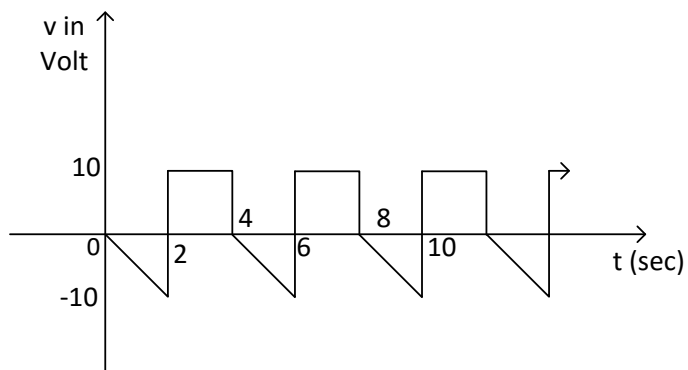
Tutorial Group:

Problem-1: In the transistor circuit shown, assume the following – $\beta=100$, $R_1=100\text{ K}\Omega$, $R_2=50\text{ K}\Omega$, $R_E=5\text{ K}\Omega$, $V_{CC}=15\text{ V}$. Find the Bias Point of the transistor and its r_e . (Assume this to be a silicon transistor with

$V_{CE,SAT} = 0.1\text{ V}$ if it is in saturation. Use $V_T = 26\text{ mV}$)



Problem-2: Determine the RMS value of a source with the voltage waveform given below. If the source is connected to a $2\ \Omega$ resistor, find the power absorbed by the resistor.

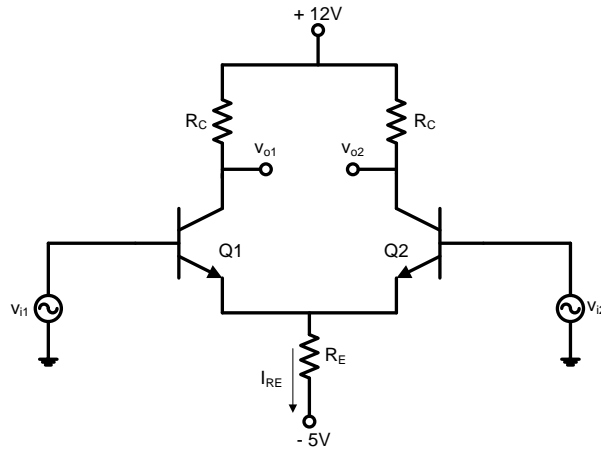


TUTORIAL-5: PROBLEMS

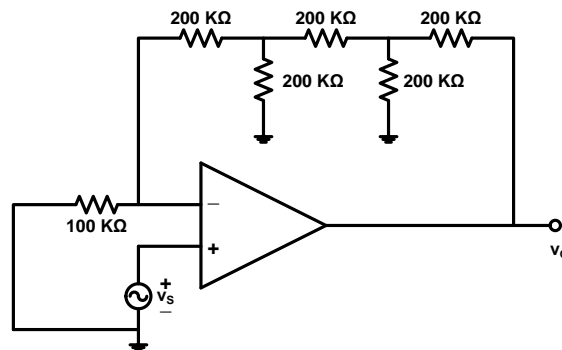
Problem 1: In the differential amplifier circuit shown, the two transistors are identical with $\beta=50$.

(a) If $R_C=50\text{ K}\Omega$ and $R_E=10\text{ K}\Omega$, what is the Q-Point for the two transistors. (Assume that v_{i1} and v_{i2} are shorted to ground for DC.)

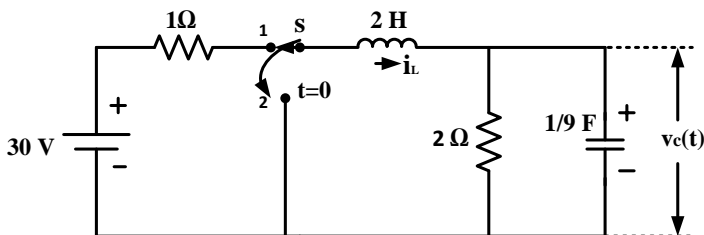
(b) Find the voltage gain A_V of this differential amplifier where $A_V=(v_{O1}-v_{O2})/(v_{i1}-v_{i2})$ assuming the transistor's output resistance r_o to be infinity.



Problem 2: Find the gain $A_V = v_o/v_s$ of the circuit shown.



Problem 3: Switch S was closed in position 1 for sufficiently long time before connection to position 2. Find the expression for $V_C(t)$ for all time $t > 0$.



Problem 4: Draw the current phasor diagram for the circuit shown below when $V_s = 100 \cos(1.43t + 53.13^\circ)$.

