

AXI_DataMemory_0

S_AXI
► S_AXI_AWADDR[31:0]
► S_AXI_AWVALID
◀ S_AXI_AWREADY
► S_AXI_WDATA[31:0]
► S_AXI_WSTRB[3:0]
► S_AXI_WVALID
◀ S_AXI_WREADY
◀ S_AXI_BRESP[1:0]
◀ S_AXI_BVALID
► S_AXI_BREADY
► S_AXI_ARADDR[31:0]
► S_AXI_ARVALID
◀ S_AXI_ARREADY
◀ S_AXI_RDATA[31:0]
◀ S_AXI_RRESP[1:0]
◀ S_AXI_RVALID
► S_AXI_RREADY
— aclk
● aresetn

AXI Data Memory (RISC V)