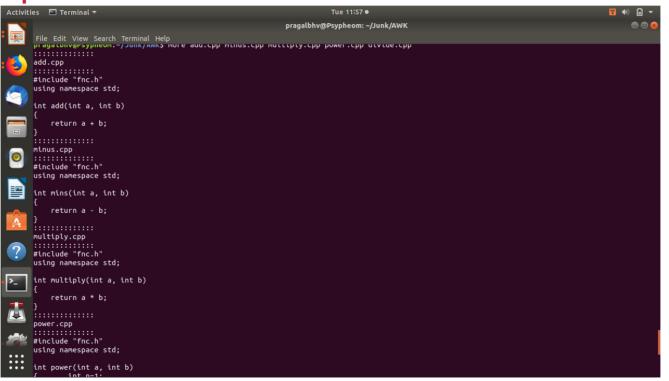
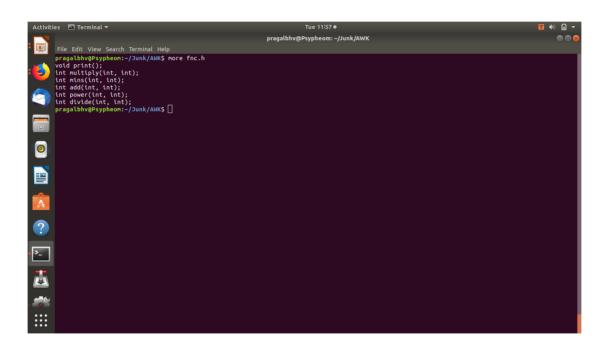
HOMEWORK 5

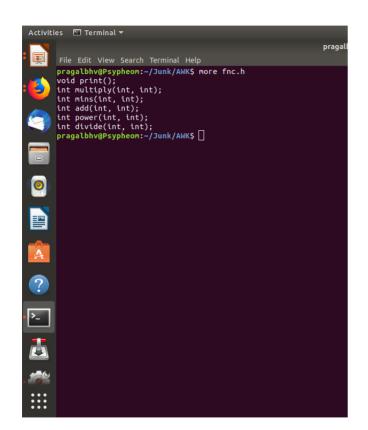
Makefile

Take one of your old codes, split the code into separate files, one for each function. Create a makefile and test the recompilation

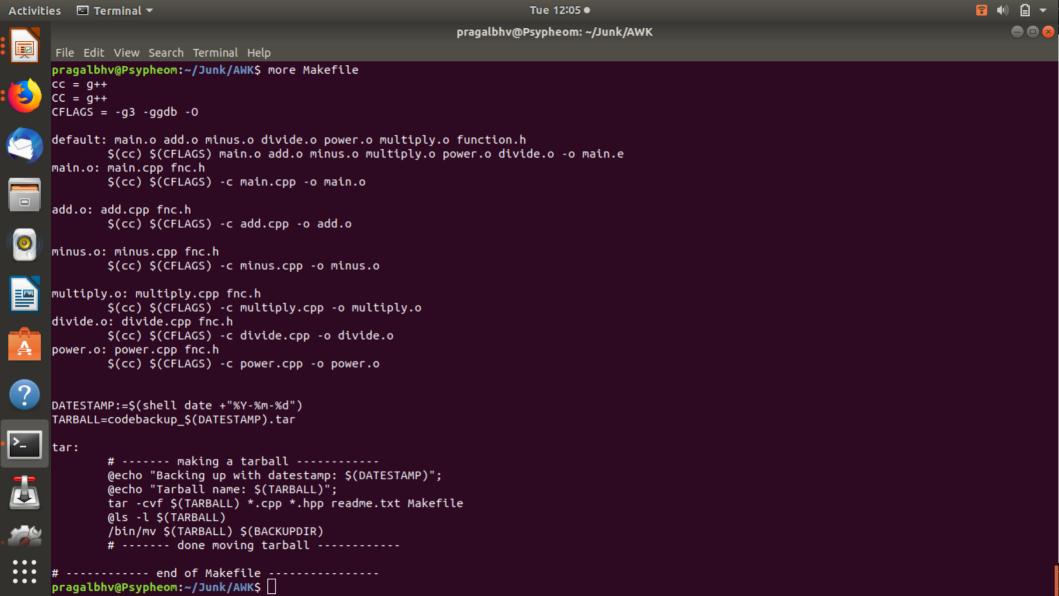


Fnc.h





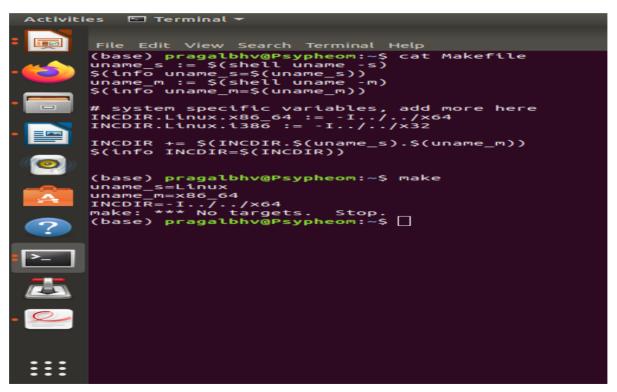




Create a makefile that uses a pattern for files rather than explicit listing of each of the files.

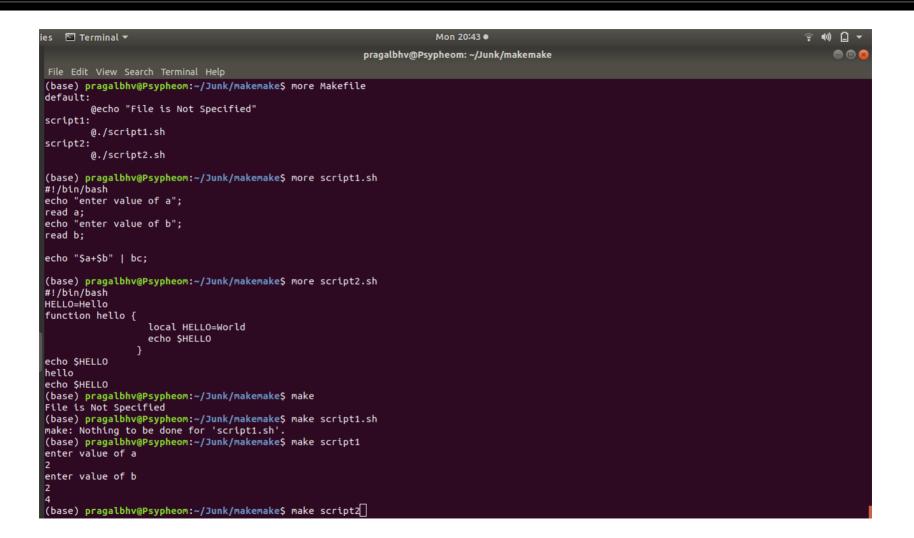
```
Activities
         ▼ Terminal ▼
                                                                  pragalbhv@
      File Edit View Search Terminal Help
      (base) pragalbhv@Psypheom:~/Junk/gimp$ more Makefile
      blah: blah.o
              cc blah.o -o blah
      blah.o: blah.c
              cc -c blah.c -o blah.o
      blah.c:
              echo "int main() { return 0; }" > blah.c
      bluh: bluh.o
              cc bluh.o -o bluh
      bluh.o: bluh.c
              cc -c bluh.c -o bluh.o
      bluh.c:
              echo "int main() { return 0; }" > bluh.c
      clean:
              rm -f %.o %.c blah bluh
      (base) pragalbhv@Psypheom:~/Junk/gimp$ make
      echo "int main() { return 0; }" > blah.c
      cc -c blah.c -o blah.o
      cc blah.o -o blah
      (base) pragalbhv@Psypheom:~/Junk/gimp$
```

Prepare a Makefile that performs conditional compilation depending on the architecture of the machine.



Create a Makefile that uses your own bash shell scripts in each recipe.

```
ties 🖾 Terminal ▼
                                                                       Mon 20:43 ●
                                                          pragalbhv@Psypheom: ~/Junk/makemake
  File Edit View Search Terminal Help
 (base) pragalbhv@Psvpheom:~/Junk/makemake$ more Makefile
 default:
         @echo "File is Not Specified"
 script1:
         @./script1.sh
 script2:
         @./script2.sh
 (base) pragalbhv@Psypheom:~/Junk/makemake$ more script1.sh
 #!/bin/bash
 echo "enter value of a";
 read a;
 echo "enter value of b";
 read b:
 echo "$a+$b" | bc;
 (base) pragalbhv@Psypheom:~/Junk/makemake$ more script2.sh
 #!/bin/bash
 HELLO=Hello
  function hello {
                    local HELLO=World
                    echo SHELLO
 echo SHELLO
 hello
 echo SHELLO
 (base) pragalbhv@Psypheom:~/Junk/makemake$ make
 File is Not Specified
 (base) pragalbhv@Psypheom:~/Junk/makemake$ make script1.sh
 make: Nothing to be done for 'script1.sh'.
 (base) pragalbhv@Psypheom:~/Junk/makemake$ make script1
 enter value of a
 enter value of b
 (base) pragalbhv@Psypheom:~/Junk/makemake$ make script2
```



Create a Makefile that can compile a code in three different ways namely (a) without any options, (b) with all options to provide warnings for non-compliance to ANSI standards etc., and (c)with options that help the final executable run fastest possible for the given architecture of the machin

```
ties 🖭 Terminal ▼
                                                                      Mon 23:00 ●
                                                   pragalbhv@Psypheom: ~/Junk/makemake/makemake
 File Edit View Search Terminal Help
 cc = a++
 cflags = #compiles without options
 Cflags = -a3 -aadb
 #c++ code is compiled providing warnings for Non-compilance to ANSI standards
 CFLAGS = -q3 - qqdb - o3
 #compiles with options that help the final executable run fastest possible for the given architecture of the machine
 default:
         @echo "No targets STOP"
 main_0:*.h
         @rm -f *.out
         @ $(cc) $(cflags) -o *.out *.cpp
main_1:*.h
         @rm -f *.out
         0 $(cc) $(cflags) -o *.out *.cpp
 main 2: *.h
         @rm -f *.out
         @ $(cc) $(cflags) -o *.out *.cpp
```