RTE and Watchdog

Learning focus:
Autosar RTE, Runnables, Watchdog

Your task is to develop an embedded architecture following a simplified concept of the Autosar RTE and to implement a watchdog driver. Please check the workbook for a description of the exercises.

Exercises

6.1	A light version of the Autosar RTE - Electronic Gaspedal	6 points
6.2	Timing Supervision	4 points

You need 40% = 8 points of both lab assignments in order to pass the lab!

Note: You need version 2021a or later of the script for this exercise. Please use the latest Python Generator.

Upload the following items to Moodle

1. Your code (zipfile of the PSOC project – please call make clean before)
2. The completed table on error handling (page 126 of the script)