

DAYANANDA SAGAR COLLEGE OF ENGINEERING

(An Autonomous Institute Affiliated to VTU, Belagavi)
Shavige Malleshwara Hills, Kumaraswamy Layout, Bengaluru – 560078
Accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade

Grade Extract of VI Semester BACHELOR OF ELECTRONICS AND COMMUNICATION

Name of the Student: JANISH AHMED HASHEER A M

USN / Roll No: 1DS16EC058

Branch: ELECTRONICS & COMMUNICATION ENGINEERING

Semester: 6

Examination: June 2019

SL. NO	COURSE CODE	COURSE	CREDITS REGISTERED	GRADE	GRADE POINTS
1	EC61	DIGITAL COMMUNICATION	4	В	7
2	EC62	EMBEDDED SYSTEM DESIGN	4	Α	8
3	EC63	FUNDAMENTALS OF ANALOG VLSI DESIGN	4	В	7
4	EC64	INFORMATION THEORY & CODING	3	Α	8
5	EC654	OPERATING SYSTEMS	3	С	6
6	EC661	DIGITAL SYSTEM DESIGN USING VERILOG	3	Α	8
7	ECL67	ADVANCED COMMUNICATION LABORATORY	2	S	9
8	ECL68	VLSI LABORATORY	2	S	9
				SGPA	7.6

Letter Grades:

S+ Outstanding (>=90); **S** Excellent (<90 - >=80); **A** Very Good (<80 - >=70); **B** Good (<70 - >=60); **C** Above Average (<60 - >=50); **D** Average (<50 - >=45); **E** Poor (<45 - >=40); **F** Fail (<40);

P - Passed; NP - Not Passed;

X - Failed in SEE but eligible for Make up Examination;

I - Failed to attend SEE due to illness;

WH - Withheld;

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