

DAYANANDA SAGAR COLLEGE OF ENGINEERING

(An Autonomous Institute Affiliated to VTU, Belagavi)
Shavige Malleshwara Hills, Kumaraswamy Layout, Bengaluru – 560078
Accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade

Grade Extract of IV Semester BACHELOR OF ELECTRONICS AND COMMUNICATION

Name of the Student: RIDDHI PRAKASH ATHREYA

USN / Roll No: 1DS17EC736

Branch: ELECTRONICS & COMMUNICATION ENGINEERING

Semester: 4

Examination: June 2019

| SL. NO | COURSE CODE | COURSE | CREDITS REGISTERED | GRADE | GRADE POINTS |
|--------|----------------|--|-----------------------|-------|-----------------|
| 1 | 17MA4ICMAT | ENGINEERING MATHEMATICS – IV | 4 | S+ | 10 |
| 2 | 17EC4DCSAS | SIGNALS & SYSTEMS | 4 | Α | 8 |
| 3 | 17EC4DCACN | ANALOG COMMUNICATION | 4 | S | 9 |
| 4 | 17EC4DCLIC | LINEAR INTEGRATED CIRCUITS AND ITS APPLICATIONS | 3 | S | 9 |
| 5 | 17EC4DCMCF | MICROCONTROLLERS | 3 | В | 7 |
| 6 | 17EC4DCNEN | NANO ELECTRONICS | 3 | А | 8 |
| 7 | | ANALOG COMMUNICATION & LINEAR INTEGRATED CIRCUITS LABORATORY | 2 | S+ | 10 |
| 8 | 17EC4DLMCR | MICROCONTROLLERS LABORATORY | 2 | S | 9 |
| | | | | SGPA | 8.72 |

Letter Grades:

S+ Outstanding (>=90); **S** Excellent (<90 - >=80); **A** Very Good (<80 - >=70); **B** Good (<70 - >=60); **C** Above Average (<60 - >=50); **D** Average (<50 - >=45); **E** Poor (<45 - >=40); **F** Fail (<40);

P - Passed; NP - Not Passed;

X - Failed in SEE but eligible for Make up Examination;

I - Failed to attend SEE due to illness;

WH - Withheld;

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