



DAYANANDA SAGAR COLLEGE OF ENGINEERING

(An Autonomous Institute Affiliated to VTU, Belagavi)
Shavige Malleshwara Hills, Kumaraswamy Layout, Bengaluru – 560078
Accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade

Grade Extract of VI Semester BACHELOR OF ELECTRONICS AND COMMUNICATION

Name of the Student: YUVRAJ SINGH JHALA
USN / Roll No: 1DS16EC166
Branch: ELECTRONICS & COMMUNICATION ENGINEERING
Semester: 6
Examination: June 2019

SL. NO	COURSE CODE	COURSE	CREDITS REGISTERED	GRADE	GRADE POINTS
1	EC61	DIGITAL COMMUNICATION	4	C	6
2	EC62	EMBEDDED SYSTEM DESIGN	4	A	8
3	EC63	FUNDAMENTALS OF ANALOG VLSI DESIGN	4	B	7
4	EC64	INFORMATION THEORY & CODING	3	B	7
5	EC653	DIGITAL IMAGE PROCESSING	3	B	7
6	EC661	DIGITAL SYSTEM DESIGN USING VERILOG	3	C	6
7	ECL67	ADVANCED COMMUNICATION LABORATORY	2	S	9
8	ECL68	VLSI LABORATORY	2	A	8
				SGPA	7.12

Letter Grades:

S+ Outstanding (≥ 90); **S** Excellent ($90 > \geq 80$); **A** Very Good ($80 > \geq 70$); **B** Good ($70 > \geq 60$); **C** Above Average ($60 > \geq 50$); **D** Average ($50 > \geq 45$); **E** Poor ($45 > \geq 40$); **F** Fail (< 40);

P - Passed; **NP** - Not Passed;

X - Failed in SEE but eligible for Make up Examination;

I - Failed to attend SEE due to illness;

WH - Withheld;

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