



## DAYANANDA SAGAR COLLEGE OF ENGINEERING

(An Autonomous Institute Affiliated to VTU, Belagavi)  
Shavige Malleshwara Hills, Kumaraswamy Layout, Bengaluru – 560078  
Accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade

### Grade Extract of VI Semester BACHELOR OF ELECTRONICS AND COMMUNICATION

Name of the Student: TUSHAR DINESH SHIRALKAR  
USN / Roll No: 1DS16EC156  
Branch: ELECTRONICS & COMMUNICATION ENGINEERING  
Semester: 6  
Examination: June 2019

SL. NO	COURSE CODE	COURSE	CREDITS REGISTERED	GRADE	GRADE POINTS
1	EC61	DIGITAL COMMUNICATION	4	F	0
2	EC62	EMBEDDED SYSTEM DESIGN	4	C	6
3	EC63	FUNDAMENTALS OF ANALOG VLSI DESIGN	4	B	7
4	EC64	INFORMATION THEORY & CODING	3	B	7
5	EC653	DIGITAL IMAGE PROCESSING	3	B	7
6	EC661	DIGITAL SYSTEM DESIGN USING VERILOG	3	A	8
7	ECL67	ADVANCED COMMUNICATION LABORATORY	2	S+	10
8	ECL68	VLSI LABORATORY	2	S	9
				<b>SGPA</b>	<b>6.24</b>

#### Letter Grades:

**S+** Outstanding ( $\geq 90$ ); **S** Excellent ( $90 > \geq 80$ ); **A** Very Good ( $80 > \geq 70$ ); **B** Good ( $70 > \geq 60$ ); **C** Above Average ( $60 > \geq 50$ ); **D** Average ( $50 > \geq 45$ ); **E** Poor ( $45 > \geq 40$ ); **F** Fail ( $< 40$ );

**P** - Passed; **NP** - Not Passed;

**X** - Failed in SEE but eligible for Make up Examination;

**I** - Failed to attend SEE due to illness;

**WH** - Withheld;

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Jun 21, 2019 9:17 PM