



## DAYANANDA SAGAR COLLEGE OF ENGINEERING

(An Autonomous Institute Affiliated to VTU, Belagavi)  
Shavige Malleshwara Hills, Kumaraswamy Layout, Bengaluru – 560078  
Accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade

### Grade Extract of V Semester BACHELOR OF ELECTRONICS AND COMMUNICATION

Name of the Student: RIDDHI PRAKASH ATHREYA  
USN / Roll No: 1DS17EC736  
Branch: ELECTRONICS AND COMMUNICATION ENGINEERING  
Semester: 5  
Examination: December 2019

SL. NO	COURSE CODE	COURSE	CREDITS REGISTERED	GRADE	GRADE POINTS
1	17HS5ICMEP	MANAGEMENT & ENTREPRENEURSHIP	4	S	9
2	17EC5DCDSP	DIGITAL SIGNAL PROCESSING	4	A	8
3	17EC5DCMAT	MICROWAVE & ANTENNA THEORY	4	A	8
4	17EC5DCHDL	HARDWARE DESCRIPTION LANGUAGES	3	A	8
5	17EC5DECSE	CONTROL SYSTEM ENGINEERING	3	A	8
6	17EC5DEPUC	PROGRAMMING IN C++	3	A	8
7	17EC5DCMT	EMERGING TECHNOLOGIES	2	S	9
8	17EC5DLDSP	DSP & CONTROLS LABORATORY	2	S+	10
9	17EC5DLHDL	HDL LABORATORY	2	S	9
				<b>SGPA</b>	<b>8.44</b>

#### Letter Grades:

**S+** Outstanding ( $\geq 90$ ); **S** Excellent ( $90 > \geq 80$ ); **A** Very Good ( $80 > \geq 70$ ); **B** Good ( $70 > \geq 60$ ); **C** Above Average ( $60 > \geq 50$ ); **D** Average ( $50 > \geq 45$ ); **E** Poor ( $45 > \geq 40$ ); **F** Fail ( $< 40$ );

**P** - Passed; **NP** - Not Passed;

**X** - Failed in SEE but eligible for Make up Examination;

**I** - Failed to attend SEE due to illness;

**WH** - Withheld;

**Note:** The results published on this web site are provisional and are provided for immediate information to the examinees. These results may not be used as official confirmation of achievement. While all efforts have been made to make the information available on this website as authentic as possible, DAYANANDA SAGAR COLLEGE OF ENGINEERING or any of their staff will not be responsible for any loss caused by shortcoming, defect or inaccuracy in the information provided on this website. Please contact the Controller of Examinations for the final official results.