



DAYANANDA SAGAR COLLEGE OF ENGINEERING

(An Autonomous Institute Affiliated to VTU, Belagavi)
Shavige Malleshwara Hills, Kumaraswamy Layout, Bengaluru – 560078
Accredited by National Assessment and Accreditation Council (NAAC) with 'A' Grade

Grade Extract of VI Semester BACHELOR OF ELECTRONICS AND COMMUNICATION

Name of the Student: BALASUBRAMANYA K
USN / Roll No: 1DS16EC030
Branch: ELECTRONICS & COMMUNICATION ENGINEERING
Semester: 6
Examination: June 2019

SL. NO	COURSE CODE	COURSE	CREDITS REGISTERED	GRADE	GRADE POINTS
1	EC61	DIGITAL COMMUNICATION	4	B	7
2	EC62	EMBEDDED SYSTEM DESIGN	4	B	7
3	EC63	FUNDAMENTALS OF ANALOG VLSI DESIGN	4	A	8
4	EC64	INFORMATION THEORY & CODING	3	A	8
5	EC653	DIGITAL IMAGE PROCESSING	3	B	7
6	EC661	DIGITAL SYSTEM DESIGN USING VERILOG	3	B	7
7	ECL67	ADVANCED COMMUNICATION LABORATORY	2	S	9
8	ECL68	VLSI LABORATORY	2	S	9
				SGPA	7.6

Letter Grades:

S+ Outstanding (≥ 90); **S** Excellent ($90 > \geq 80$); **A** Very Good ($80 > \geq 70$); **B** Good ($70 > \geq 60$); **C** Above Average ($60 > \geq 50$); **D** Average ($50 > \geq 45$); **E** Poor ($45 > \geq 40$); **F** Fail (< 40);

P - Passed; **NP** - Not Passed;

X - Failed in SEE but eligible for Make up Examination;

I - Failed to attend SEE due to illness;

WH - Withheld;

Note: The results published on this web site are provisional and are provided for immediate information to the examinees. These results may not be used as official confirmation of achievement. While all efforts have been made to make the information available on this website as authentic as possible, DAYANANDA SAGAR COLLEGE OF ENGINEERING or any of their staff will not be responsible for any loss caused by shortcoming, defect or inaccuracy in the information provided on this website. Please contact the Controller of Examinations for the final official results.

Downloaded from www.dayanandasagar.edu/dsce

Jun 21, 2019 9:13 PM