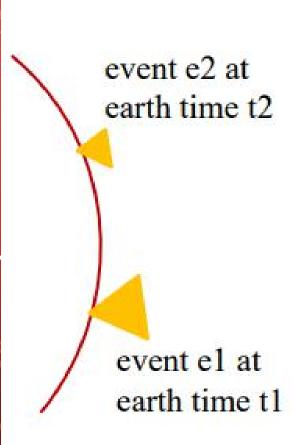
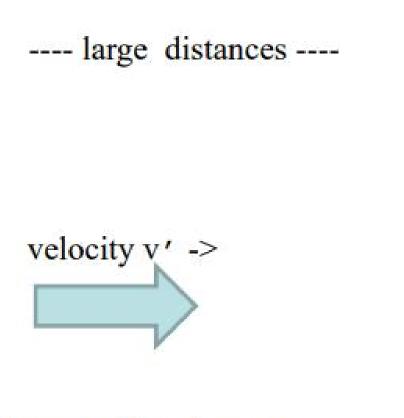
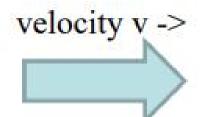
TIME IN DISTRIBUTED SYSTEM







The spaceships observe

- e1 and e2 in different time frames
- different values for time elapsed between e1 and e2
- e1 and e2 (from the same source) in the same order.





----- enormous distances -----

velocity v'

spaceship observes e1 before e2 velocity v



spaceship observes e2 before e1

TIME IN SPACE

PHYSICAL CLOCK

Based on frequency of oscillation of a quartz crystal

Each computer has a timer that interrupts periodically. Timer consists of two register

- Counter: counts the oscillation (decrement on each oscillation)
- Reset: stores the reset value of counter

Clock drift: in practice, the number of interrupts per hour varies slightly in the fabricated devices, also with temperature, so clocks may drift, typically 1/106 (1 sec in 11.6 days)

Timers can be set from transmitted UTC.

LEAP SECOND

LITC Data	UTC Time	Difference TAI
UTC Date 30/06/1972		11 secs
30/00/19/2	23:33:60	• •
31/12/1972	23:59:60	12 secs
31/12/1973	23:59:60	13 secs
31/12/1974	23:59:60	14 secs
31/12/1975	23:59:60	15 secs
31/12/1976	23:59:60	16 secs
31/12/1977	23:59:60	17 secs
31/12/1978	23:59:60	18 secs
31/12/1979	23:59:60	19 secs
30/06/1981	23:59:60	20 secs
30/06/1982	23:59:60	21 secs

		Difference TAI
UTC Date	UTC Time	vs. UTC
30/06/1983	23:59:60	22 secs
30/06/1985	23:59:60	23 secs
31/12/1987	23:59:60	24 secs
31/12/1989	23:59:60	25 secs
31/12/1990	23:59:60	26 secs
30/06/1992	23:59:60	27 secs
30/06/1993	23:59:60	28 secs
30/06/1994	23:59:60	29 secs
31/12/1995	23:59:60	30 secs
30/06/1997	23:59:60	31 secs
31/12/1998	23:59:60	32 secs
31/12/2005	23:59:60	33 secs
31/12/2008	23:59:60	34 secs
30/06/2012	23:59:60	35 secs
30/06/2015	23:59:60	36 secs
31/12/2016	23:59:60	37 secs

PHYSICAL CLOCK **SYNCHRONIZATI** ON

Cristian's Algorithm

Berkeley Algorithm

Network time protocol

CAUSAL ORDERING

- HB1: if \blacksquare process pi : e \rightarrow ie', then e \rightarrow e'
- HB2: For any message m, send(m) → receive(m)
 - Where send(m) is the event of sending the message and receive(m) is the event of receiving it
- HB3: if e, e' and e'' are events such that e → e' and e' → e''
 then e → e''

LOGICAL CLOCK (LAMPORT CLOCK)

LC1: L_i is incremented before each event is issued at process p_i:
 L_i:=L_i+1

• LC2:

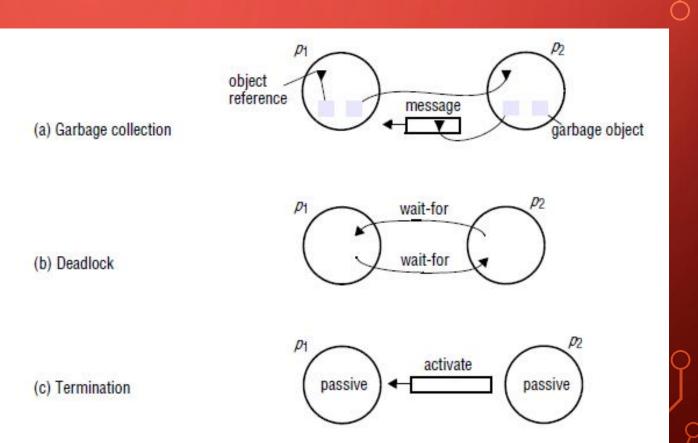
- When a process pi sends a message m, it piggybacks on m the value t = Li .
- On receiving (m, t), a process p_j computes $L_j := max(L_j, t)$ and then applies LC1 before timestamping the event receive(m).

VECTOR CLOCK

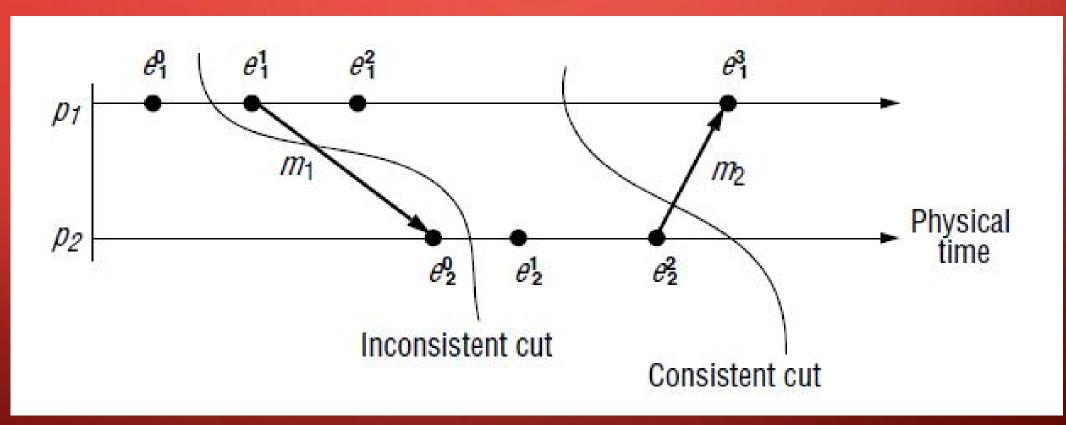
- VC1: Initially, Vi[j] = 0 , for i,j = 1,2...,N.,
- VC2: Just before pi timestamps an event, it sets Vi[i] := Vi[i] + 1.
- VC3: pi includes the value t = Vi in every message it sends.
- VC4: When pi receives a timestamp t in a message, it sets Vi[j] := max(Vi[j], t[j]), for j = 1,2...,N. Taking the component wise maximum of two vector timestamps in this way is known as a merge operation.

GLOBAL STATES

- Usages
 - Distributed garbage collection
 - Distributed deadlock detection
 - Distributed termination detection
 - Distributed debugging



CUTS



'SNAPSHOT' ALGORITHM

Ву

Chandy and Lamport

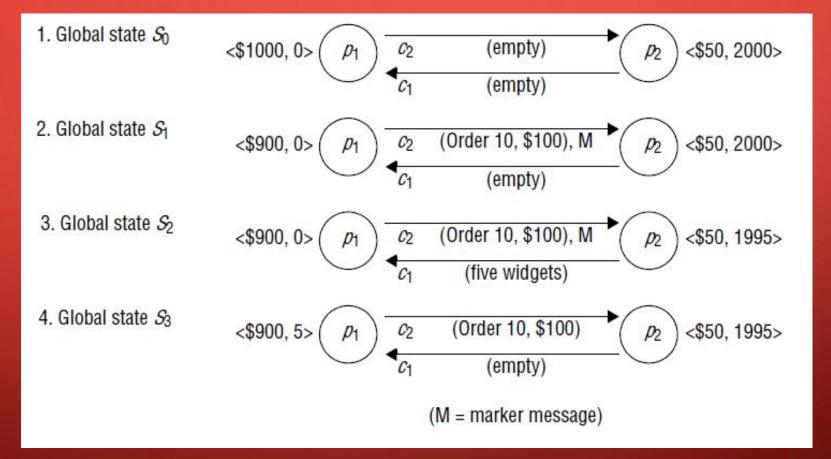
Assumptions

- Neither channels nor processes fail communication is reliable so that every message sent is eventually received intact, exactly once.
- Channels are unidirectional and provide FIFO-ordered message delivery.
- The graph of processes and channels is strongly connected (there is a path between any two processes).
- Any process may initiate a global snapshot at any time.
- The processes may continue their execution and send and receive normal messages while the snapshot takes place.

ALGORITHM

```
Marker receiving rule for process p;
   On receipt of a marker message at p_i over channel c:
       if (p_i) has not yet recorded its state) it
            records its process state now;
            records the state of c as the empty set;
            turns on recording of messages arriving over other incoming channels;
       else
            p_i records the state of c as the set of messages it has received over c
            since it saved its state.
       end if
Marker sending rule for process pi
  After p_i has recorded its state, for each outgoing channel c:
       p_i sends one marker message over c
       (before it sends any other message over c).
```

EXAMPLE



THANK YOU