College of Engineering, Pune (An Autonomous Institute of Government of Maharashtra)

(An Autonomous Institute of Government of Maharashtra)
Department of Electrical Engineering

T-2 Examination-2022

Course:-Basic Electrical Engineering (E-Group)

Programme: F. Y. B. Tech. (Division 6-10)

Max. Marks: 20

Date: 24.07.2022

Time: 4-5 pm

Instructions:-

1. All questions are compulsory.

- 2. Make necessary assumptions and assume suitable data wherever required.
- 3. Non programmable calculator is allowed.
- 4. Figures to the right indicate full marks

Q. 1	Rewrite the following with correct answer.	8
(i)	In delta connection, line current lags behind the phase current bydeg.	
(ii)	A circuit with a resistor, inductor and capacitor in series is resonant at f ₀ Hz. If all the component values are now doubled, the new resonant frequency isHz. a) 2 f ₀ b) still f ₀ c) f ₀ /2 d) f ₀ /4	
(iii)	To a highly inductive circuit, a small capacitance is added in series. The angle between voltage and current will a) increase b) decrease c) remain nearly the same d) become indeterminant	
(iv)	With reference to the applied voltage, current in a circuit is expressed as i = 3.2sin (314t-30°). The circuit is & its power factor is a) inductive, 0.866 lead b) capacitive, 0.866 lead c) inductive, 0.866 lag d) capacitive, 0.866 lag	
(v)	A pure inductive coil of 20 mH is connected across a dc circuit. Its impedance will be	
(vi)	The voltage phasor of a circuit is 10<15° V and the current phasor is 2<-45° A. The active and reactive powers in the circuit arerespectively. a) 10 W and 17.32 VAr b) 5 W and 8.66 VAr c) 20 W and 60 VAr d) 20√2 W and 10√2 VAr	

