Pranav Salunkhe

SY Elex

Task 1 (1.2)

Member 5 - S-R flip-flop Using NAND gate

|  |  |  |  |
| --- | --- | --- | --- |
| **S** | **R** | **Q** | **Q\*** |
| *0* | *0* | *Invalid* | |
| *0* | *1* | *1* | *0* |
| *1* | *0* | *0* | *1* |
| *1* | *1* | *No change* | |

OUTPUT







