Section 1: Simple OPS Analysis

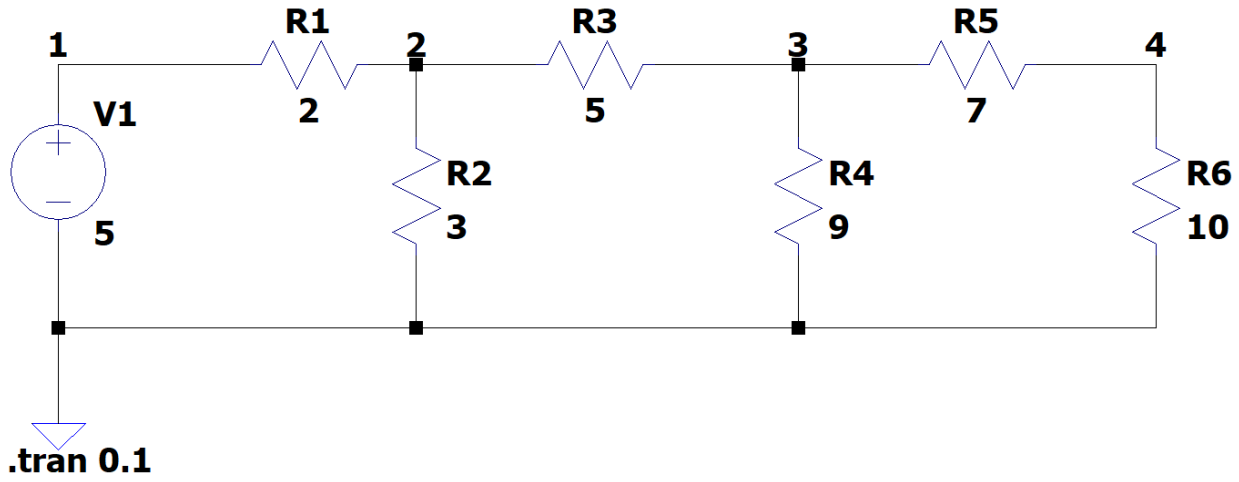

Figure 1: IRV circuit that was analyzed using OPS analysis in Python from LTspice

Figure 2: Timing diagram of the IRV circuit that was analyzed with OPS in Python from LTspice

```
OP simulation results for 'Simple Example Circuit'.
Run on 2022-10-01 15:35:10, data file C:\Users\pskon\AppData\Local\Temp\tmprdgc_pgf.op.
Variable    Units        Value         Error    %
----------  -------  ---------  ------------  ---
VN1         V        5          -5.00022e-12    0
VN2         V        2.7021     -2.70228e-12    0
VN3         V        1.46085    -1.46083e-12    0
VN4         V        0.859325   -8.59313e-13    0
I(V1)       A       -1.14895     0              0
```

Figure 3: Output for the OPS analysis of the IRV circuit from figure 1

The voltage measurements at node 1, 2, 3, and 4 from figure 3 match the gain/voltage measurements values visually at the same nodes in LTspice in figure 2; OPS analysis provides greater precision with decimal place values going up to one hundred thousandths. So, both methods can be used for simple circuit design and analysis.

Section 2: Simple AC and Trans Analysis

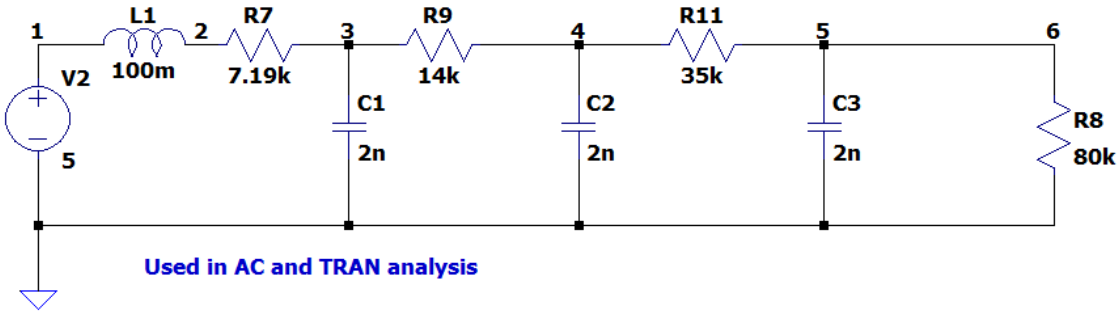

Figure 4: RCL circuit used in AC and TRAN analysis

Section 3: Simple PZ (Pole-Zero) analysis

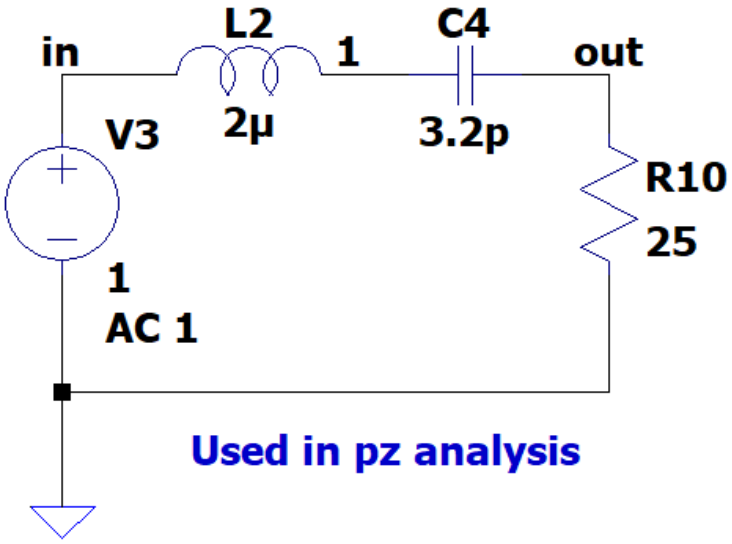

Figure 5: RCL circuit used in pz analysis