

connection oriented \Rightarrow TCP/IT

connectionless \Rightarrow UDP.

(router doesn't
do any
reordering.)

G-10 for
uplink
connect.

Transmission basis: spectrum \rightarrow actual freq range
bandwidth \rightarrow a diff b/w highest & lowest
effective bandwidth.

when I have
continuous signal,
I can't send it
directly, I sample
it. if freq f
then sample it $2f$.

Nyquist \rightarrow sampling rate
 \hookrightarrow channel capacity.

Shannon capacity

dB, SNR, attenuation

Media: twisted pair & OFC (types)

range, comparison.

(practical advantage)
(distance constraints / interference)

Direct
Communicatⁿ b/w 2 machine on single link

(defmatⁿ,
why needed)

→ synchronizatⁿ

(write anything
you assume
which is not
in question)

① If blocks are out of sync
by $x\%$, bit duratⁿ is
 y & sampled at
receiver after $p\%$
fraction of the bit, how
many bits before you lose
a bit?

② What is given y, p &
size of one frame,
what is the max difference
between the clocks you
can tolerate?

bit stuffing (work out simple examples)

Encoding: desirable qualities

(given bit streams, show
the encoding)

(given the encoding find bit stream.)

(transition more
bandwidth
use data rate)

if size
length
burst
of
Can

→ given bit stream & encoding, how many transitions will occur?

ASK, FSK, PSK/DPSK: definitⁿ with diagram only.

Error detection: parity
CRC
Checksum

} work out examples

if size of length k chosen, burst error of length k can be detected.

what errors CRC/Checksum can tolerate? no need to remember for exam.

- ① Given bit stream, generate CRC/Checksum
- ② Given CRC scheme, find bit stream received, see if it correct.

flow control

stop & wait (different variants of link efficiency)

- Practise with non negligible ack size, processing delays. at receiver, repetition specified delays in between etc
→ principle is same

ARQ : practice some scenarios.

stop & wait : what if bit error rate is 0.1? ^(t)

frame size given,
what is the
expected line
efficiency.

→ calc prob of successful
transmission

→ calc expected no. of
transmission before the
message success.

if chance of success $\frac{1}{2}$

∴ you need to send twice
to guarantee success

∴ expected time b/w two
successive send (twice original)

$$\begin{aligned} \text{prob that transmission fails} &= \text{frame damaged} + \text{ack damaged} \\ &= 1 - \left(\frac{1-p}{2} \right)^{\text{frame size}} + 1 - \left(\frac{1-p}{2} \right)^{\text{ack.}} \end{aligned}$$

no error

window size \Rightarrow what & why? (max)

Medium sharing :

FDM — given n signals each with bandwidth requirements & given guard band size, what is the bandwidth of the link needed?

$(n-1)$ guard band

TDM : given no. of senders with data rate, given frame format what minimum data rate to be supported.

CSMA : basic definition

if $p = 0.5$ in p -persistent, given n senders what is the prob that at least one will transmit, at $t = 0$ successfully.

CSMA/CD : slot time definition. (time to transmit min size frame)

minimum frame length computation.
(one bit to travel, one to track). $\frac{2D}{V} \times R$.

(doesn't matter)

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add processing delays, repeaters with delays & so on.

(time for one bit to go & one bit to come) $\times R$.

Given slot time & the fact that a station/node suffers 3 collision before it is transmitted successfully, what is the min & max time it could have waited

first collⁿ 0-1 time slot

second collⁿ 0-3 time slot

(binary explosion base of 2)

third collⁿ 0-7 time slot.

min = 0 max = 11

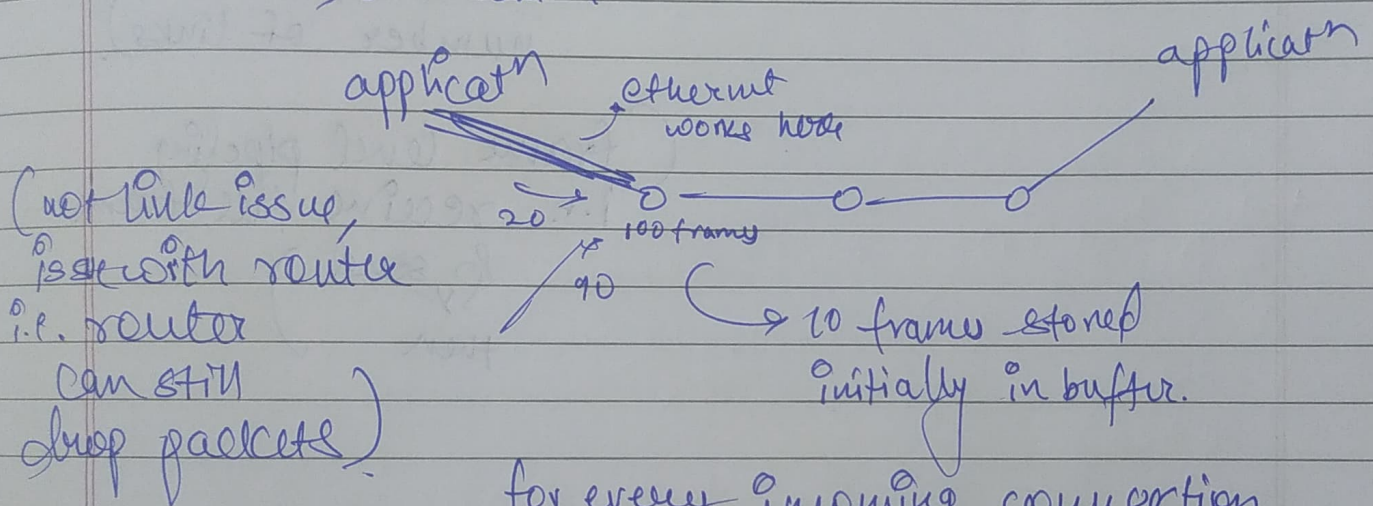
Hidden terminal: why doesn'tⁿ RTS/CTS solve it fully?

(CTS might get lost)

LANs: ① why no flow control & error control in ethernet?

→ data link layer

→ error control



for every incoming connection, we need to maintain window for flow & error control.

② Minimum how many ~~switches~~ switches will be needed to connect n machines.

what if we say delay per switch (processing delay at switch is p)

& total delay can't be more than T ?

How end of frame is detected.

Layering : name

packet switching : practice last picture

(frame size,
header,
number of links)

(frame level pipelining
i.e. receive full frame
& send
then)