ASIC DIGITAL DESIGN-Verification(SERDES)

6 months Internship at Synopsys Hyderabad

Done by:

Name: Pranes S

Roll no.: 108121091

Department: ECE- A

Guide's Signature:

Srinivasulu Jogi

Manager's Signature:

Hanumantha Rao

Work Done Till date:

- Learning **System Verilog** and practicing the codes in EDA Playground.

- Getting familiar with **Linux Commands**

- Also going through the physical Layer Protocol - **PCIe**(Peripheral Component Interconnect Express)

- Studying the function of **SerDes**(Serializer/ Deserializer)