BONAFIDE CERTIFICATE

This is to certify that the project titled '**Create Design under test in Verilog, create a testbench environment for I2C protocol with assertion and a Perl script for I2C bus log analyzer**' at **Synopsys (Hyderabad)** as a part of 6 months internship is a bonafide record of the work done by

Praens S (108121091)

in partial fulfilment of the requirements for the award of the degree of **Bachelor of Technology in Electronics and Communication Engineering** of the **NATIONAL INSTITUTE OF TECHNOLOGY, TIRUCHIRAPPALLI**, during the year 2024-2025.

Signature of Manager | Guide | Head of the Department
Hanumantha Rao | **Dr. Srinivasulu Jogi** | **Dr. M.Bhaskar**

Project Viva-voce held on ………………………………

Internal Examiner | **External Examiner**