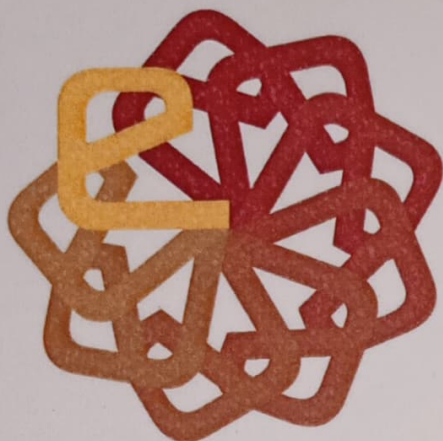ERTS Lab
Department of Computer Science and Engineering
Indian Institute of Technology Bombay,
Powai, Mumbai-400 076.

INTERNSHIP CERTIFICATE

This is to certify that **Pranes S**, student from **National Institute of Technology, Tiruchirappalli** has undertaken Internship at e-Yantra, IIT Bombay working on a project entitled: **Origami with Electronics** during the period from **22nd May 2023 to 7th July 2023** and has successfully completed the same.

Prof. Kavi Arya
Principal Investigator, e-Yantra
Professor
Department of Computer Science and Engineering
Indian Institute of Technology Bombay

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Engineering a better tomorrow

Intern Evaluation

The project was focused on designing innovative applications for various domains such as Embedded Systems, Bio-medical, and Agriculture. The project involved utilising a range of controllers including ATmega2560 Development Board, LPC2148 Development Board, P89V51RD2 Development Board, Raspberry-Pi, STM32 Nucleo board, Tiva Launchpad, Altera Cyclone IV FPGA (DE0-Nano), ESP8266 Development Board, and ESP32 Development Board. The main task was to develop crafty applications for these systems and create tutorials and hands-on experiments as finished products.

Pranes actively contributed to the development of experiments and tutorials involving Tiva Launchpad, Altera Cyclone IV FPGA, DE0-Nano, and P89V51RD2 Board. These experiments encompassed I/O interfacing, working with ADC, PWM, interrupts, and communication protocols with different controllers. Additionally, Pranes played a significant role in constructing a Line and Wall Follower robot, designing an Oscilloscope. In addition to his contributions, he was also involved in designing an Oscilloscope, which was subsequently laser-cut for fabrication.