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Dr. G. Lakshminarayanan
Professor

DATE: 07-03-2023

Certificate

This is to certify that **Mr. Pranes S**, Roll No. 108121091, Department of Electronics and Communication Engineering, National Institute of Technology, Tiruchirappalli has worked on a project titled **"Design of brain floating point based Systolic Array Architectures for convolution"** under my guidance in the Wireless System Design Laboratory of Electronics and Communication Engineering department, NIT, Tiruchirappalli from 26.12.2022 to 12.02.2023. The work carried out by him was good.

(Dr.G.Lakshminarayanan)