RS232-T200

- **Trojan Description**
  - Trojan trigger is counter inserted in the receiver part of micro-UART core. Trojan counter's clock is negative of the system clock, and Trojan counter's enable is comparator over 16 internal signals.

- **Trojan Taxonomy**
  - Insertion phase: Design
  - Abstraction level: Register-transfer level (RTL)
  - Activation mechanism: Internally conditionally triggered
  - Effects: Reduce design reliability
  - Physical characteristics: Functional

Functional

The number of Received	Trojan activation	Payload Change	Simulation Length (No. Clock)	Length of each Receive
1 112 583	4 332	Not Available	168 000 000	150