Verilog HDL:

Timing and Delays

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Outline

- `timescale
- \$timeformat
- \$printtimescale
- Measure time

timescale

- Defines time units and precision for simulation
- Syntax: timescale <time_unit> / <time_precision>
- Example: timescale 1ns / 1ps
 - > Time unit: 1 nanosecond.
 - Time precision: 1 picosecond
- Affects simulation timing and delay calculations.

Example: timescale

```
`timescale 1ns/1ps
     module timescale example;
 2
       initial begin
          #10 $display("Time: %t", $time);
 4
 5
       end
     endmodule
 6
     module tb_timescale_example;
 8
       timescale_example uut();
 9
       initial #20 $finish;
10
11
     endmodule
     Output:
      Time: 10
```

\$timeformat

- Controls how simulation time is displayed in output
- Syntax:
 - \$timeformat(unit, precision, suffix, min_field_width);
- Parameters:
 - unit: Base time unit (e.g., 0 for seconds, -9 for nanoseconds)
 - precision: Number of decimal places
 - suffix: Character string to display after time (e.g., "ns")
 - min_field_width: Minimum width of displayed time value
- Example: \$timeformat(-9, 2, " ns", 10);

Example: \$timeformat

```
module timeformat example;
       initial begin
         $timeformat(-9, 2, " ns", 10);
         #5 $display("Formatted Time: %t", $time);
 4
 5
       end
     endmodule
 6
 8
     module tb timeformat example;
       timeformat_example uut();
10
       initial #10 $finish;
11
     endmodule.
```

Output:

```
Formatted Time: 5 ns
```

\$printtimescale

- Displays the current timescale settings of a module
- Syntax: \$printtimescale(<module_name>)

Output:

```
Time scale of (printtimescale_example) is 1 ns / 1 ps
```

Comparison Between \$time, \$stime, and \$realtime

Function	Data Type	Precision	Use Case
\$time	Integer	Low	Coarse time measurement
\$stime	32-bit Integer	Medium	Medium precision timing
\$realtime	Real Number	High	High-precision time measurement

Comparison Between \$time, \$stime, and \$realtime

```
module time_comparison;
 1
        initial begin
          $display("$time: %t", $time);
          $display("$stime: %0d", $stime);
 4
 5
          $display("$realtime: %0.3f", $realtime);
 6
       end
     endmodule
 8
 9
     module tb_time_comparison;
10
       time comparison uut();
11
        initial #10 $finish;
12
     endmodule
     Output:
      $time: 0
      $stime: 0
```

\$realtime: 0.000

Example 1 - Using \$time and \$realtime in a Counter

```
1
     module counter example;
       reg clk;
       integer count = 0;
 4
       initial begin
         clk = 0;
6
         forever #5 clk = ~clk;
       end
       always @(posedge clk) begin
8
         count = count + 1;
10
         $display("Time: %d ns, Count: %d", $time, count);
11
       end
12
     endmodule
13
14
     module tb counter example;
                                                Output:
15
       counter example uut();
16
       initial #50 $finish;
                                                 Time: 5 ns, Count: 1
                                                 Time: 15 ns, Count: 2
     endmodule
17
                                                 Time: 25 ns, Count: 3
```

Example 2 - Measuring Delay Using \$realtime

```
module delay measure;
 1
 2
       reg signal;
 3
       real start time, end time, delay;
       initial begin
 4
 5
         signal = 0;
 6
         #7 signal = 1;
         start time = $realtime;
 8
         #12 signal = 0;
 9
         end time = $realtime;
10
         delay = end time - start time;
         $display("Measured delay: %0.3f ns", delay);
11
12
       end
     endmodule
13
14
                                          Output:
15
     module tb_delay measure;
16
       delay measure uut();
       initial #30 $finish;
17
                                           Measured delay: 12.000 ns
18
     endmodule
```

Example 3 – Time Test

```
1
     module time test;
       initial begin
2
         $timeformat(-9, 2, " ns", 10);
         #5 $display("Current Simulation Time: %t", $time);
5
         #10 $display("Current Simulation Time: %t", $time);
6
       end
7
     endmodule
8
     module tb_time_test;
9
       time_test uut();
10
11
       initial #20 $finish;
12
     endmodule
```

Output:

```
Current Simulation Time: 5 ns
Current Simulation Time: 15 ns
```

Summary

- Accurate timing is essential for simulation and verification of digital designs.
- `timescale sets the resolution and precision of simulation delays.
- \$timeformat customizes the format of time values in output displays.
- \$printtimescale helps verify module timescale settings during simulation.
- \$time, \$realtime, and \$stime allow designers to measure and track simulation time, useful in debugging and testbench analysis.



Thank you!

Happy Learning