# Major Project

**VLSI-based Simple Encryption Algorithm**

- **Objective**: Design a VLSI (Very-Large-Scale Integration) module to implement a basic encryption algorithm such as XOR-based encryption. Simulate the encryption and decryption processes to understand cryptographic systems at the hardware level.
- **Features**:
    - Design the encryption and decryption modules using basic XOR gates.
    - Implement key management for encryption and decryption.
    - Simulate the encryption and decryption process using Verilog.
- **Tools**: Verilog for design, ModelSim for simulation.