EXPT-8

LINE AND LOAD REGULATION USING ZENER DIODE

AIM

To understand the application of Zener voltage in the area of Voltage regulation using OrCAD Pspice software.

APPARATUS/TOOLS REQUIRED

ORCAD / Capture CIS --> **Analog Library – R,**

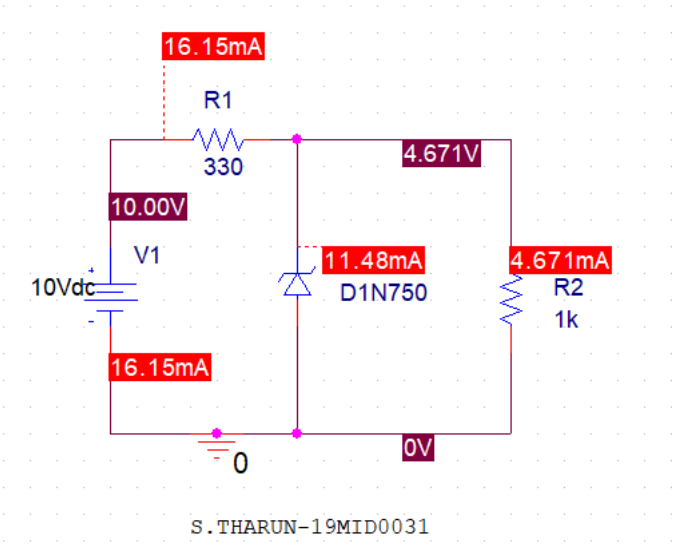
Source Library – Vdc, Idc &

Diode Library - Zener diode D1N750

Ground (GND) – 0 (zero)

Simulation Settings: **Analysis Type - Bias Point**

CIRCUIT DIAGRAM:



THEORY

Zener diodes are generally used in the reverse bias mode. You have seen already in one of your previous experiments that the Zener diode has a region of almost a constant voltage in its reverse bias characteristics, regardless of the current flowing through the diode. This voltage across the diode (Zener Voltage, V_Z) remains nearly constant even with large changes in current through the diode caused by variations in the supply voltage or load. This ability to control itself can be used to great effect to regulate or stabilize a voltage source against *supply* or *load* variations.

The output voltage across the load resistor, V_L, is ideally equal to the Zener voltage and the load current, I_L, can be calculated using Ohm's law:

$$V_L = V_Z \text{ and } I_L = \frac{V_L}{R_L}$$

Thus, the Zener current, I_Z, is

$$I_Z = I_S - I_L .$$

Now that you have constructed a basic power supply, its quality depends on its load and line regulation characteristics as defined below.

Line Regulation: It indicates how much the load voltage varies when the input line voltage changes. Quantitatively, it is defined as:

$$Line\ Regulation = \frac{\Delta V_o}{\Delta V_{IN}} \times 100$$

where $\Delta V_o = V_{L-high} - V_{L-low}$ = difference in the high and low output voltage, and

ΔV_{IN} = difference in the high and low input voltage

Load Regulation: It indicates how much the load voltage varies when the load current changes. Quantitatively, it is defined as:

$$Load\ regulation = \frac{V_{NL} - V_{FL}}{V_{FL}} \times 100\%$$

where V_{NL} = load voltage with no load current ($I_L = 0$) or a very high value of load resistance (1K or above) and

V_{FL} = load voltage with full load current or a very low value of load resistance.

The smaller the regulation, the better is the power supply.

PROCEDURE:

- ✓ Complete the rest part of the circuit as shown in the circuit diagram. Note down all the values of the components being used including the Zener breakdown voltage.
- ✓ Keeping R_L fixed, vary the input voltage and measure again the output DC voltage, current and input DC voltage. (Will be need for line regulation)
- ✓ Keeping input voltage suitably fixed, use different values of R_L and measure both the output DC voltage and current.
- ✓ Tabulate all your data and ***calculate percentage regulation in each case.***

OBSERVATIONS:

- ✓ Specifications of Zener diode: Breakdown voltage = 8 V
- ✓ R_S = 330Ω

TABLE:

(i) Line Regulation:

Load resistor = 1kΩ

Sl. No	Input DC Voltage (V_{IN}) in Volts	Output DC Voltage (V_L) in Volts
1	1	0.7519
2	2	1.504
3	3	2.255
4	4	3.002
5	5	3.721
6	6	4.326
7	7	4.581
8	8	4.630
9	9	4.654
10	10	4.671
11	11	4.683
12	12	4.693
13	13	4.701
14	14	4.709
15	15	4.715

(ii) Load Regulation:

Input DC voltage = 10V

Sl. No	Load Resistance in ohms	Output DC Voltage (V_L) in Volts
1	10	0.2941
2	30	0.8333
3	50	1.316
4	100	2.325
5	200	3.753
6	250	4.228
7	300	4.519
8	400	4.620
9	500	4.643
10	1k	4.671
11	2k	4.680
12	3k	4.683
13	5k	4.685
14	10k	4.687
15	15k	4.688

MANUAL CALCULATIONS AND WORKS:

(i) LINE REGULATION:

$$\Delta V_o = V_{L-high} - V_{L-low}$$

$\Delta v_0 = 4.715 - 0.7519 = 3.9631$

ΔV_{IN} = difference in the high and low input voltage

$\Delta v_{in} = 15 - 1 = 14$

$$Line\ Regulation = \frac{\Delta v_0}{\Delta v_{in}} x100$$

$$= \frac{3.9631}{14} x100$$

$$= 28.3\%$$

(ii) LOAD REGULATION:

$$Load\ Regulation = \frac{v_{NL} - v_{FL}}{v_{FL}} \times 100$$

V_{NL} = load voltage with a very high value of load resistance

$v_{NL} = 15000$

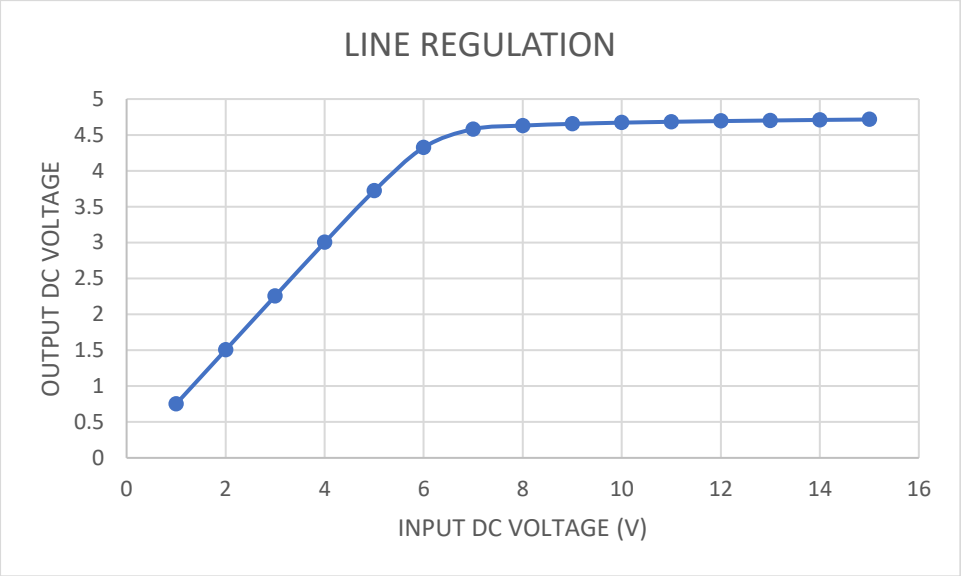
V_{FL} = load voltage with a very low value of load resistance

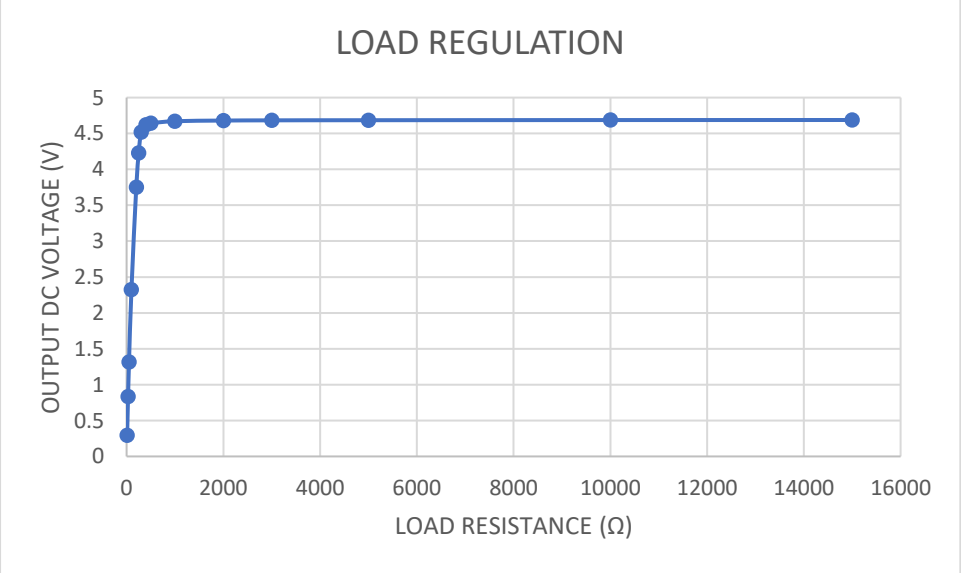
$v_{FL} = 10$

$$Load\ Regulation = \frac{4.688 - 4.519}{0.2941} \times 100$$

$$= 57.46\%$$

GRAPHS:





RESULT:

The Voltage regulation application of Zener diode is understood and proved using OrCAD Pspice Software.