

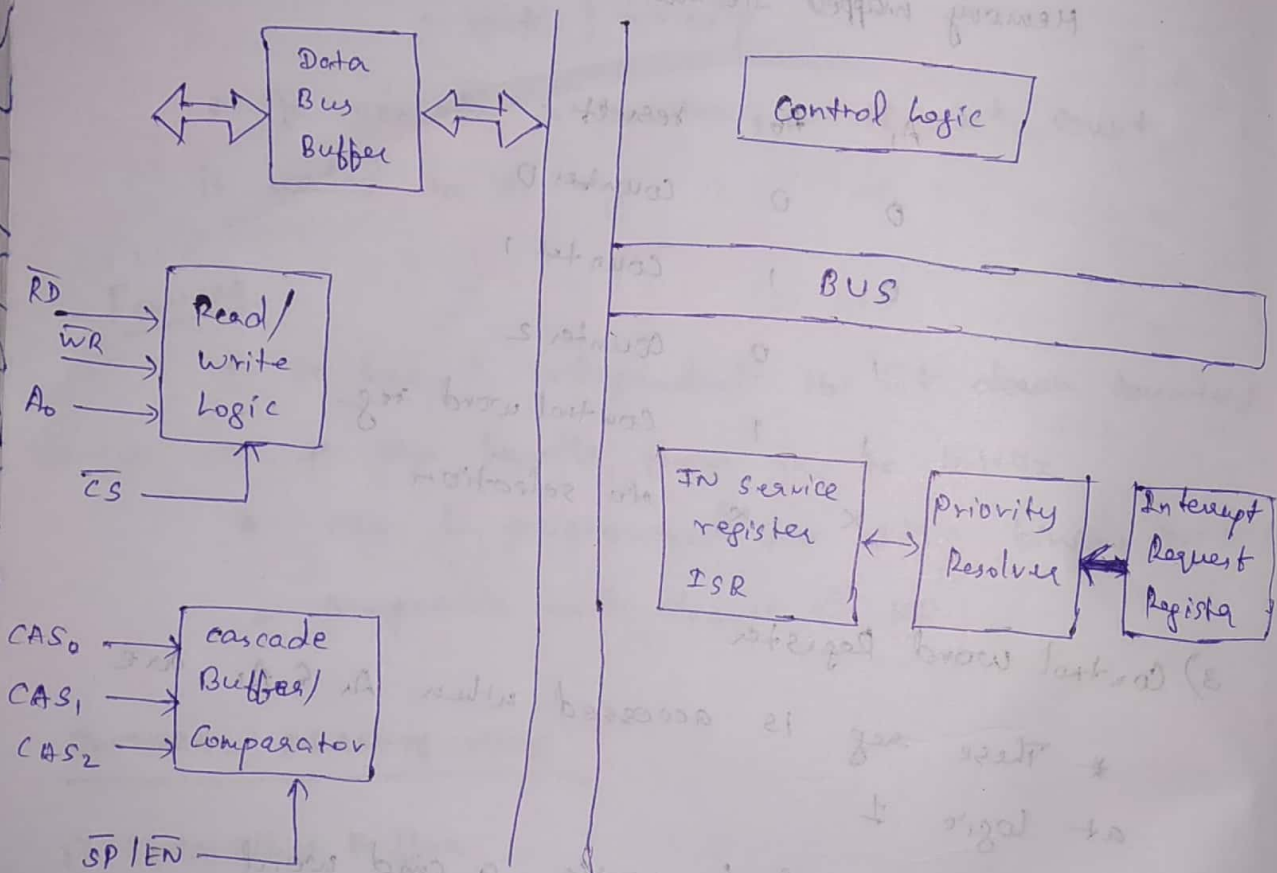
21/03/2022

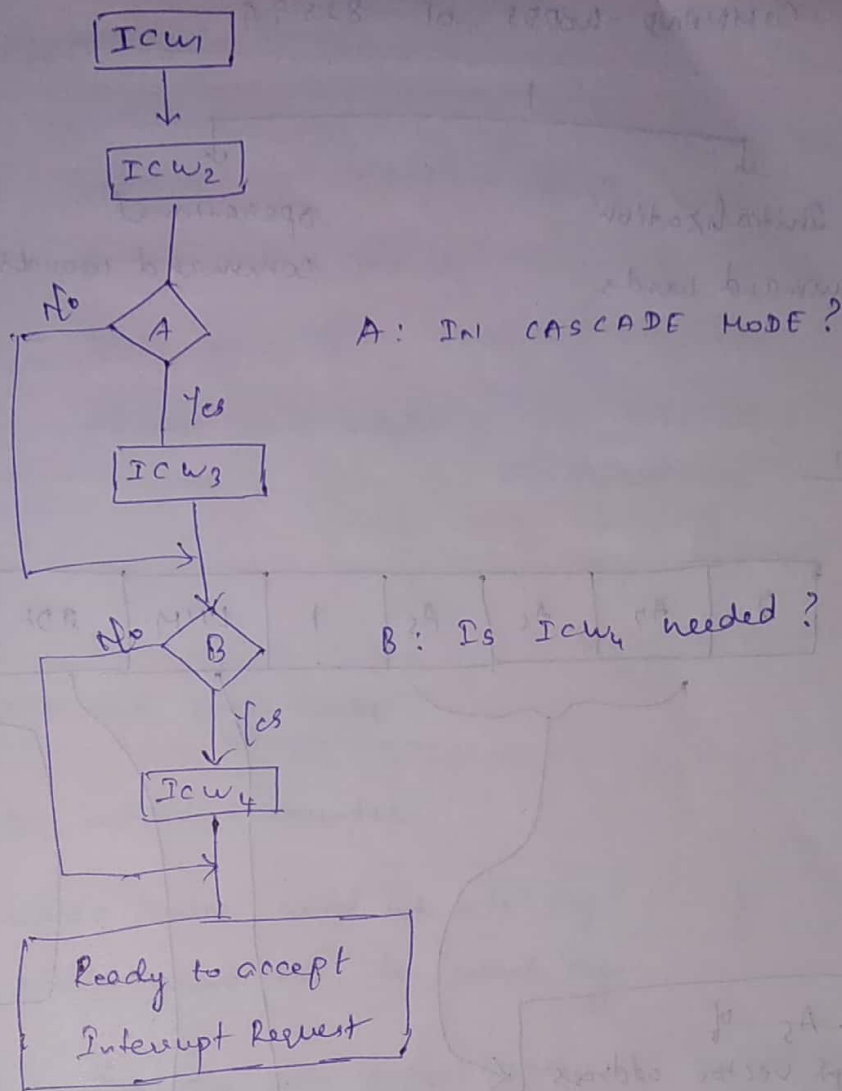
## 8259A

### PRIORITY INTERRUPT CONTROLLER

40 pins  $\rightarrow$  2 pins for ~~interrupt~~ interrupt

#### ARCHITECTURE





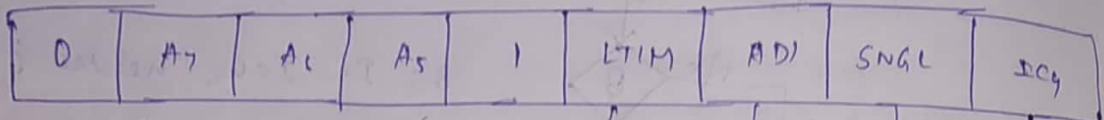
0000	0000	1010	0000	left shift
0000	0001	0100	0000	
0000	0010	1000	0000	
0	2	8	0	

# COMMAND WORDS OF 8259A

Initialization  
command words

operational  
command words

ICW1



A7 - A5 of  
Interrupt vector addresses  
MCS 80/85 mode only

I = Level triggered  
0 = edge triggered

I = ICW4  
0 = no ICW4

1 - Single (8)  
0 - Cascade (64)

Call Address Interval  
1 - Interval of 1 byte  
0 - " " 8 bytes

ICW3 → A0 = 1

## CAT-2

- 1) 8254  $\left\{ \begin{array}{l} \text{MODE 0 \& ALP} \\ \text{MODE 3 \& ALP} \end{array} \right.$
- 2) 8255  $\left\{ \begin{array}{l} \text{Internal Block Diagram \& Bit set/Reset} \\ \text{control format} \\ \text{MODE 1 operation diagram \& mode format} \\ \text{control word} \end{array} \right.$
- 3) 8251 (UART)  $\rightarrow$  ALP to transmit serial data
- 4) 8254 (PIC)  $\left\{ \begin{array}{l} \text{Internal Block Diagram \& Initialization flowchart} \\ \text{ICW1, ICW2, ICW3, ICW4} \end{array} \right.$
- 5) D-A \& A-D

### COMMAND WORDS OF 8259A

A <sub>0</sub>	D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>
1	T <sub>7</sub>	T <sub>6</sub>	T <sub>5</sub>	T <sub>4</sub>	<del>A<sub>3</sub></del> T <sub>3</sub>	A <sub>10</sub>	A <sub>9</sub>	A <sub>8</sub>

what will be the ICW<sub>2</sub> when your PIC is programmed with base address 08H?

$$\begin{array}{rcl}
 \text{CB} & = & \underline{1100} \underline{1000} \quad - \text{IR}_0 \\
 & \downarrow & \\
 & \text{don't change} & \\
 & 11001 \underline{111} & - \text{IR}_7
 \end{array}$$



							VECTOR ADDRESS
IR0	-	0000	0000	1100	1000	- C8H	0320 : 0323
IR1	-	0000	0000			- C9	0324 : 0327
IR2	-	0000	0000			- CA	0328 : 032B
IR3	-	0000	0000			- CB	032C : 032F
IR4	-	0000	0000			- CC	0330 : 0333
IR5	-	0000	0000			- CD	0334 : 0337
IR6	-	0000	0000			- CE	0338 : 033B
IR7	-	0000	0000			- CF	033C : 033F

### Vector address

IR0 → Either shift left 2 times  
(or)  
multiply by 4

IR0  
C8 →

0000	0000	1100	1000
0000	0001	1001	0000
0000	0011	0010	0000
0	3	2	0

left shift  
0320 : 0323

IR1  
C9 →

0000	0000	1100	1001
0000	0001	1001	0010
0000	0011	0010	0100
0	3	2	4

0324 : 0327

### Master mode ICW3

A <sub>0</sub>	D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>
1	S <sub>7</sub>	S <sub>6</sub>	S <sub>5</sub>	S <sub>4</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	S <sub>0</sub>

$S_n = 1 \rightarrow IR_n$  Input has slave

$S_n = 0 \rightarrow IR_n$  input doesn't have slave

### slave mode ICW3

A <sub>0</sub>	D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	D <sub>4</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>
1	0	0	0	0	0	IR <sub>2</sub>	IR <sub>1</sub>	IR <sub>0</sub>

D<sub>2</sub> D<sub>1</sub> D<sub>0</sub> - 000 to 111 for IR<sub>0</sub> to IR<sub>7</sub>

(000)

slave 1 to slave 8

### ICW3 slave

If slave is connected to IR<sub>6</sub> of master,

then ICW3 = 0000 0110

### ICW4

1	0	0	0	SMN	BUF	M/3	AEOL	MPM
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