

Microprocessor and Interfacing Techniques
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IF Stack Segment=5D270H and BP=2C30H, the BIU will form the physical address *

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- ☐ 5D270H
- ☐ 02C30H
- ☒ 5FEA0H
- ☐ 5D2C3H

For an 8-bit parallel comparator A/D converter comparators is needed *

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- ☐ 127
- ☒ 255
- ☐ 1023

A Certain PIC has ICW2 programmed for C8H. what is the type number output by an interrupt request on IR5? *

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- ☒ CDH
- ☐ CCH
- ☐ C9H
- ☐ CFH

Choose the correct instruction for indexed addressing mode *

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- ☐ MOV AX,[5000]
- ☐ MOV AX,50H[BX
- ☐ MOV AX,0005
- ☒ MOV AX,[SI]

when it is desired to convert the results of a decimal addition to ASCII the instruction should be used *

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- ☐ AAM
- ☒ AAA
- ☐ AAS

The size of code segment in the memory is *

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- ☐ 8Kb
- ☒ 64Kb
- ☐ 16Kb
- ☐ 32Kb

If port A is initialised as a handshake output port, then port C pins function as handshake signals *

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- ☐ PC3, PC4 and PC2
- ☐ PC0, PC1 and PC2
- ☒ PC3, PC6 and PC7

In 8086 the SAL instruction in effect divides the data by 2 *

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- ☒ False
- ☐ True

The CPU of the 8086 microprocessor is divided into an and a unit *

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- ☐ control, bus interface
- ☐ control, numeric execution
- ☒ execution, bus interface

What action is taken by 8255 if the control byte 0EH is written to the control port? *

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- ☐ only bit 7 of port C is set
- ☐ only bit 5 of port C is set
- ☒ only bit 7 of port C is reset