Continuous Assessment Test - II



Programme Name & Branch: M.Tech (Integrated)

Course Name & Code: CSI2005 Principles of Compiler Design

Exam Duration: 50+10 Min Slot:B2

Faculty Name: R KANNADASAN Maximum Marks: 30

Answer all Questions		
S.No.	Questions SET A	Course Outcome (CO)
1	Show that the following grammar is LR(1) but not LALR(1) S→Aa/bAc/Bc/bBa A→d B→d	Č03
2	Explain peephole optimization with a suitable example.	C04
3	Translate the following assignment statement into three address codes $A[i,j] := B[i,j] + C[A[k,1]] + D[i+j]$	CO5
	SET B	
1	Show that the following grammar is LR(1) but not LALR(1) A→BrC/CpBa C→w	CO3
	$B \rightarrow q$ Terminals { a,r,p,w,q }	
2	Explain the DAG representation of the basic block 1. t ₁ := 4 * i 2. t ₂ := a[t ₁] 3. t ₃ := 4 * i 4. t ₄ := b[t ₃] 5. t ₅ := t ₂ * t ₄ 6. t ₆ := prod + t ₅ 7. prod := t ₆ 8. t ₇ := i + 1 9. i:= t ₇ 10. if i <= 20 goto (1) Suggest any two solutions to minimize/avoid temporaries generated during three address code generation.	C04
3	Explain the Function preserving transformations.	CO5