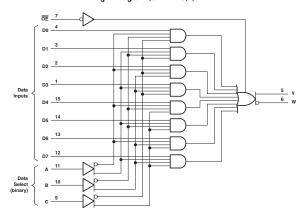
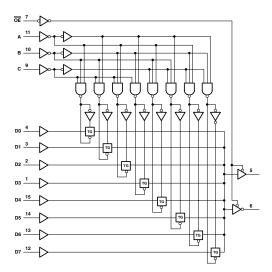
DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

- 3-State Version of '151
- 3-State Outputs Interface Directly with System Bus
- Perform Parallel-to-Serial Conversion
- Complementary Outputs Provide True and Inverted Data

Logic Diagram (SN74ALS,F)



Logic Diagram (SN74HC)



FUNCTION TABLE (SN74)

		OUTPUTS					
S	ELEC	T	STROBE	v	w		
С	В	Α	OE		••		
Х	Х	Х	Н	Z	Z		
L	L	L	L	D0	D0		
L	L	Н	L	D1	<u>Z</u> <u>D0</u> <u>D1</u> <u>D2</u>		
L	Н	L	L	D2	D2		
L	Н	Н	L	D3	D3		
Н	L	L	L	D4	D4		
Н	L	Н	L	D5	D4 D5		
Н	Н	L	L	D6	D6		
Н	Н	Н	L	D7	D7		

ELECTRICAL CHARACTERISTICS AND RECOMMENDED OPERATING CONDITIONS

PARAMETER	MAX or MIN	TTL	LS	s	ALS	F	SN74 HC	CD74 HC	CD74 HCT	SN74 AC	CD74 AC	UNIT
lcc	MAX	62	12	85	14	24	0.08	0.16	0.16	0.16	0.16	mA
Іон	MAX	-5.2	-2.6	-6.5	-2.6	-3	-6	-4	-4	-24	-24	mA
lor	MAX	16	8	20	24	24	6	4	4	24	24	mA

SWITCHING CHARACTERISTICS

SWITCHING CHA	RACTERISTICS												
PARAMETER	INPUT	OUTPUT	MAX or MIN	TTL	LS	S	ALS	F	SN74 HC	CD74 HC	CD74 HCT	SN74 AC	CD74 AC
tplH	A, B, C (CD74: S ₀ , S ₁ , S ₂)	Y	MAX	45	45	18	18	9.5	51	74	63	18.2	18.2
tPHL .				45	45	19.5	24	7.5	51	74	63	18.2	18.2
tPLH .	A, B, C (CD74: S ₀ , S ₁ , S ₂)	W (CD74: Y)	MAX	33	33	15	24	12.5	51	74	63	19.6	19.6
tphl.				33	33	13.5	23	9	51	74	63	19.6	19.6
tPLH .	ANY D	Y	MAX	28	28	12	10	7	49	53	53	13.5	13.5
tPHL .	(CD74: ANYI)			28	28	12	15	5	49	53	53	13.5	13.5
tPLH .	ANY D	W (CD74: Y)	MAX	15	15	7	15	8	49	53	53	14.9	14.9
tPHL .	(CD74: ANYI)			15	15	7	15	8	49	53	53	14.9	14.9
tРZH	Ē	Y	MAX	27	45	19.5	15	7	36	42	45	13.5	13.5
tPZL] "			40	40	21	15	6.5	36	42	45	13.5	13.5
tPZH	Ē	W (CD74: Y)	MAX	27	27	19.5	15	6	36	42	45	13.5	13.5
tPZL	1 "			40	40	21	15	4.5	36	42	45	13.5	13.5
tPHZ	Ğ	Y	MAX	8	45	8.5	10	8.5	49	42	45	13.5	13.5
tPLZ				23	25	14	10	8	49	42	45	13.5	13.5
tPHZ	G	W (CD74: Y)	мах	8	55	8.5	10	5.5	49	42	45	13.5	13.5
tPLZ				23	25	14	10	4.5	49	42	45	13.5	13.5

UNIT: ns