

When the circuit is enoughzed by switching on the de power source it starts oscillating. The oscillations may start due to minor variation in de supply or inherent noise in the BIT.

. The variation in the base current is amplified and Fedback through the RC phase-shift network and finally applied to the base. INDERJIT SINGH

Att, some Frequency, the phase shift introduced by the 3 RC network :08 will be precisely 180° and at this Frequency the total phase shift from the base around the cRt and back to the base will be exactly zero.

· This particular frequency will be the one at which the circuit will oscillate. The oscillations will be maintained if the loop gain (ie A, K) is atleast equal to unity.

. However, to start the oscillations, the loop goin (A1.K) must be greater than unity.

- Using network analysis, it can be shown that the circuit brequercy of oscillation is given by,

Frequency of oscillation of RC phase-shift oscillator.

· For the loop gain (A.I) to be greater than unity, the current current gain of the transister must be,

K > 23 + 4R + 29

· Variable-frequency feature :-

· RC phase-shift oscillator is particularly suited to the range of brqueries from several Hz to 100 KHz that includes the audio range"

. The frequency of oscillation may be varied by changing the 3 rowtholk) of capacitors (c) in the phase-shifting network. simultaneously

Please Note: The amplitude of oscillations should not be affected as the Advantage: . Frequencies of oscillation's include audio range. (20Hz-20KHz) frequency is adjusted.

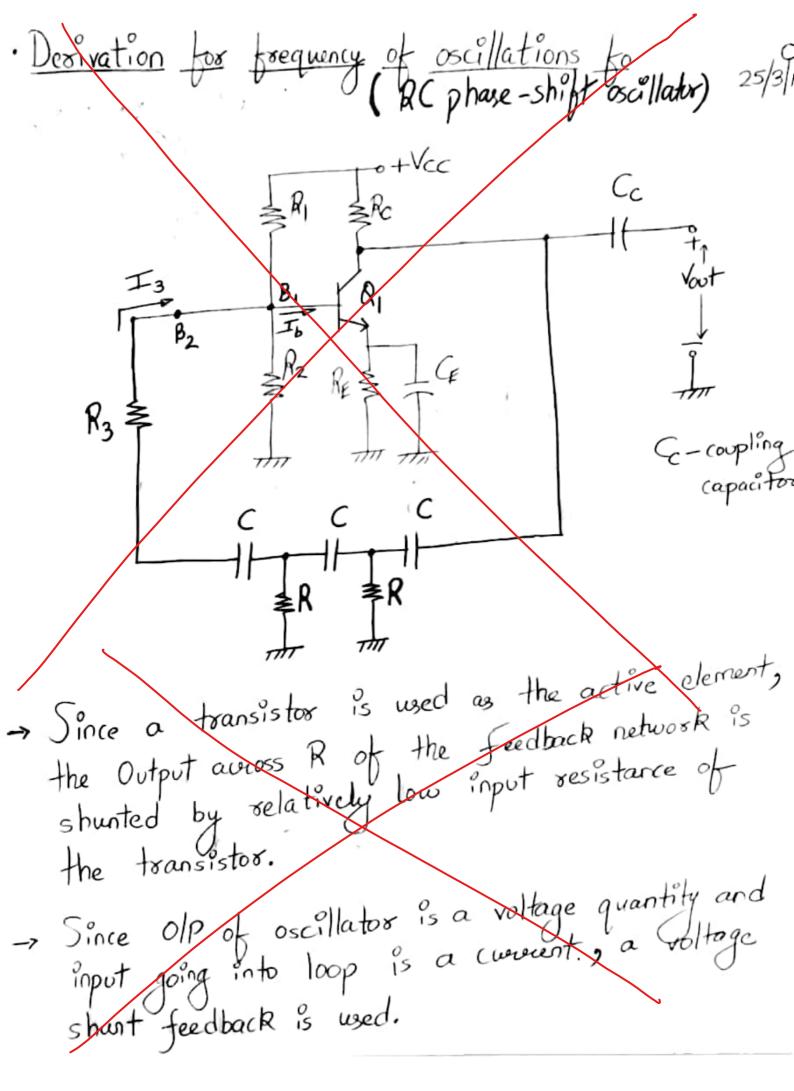
· Does not require the use of inductors (coils) making the cht small & compact. · Sinusoidal olp can be obtained. Disadvantages: 1) When sequired to provide a variable Frequency ckt, it is difficult to adjust equally the capacitor values of the phase-shift NIW simultanously.

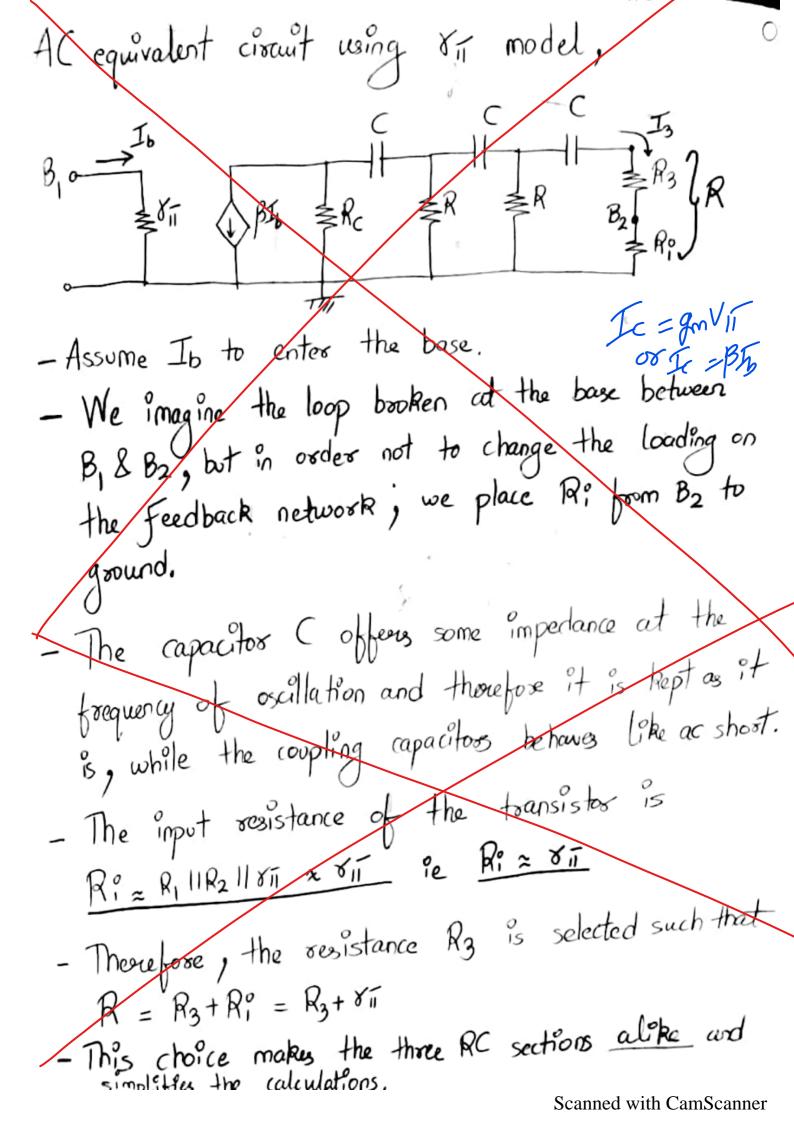
2) Frequency instability: Frequency in RC phase-shift oscillator depends on the values of R and c elements in Flb network. The values of R and C can change

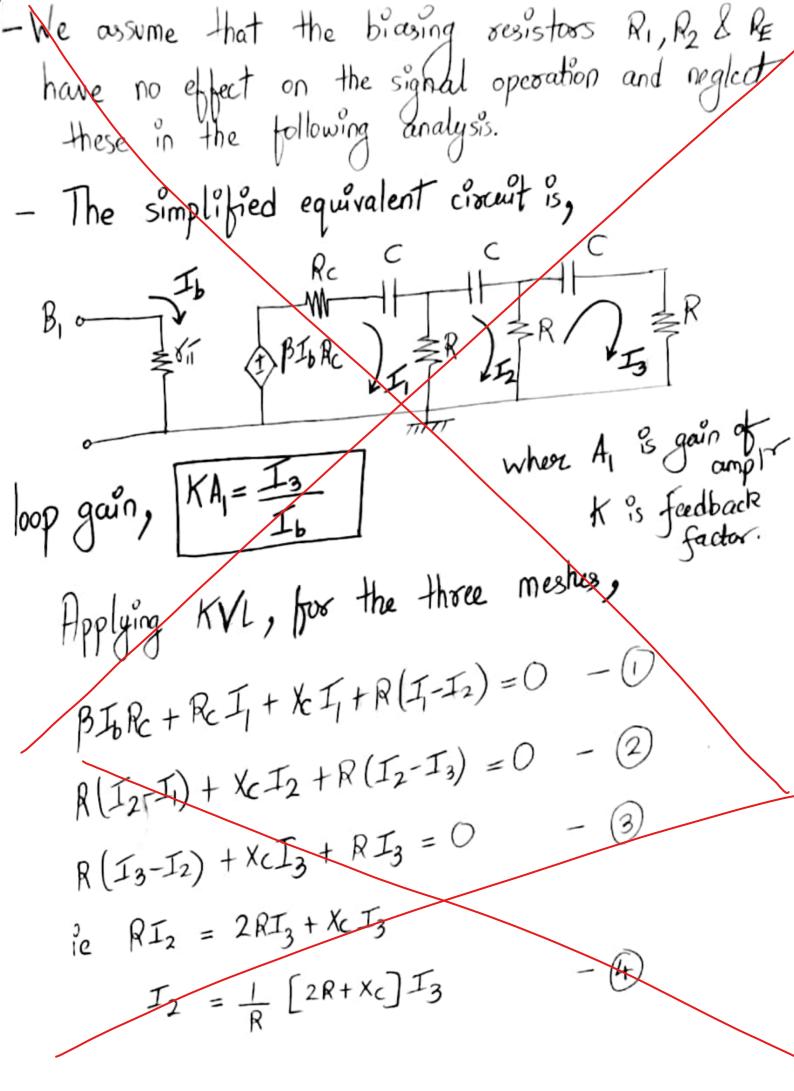
HOME TILFAQUETURE, aging to levance limits, etc.

3) of sinusoidal sig is not penfect as it suffers from distortions due to loading effect
of each RC network in the FIB stage.

Note: In a RC Phase Shift oscillator, in actual practise, it is difficult to get 9 a feedback network of 3 identical RC sections, which provides a total phase-shift of 180°. It is because of the fact, that each RC section in the feedback network loads down the previous one.







From (2),

$$R(I_2 I_1) + X_c I_{2} + R(I_{2} - I_{3}) = 0$$
 $-I_1 R = -RI_2 - X_c I_{2} - RI_{2} + RI_{3}$
 $I_1 R = (2R + X)I_2 - RI_{3}$

from (b), $I_2 = \left(\frac{2R + X_c}{R}\right)I_{3}$
 $I_1 R = (2R + X_c)\frac{(2R + X_c)}{R}I_{3} - RI_{3}$
 $I_1 R = \left(\frac{(2R + X_c)^2}{R} - R\right)I_{3} - RI_{3}$
 $I_2 = \frac{1}{R} \left[\frac{(2R + X_c)^2}{R} - R\right]I_{3} - G$
 $I_3 = \frac{1}{R} \left[\frac{(2R + X_c)^2}{R} - R\right]I_{3} - G$
 $I_4 = \frac{1}{R} \left[\frac{(2R + X_c)^2}{R} - R\right]I_{3} - G$
 $I_5 = \frac{1}{R} \left[\frac{(2R + X_c + R)}{R}\right]I_{3} - G$
 $I_7 = \frac{1}{R} \left[\frac{(2R + X_c + R)}{R}\right]I_{3} = 0$
 $I_7 = \frac{1}{R} \left[\frac{(2R + X_c)}{R}\right]I_{3} = 0$

Scanned with CamScanner

BRCIb R2 = - (RC+R+XC) (3R2+XC2+4RXC) + (2R+XC) 283+ R2Xc - 3R2Rc -Xc2Rc - $-3R^3-Xc^2R-4R^2XC$ 3R2XC - Xc3/4RXc2 BRC R2 -6R2XC-5RXC2-3RCR2 4RRCXC - Xc3 - RcXc2 BRCR2 {-R3-5Rxc2-3RcR2-RcXc2} Red pas + { - 6 R2xc - 4 RRCXc - Xc3} Imag -To determine the frequency of oscillation, put imaginary part 20 - Since Iz and Ib must be in phase Barthausers criterion.

Whing imaginary part
$$\approx 0$$

$$-6R^{2}X_{c} - 4RR_{c}X_{c} - X_{c}^{3} = 0$$

$$-6R^{2} - 4RR_{c} = X_{c}^{2} - (i) \qquad \begin{cases} X_{c} = \frac{1}{s_{w}c} \\ X_{c}^{2} = -\frac{1}{w^{2}c^{2}} \end{cases}$$

$$-6R^{2} - 4RR_{c} = -\frac{1}{w^{2}c^{2}} \qquad \begin{cases} X_{c}^{2} = -\frac{1}{w^{2}c^{2}} \\ X_{c}^{2} = -\frac{1}{w^{2}c^{2}} \end{cases}$$

$$= \frac{1}{2\pi RC} \sqrt{6R^{2} + 4RR_{c}}$$

$$= \frac{1}{2\pi RC} \sqrt{6 + 4R_{c}} \qquad \begin{cases} R = Rc \\ R = Rc \end{cases}$$

$$= \frac{1}{2\pi RC} \sqrt{6 + 4R_{c}} \qquad \begin{cases} R = Rc \\ R = Rc \end{cases}$$

$$= \frac{1}{2\pi RC} \sqrt{6 + 4R_{c}} \qquad \begin{cases} R = Rc \\ R = Rc \end{cases}$$

Scanned with CamScanner

Hso, initially I3>Ib, therefore, for oscillations 07 (I3/Ib is a real quantity) BRCR2 > {-R3-5RXc2-3BCR2-RcXc2} Xc2 = -6R2-4RRC at fright of oscillations. ie BRCR2 > (-R3-5R(-6R2-4RRC) -3RCR2 - Rc (-8R=4RRc)} > {-R3+30R3+20RRc-3RcR2 +6RcR2+4RRc2} > { 29R3+ 23R2Rc + 4RRc2} > { 29 R + 23 + 4 Rc } 4 8 + 23 The requirement that current gain of Scanned with CamScanner

for the loop-gain to greater than unity, the OS
requirement on the current gain of the transistor .. The two conditions (A) & (B) must also be satisfied for oscillations to start & be sustained. -v To de termine the value of k' with minimon B Consider eq B, B > 4R + 23 + 29 R $\frac{d\beta}{dk} = 4 - \frac{29}{k^2} = 0$ $e = 4 = \frac{29}{k^2} \Rightarrow k = \left(\frac{29}{4}\right)^{\frac{1}{2}} = 2.69$ ie R=2.7 $(3) \min_{0} \ge 4(2.7) + 23 + 29$ (B) min 2 44,5 Hence the value of B for a transistor must be atteast 45 for the around to oscillate. It B < 44.5, the around won't oscillate, since then 14,15 would be < 1.

To find $A_1K = \frac{I_3}{I_b}$ Loop gain = AIK. Put imaginary part =0 in eq (8), From (1), $\chi_{c^2} = -6R^2 - 4RRc$ B RCR2 -R3-5R(-6R2-4RR)-3RcR2- Rc (-6R2-4RR) BRER² -R³ + 30R³ + 20R²Re - 3ReR² + 6R²Re + 4RRe² 29R3 + 23 RcR2 + 4RR2 n' and or by Divide B RCP 29 + 23 Rc + 4 (Rc)

Rc +4k2+23k 1.01 14,K1