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Name ! Frathamesh Chavan
Div : H Roll no ! PH18

Problem statement: Write ×86/64 ALP to identify

Objectives: To learn displaying processor identifica-

instruction, who was a year

1. Explain CPUID instruction Conformation returned by CPUID instruction) in detail

-> CPUID returns processor identification and fea-

-ture information in the EAX, EBX, EDX regi--sters. The instruction's output is dependent

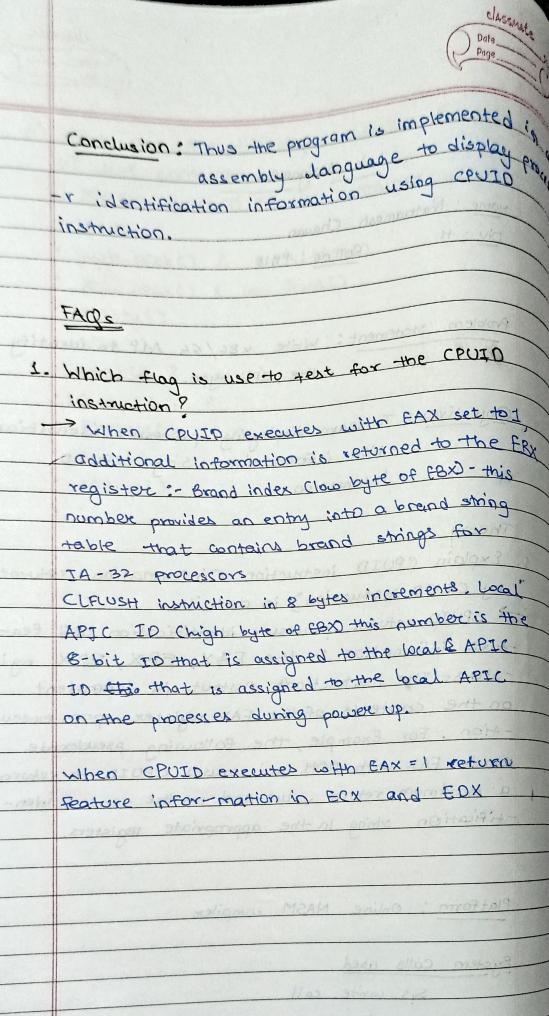
on the contents of the FAX register upon execu--tion. For Example, the following pseudocode

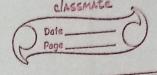
a maximum return value and an the vendor iden- titication string in the appropriate registers.

Platform: Online NASM compiler

System Calls used

sys\_exit call





2) Which flag is used for CPUID instruction? -> The ID flag (bit 21) in the EFlags register indicates support for the CUPID instruction. If a software procedure can set and clear the flag, the processor executing the procedure supports the CPUID instruction 3) Draw the output diagram of CPUID instruction if When the CPUID executes with EAX = 0, the processor returns the highest value of the CPUID recognises for returning basic processor returns the highest value of the CPUID recognises for ret--urning basic processor information. The value is returned in the EAX register and processor specific