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Survey No. 140,141/1 behind Br. Sheshrao Wankhade Shetkari Sahkari Soot Girni, Village - Waranga, PO - Bori (Butibori) Tahsil- Nagpur (Rural) District Nagpur - 441108

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20th February 2025

<u>Schedule of Written Test and Interview for Ph.D. Admission under Full-time</u> Visvesvaraya PhD Scheme for Electronics and IT: Phase II Winter 2024-25 session

The written test and interview of the eligible applicants for Ph.D. admission for the session starting from March 2025 is scheduled on **25**th **February 2025** as follows:

Computer Science and Engineering Department		
Document Verification	25/02/2025	9:00 am to 10:00 am
Written Test	25/02/2025	10:00 am to 11:00 am
Display of list of eligible candidates for interview	25/02/2025	11:30 am
Interview of shortlisted candidates	25/02/2025	12:00 noon onwards
Electronics and Communication Engineering Department		
Document Verification	25/02/2025	9:00 am to 10:00 am
Written Test	25/02/2025	10:00 am to 11:00 am
Display of list of eligible candidates for interview	25/02/2025	11:30 am
Interview of shortlisted candidates	25/02/2025	12:00 noon onwards

The written test and interview will be conducted in offline mode at the permanent campus of IIIT Nagpur. The applicants have to bring their original documents for verification along with a government approved ID card.

In case of any discrepancy, please contact the following:

Ph.D. Coordinator (CSE): Dr. Mayuri Digalwar (9461057601 / phdcell@iiitn.ac.in) Ph.D. Coordinator (ECE): Dr. Parul Sahare (8055156130 / phdcell@iiitn.ac.in)

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Associate Dean