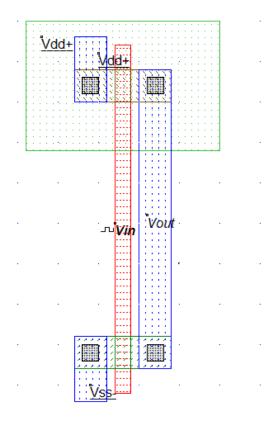
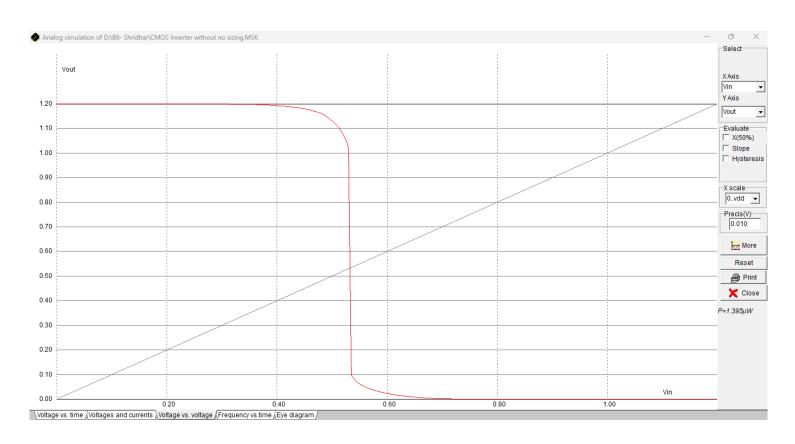
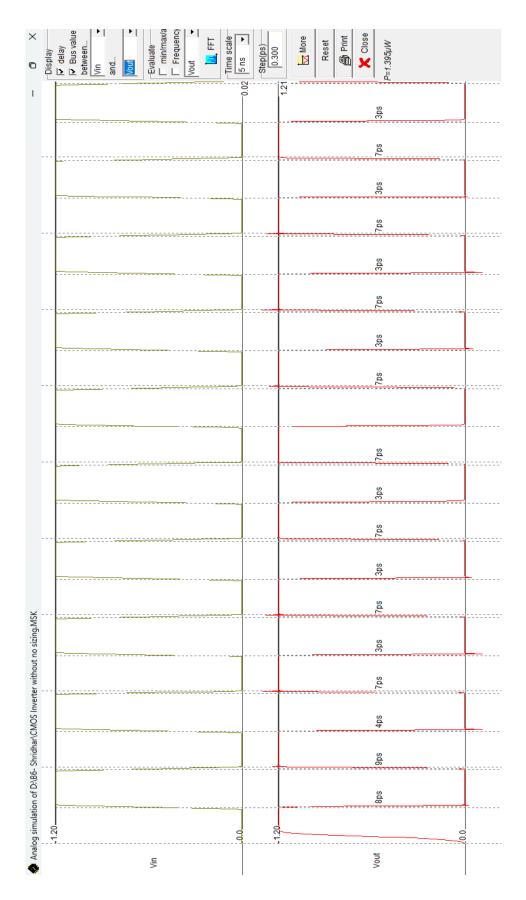
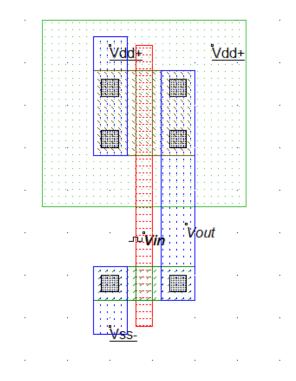
#### CMOS INVERTER WITHOUT SIZING AND WITHOUT LOAD CAPACITANCE

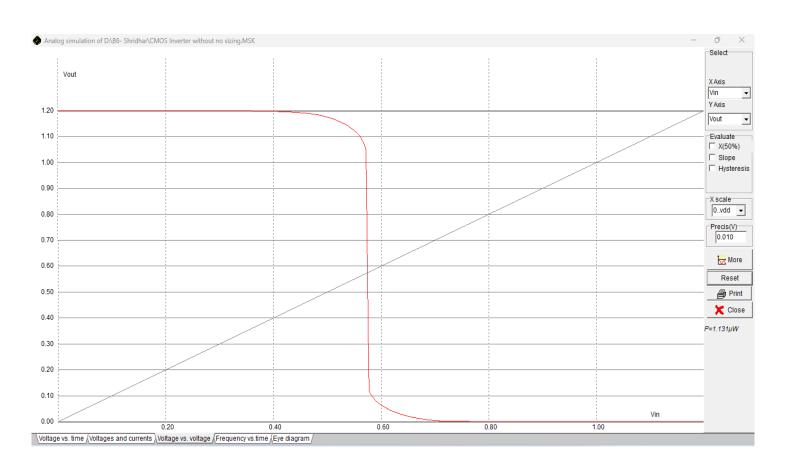


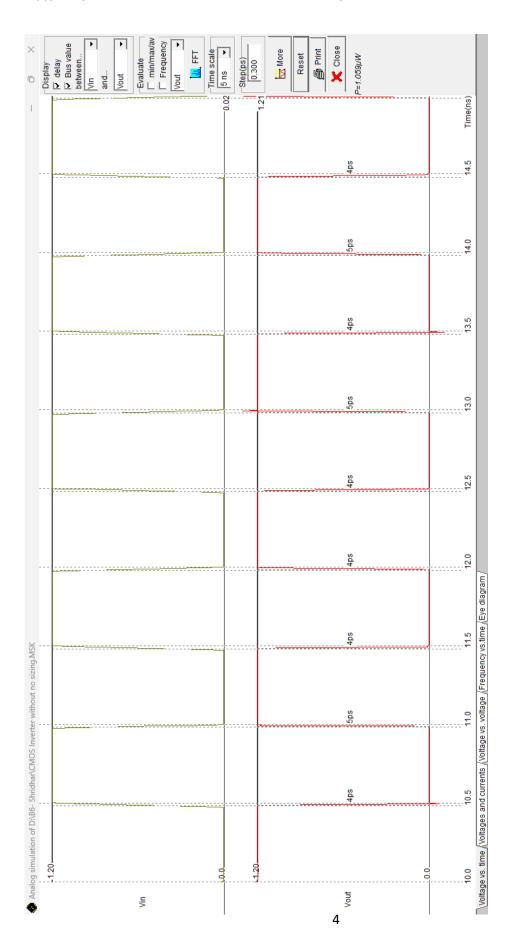




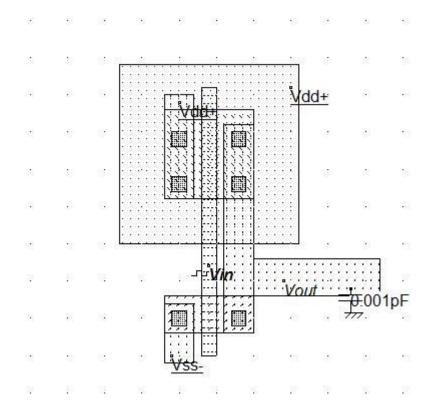
# CMOS INVERTER WITH SIZING AND WITHOUT LOAD CAPACITANCE

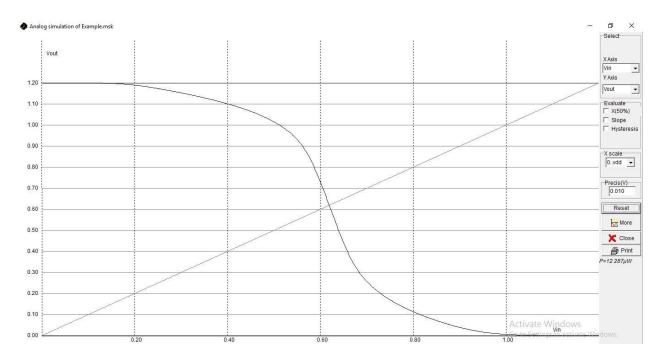


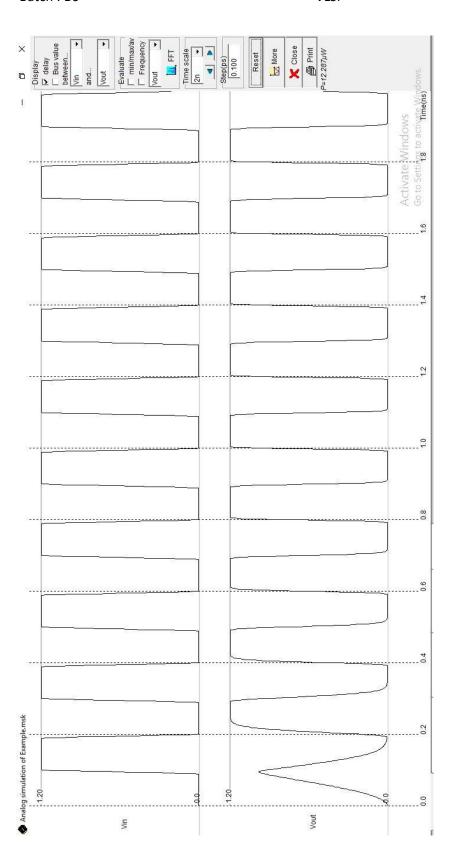




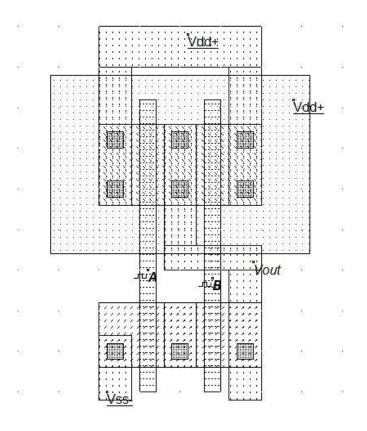
### CMOS INVERTER WITH SIZING AND LOAD CAPACITANCE

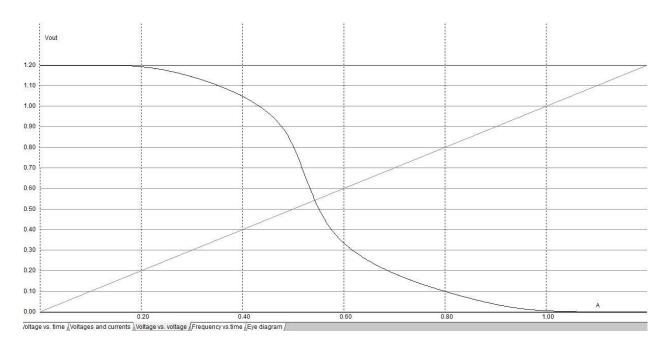


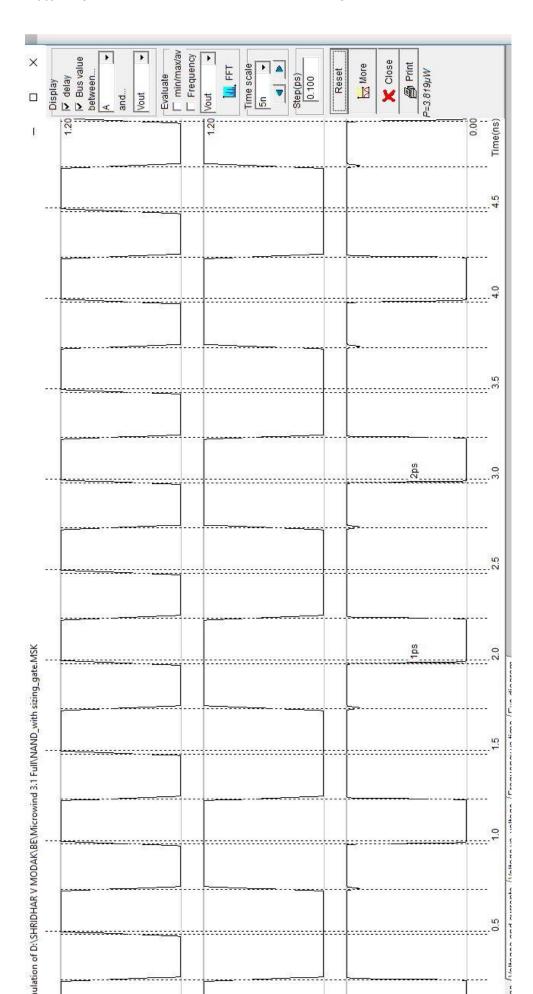




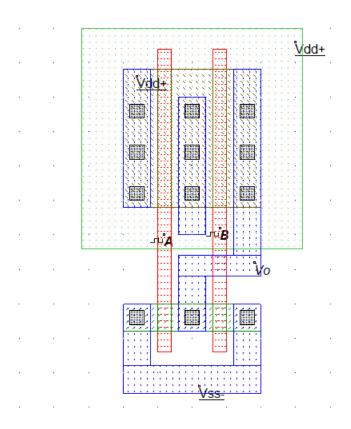
# **NAND GATE WITH SIZING**

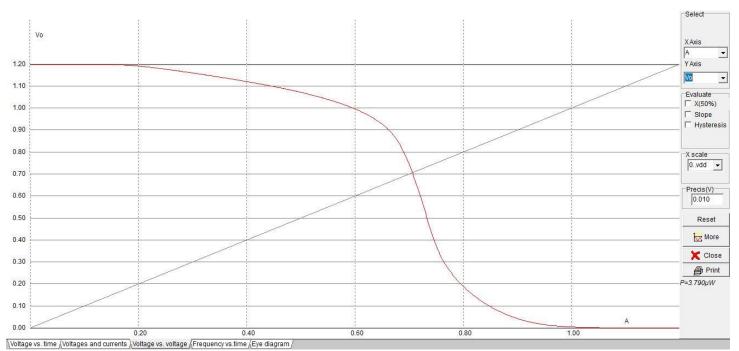


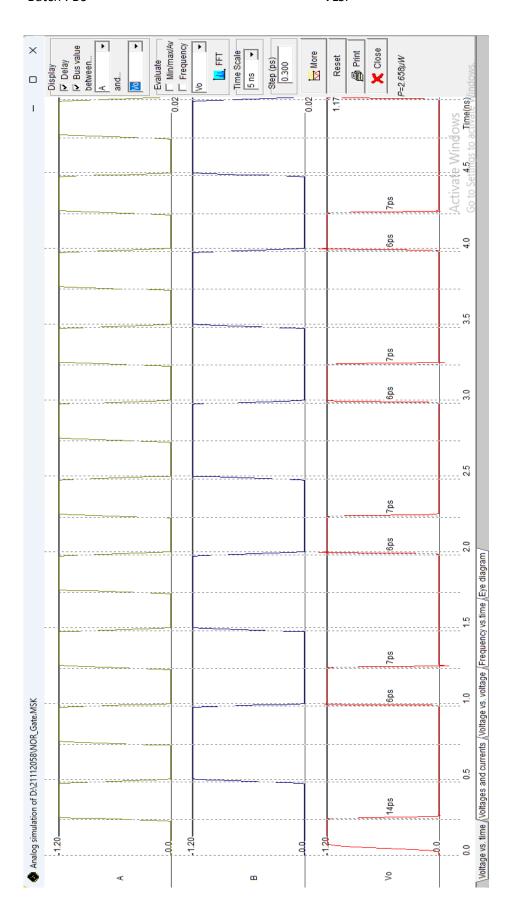




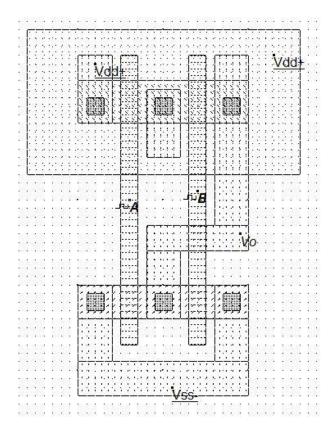
### NOR GATE WITH SIZING

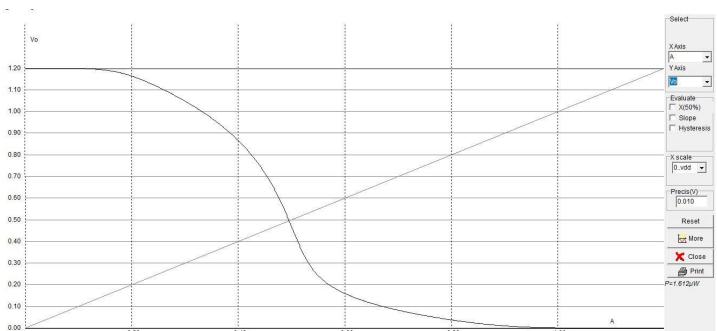


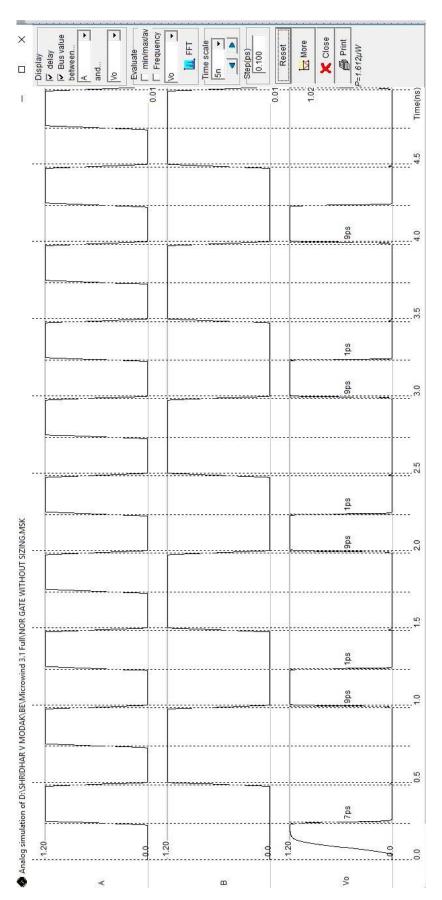




## NOR GATE WITHOUT SIZING







### NAND GATE WITHOUT SIZING

