

1. The circuit shown in the figure functions as which one of the following digital circuit blocks?

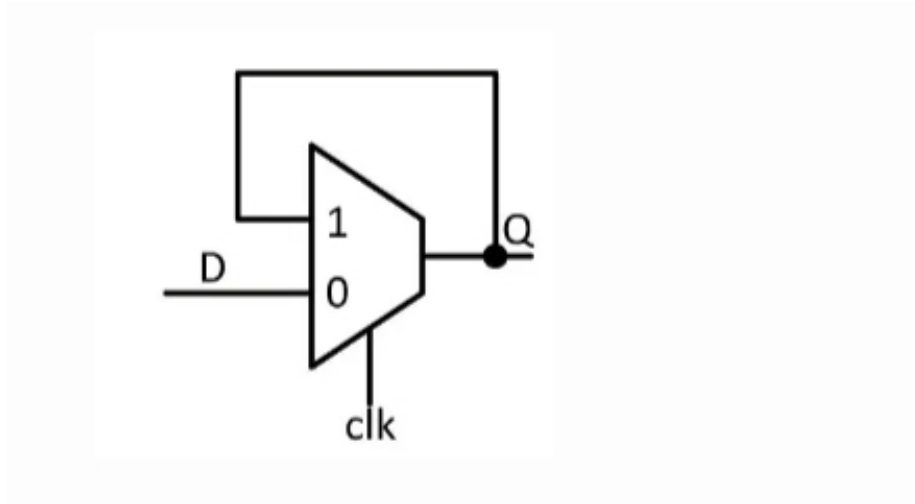


Figure 1: img

- (a) Negative level triggered D-latch
- (b) Positive level triggered D-latch
- (c) Negative edge triggered D-flip-flop
- (d) Positive edge triggered D-flip-flop