TITLE: PLC BASED LOGIC GATES PROGRAMMING

PROJECT OVERVIEW: This mini project was done using a PLC ladder logic simulator, where I designed and tested basic logic gates like AND, OR, NOT, NAND, NOR, XOR, and XNOR. It helped me understand how digital logic works in automation through ladder diagrams, which are commonly used in industrial control systems.

RESPONDSIBILITY: I was responsible for creating the ladder logic for each gate, running the simulations, and checking if the output matched the expected logic. I in real industrial systems.

EXPERIENCE: I designed and tested basic logic gates like AND, OR, NAND,NOR,XOR and XNOR using a PLC ladder logic simulator. It helped me understand how simple digital logic works in automation. I enjoyed learning how to build and simulate each gate step by step.

OUTPUT: The output obtiained is been put in github.Kindly review the videos.