



Pratik Sanghavi

Software Engineer

I am a self-motivated individual with a strong work ethic seeking to apply my skills in a challenging environment to utilize my proficiency for the growth of the organisation.

✉ sanghavipratikr@gmail.com

📍 Mumbai, India

🐙 github.com/PratikSangh

📞 9819545591

in linkedin.com/in/pratik-sanghavi-62a571154

EDUCATION

BE in Electronics and Communications Engineering with minors in Finance

Birla Institute of Technology and Science (BITS), Pilani, KK Birla Goa Campus

08/2017 - 06/2021

9.04

EXPERIENCE

SWE Intern

Apple Inc

01/2021 - Present

Bengaluru, India

Achievements/Tasks

- Worked towards improving the **robustness** of **inter-RAT handover and redirection** scenarios.
- Verified **data continuity** in RAT change during handover and redirection.
- **Identified bugs** pertinent to different components in the process.

Intern

VKS Computer Services

11/2020 - 12/2020

Mumbai, India

A data analytics firm serving clients in India as well as the Middle East

Achievements/Tasks

- Involved in scraping and cleaning data from an OTT platform.

Research Intern

Indira Gandhi Centre for Atomic Research

06/2019 - 08/2019

Kalpakkam, India

Achievements/Tasks

- Implementation of **noise suppression techniques** for **multi-channel analyzer** in an **FPGA based system**.
- Used **MATLAB** to simulate the **filter response** of various digital filters to the data collected from the **MCA** in the **Plutonium Continuous Air Monitoring System** (and generated VHDL code for a suitable model)

Intern

Cateina Technologies

05/2018 - 06/2018

Mumbai, India

One of the pioneers of blockchain solutions in India building products towards enhancing privacy and security of transactions

Achievements/Tasks

- Developed a prototype suitable for **cold chain management applications** enabling **real-time acquisition** and **transmission of temperature data** as well as developed a blockchain based wallet that could be integrated with the system to penalise the logistics provider.

ORGANIZATIONS

Department of Arts and Deco (09/2017 - 04/2020)

Creative Head - Helmed brainstorming sessions for laying the roadmap for the verticals to be taken up during the course of the semester

SKILLS

Mobile Telecom Networks

Data Analytics

Computer Networking

DBMS

Machine Learning

Version Control

Analog & Digital Design

Signal and Image Processing

Shell Scripting

Photo, Video Editing

PROJECTS

RAM tester (03/2019 - 04/2019)

- Designed a **RAM tester** for **checking the functionality** of a given **RAM module**.

Antenna Design for government and military communication (10/2019 - 11/2019)

- Designed a **microstrip patch antenna** spanning a bandwidth of **7-14 GHz** using the CST Studio Suite, antenna workflow.

Verilog Implementation of a rudimentary pipelined processor (04/2020 - 05/2020)

- Designed a **4 stage pipelined processor** able to execute a subset of the **RISC-V ISA**.

EXTRACURRICULARS

Elementary and intermediate drawing examinations

Secured "A" grade in both the examinations

Indian Classical Music (instrument - keyboard)

Secured "Pratham Shreni" in the four year course recognised by the Gandharva Mahavidyalaya (Ahmedabad)

Brown and blue belt in Karate and Judo from YKKF

Awarded 'Best Delegate' at PACE MUN, 2017

Delegate of Hungary, EU. Agenda - Refugee Crisis in Europe

LANGUAGES

English

Full Professional Proficiency

Hindi

Native or Bilingual Proficiency

Gujarati

Native or Bilingual Proficiency

Marathi

Professional Working Proficiency

TOOLS

Python

MATLAB

C/C++

HTML, CSS

Javascript

Verilog

x86 Assembly

SQL

Cadence Virtuoso

AutoCAD

CST Studio Suite

Quartus Prime

Git