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**6CS005 High Performance Computing**

**Portfolio Task**

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**Submission Date**: 29th January, 2026

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# Introduction

This project will examine the use of parallel and heterogeneous computing methods to address performance-sensitive problems that prove to be inefficient when run in single thread execution. With growing data-sets and computational complexity, standard sequential programs do not utilizes the the newer multi-core CPUs and GPUs. To overcome this weakness, the coursework applies several high-performance solutions based on **POSIX threads, OpenMP, and CUDA**, showing how concurrency, synchronization, and appropriate memory management can contribute significantly to the speed of execution without reducing correctness and reliability.

The project has four tasks that are major. The former one is an implementation of a parallel word frequency counter in C with POSIX threads, in which a large input file is counted by a number of threads carefully using shared memory, synchronization, and boundary conditions. The second assignment deals with the operations of matrices on OpenMP such as arithmetic, transposition, and matrix multiplication, and the importance of scalability of the work, its resistance to the issue of input corruption, and dynamical allocation of the memory. The third assignment presents the idea of password cracking on the GPUs, and the specifics of CUDA include the execution of a kernel, transfers between memory, and the communication between the device and the host. The last task uses CUDA to edge detection with Sobel showing parallel applications to the images on the GPU. Taken together, these tasks demonstrate fundamental concepts of parallel computing both on the CPU and the GPU architectures such as optimization of performance, synchronization in parallel computers, and proper use of memory in computing high-performance computer systems.

# Word Occurance Counter Using Posix

## Input Validiation

This programm expects 2 additional argument which are the fiel containing the word and another is the number of threads to use. In this very first steps we check weather or there are right number of arguments which is 3 in this case, and also we check and notify the user if the number of thread is a positve integer.

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*Figure 1 Input Validiation*

## File handeling

In this process we read the file in the memory. If the file doesn’t exist than a error message is shown to the user. Initially the file pointer as shown in figure below is pointing to the start of the file than we use the **fseek()** function with **SEEK\_END** argument to change the pointer to the end and then read the current pointer value using **ftell()** which return the **file size** in bytes. Next we use **rewind()** with the file pointer argument to repoint the pointer to the start of the file. Now we check weather or not the file is empty using the **file\_size,** if it is zero than the file is empty. If the file is empty we close the programm. If file content exist than we use **malloc()** to allocate memory. We allocate one additioal byte of memory which is later filled with end terminator ‘\0’ to indicate the end of cotnet. Lastly we fill the memory with the **file data** usign the **fread()** function and close the file using **fclose()** function.

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*Figure 2 File handeling*

## Buffer slicing and thread creation

Here the thread id is initialized and the thread argument is also declared using malloc. Next a loop is runned to the thread count so that we can populate the thread arguments with necessary value. Now we fill each thread arguments with the start(size from where to start in bytes), end(size where to end in bytes), buffer(the actual file buffer), and the total buffer size(bytes). Next we run the thread function using the **pthread\_create().** In this case we run the **process\_slice()** function with the thread arguments creted previously.Lastly we joint all the threads using the **pthred\_join()** function.

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*Figure 3 threads argument creation*

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*Figure 5 Pouplating Threads args and running thread*

## Result Generation and Output Handling

As shown in figure below, the final processing of results, reporting, and cleanup of all threads that have completed counting words are the responsibilities of this part of the program. To begin with, the global words array is sorted using **qsort()**in a descending order of frequency of the words in the array where the most frequently appearing words come at the beginning of the output. The program subsequently opens an output file with the error control so as to check if the file result.txt has been created successfully. A structured header is appended to the file alongside the number of unique words which are summed up and the column names that should be read. The program repeats the sorted word list reading and writing out each word and its frequency to the file and at the same time adds together the total number of words handled by adding individual counts together. The key statistics are also shown on the console afterward after printing a summary section (total words processed) so that the user can give feedback. The last thing that happens is the program closes the output file, the dynamically allocated memory (buffer, thread array, and thread argument array) are freed, and 0 is returned indicating successful execution.

*Figure 6 Adding content to file and shwoign necessary logs*

## The process\_slice() function

This is the worker logic that is the essence of every thread and that which is used to process a given segment of the input text buffer. Each thread is provided with a **ThreadArgs** structure that has the shared text buffer and start and end indices of it slice. The operation starts with the initialisation of a temporary **current\_word buffer** and a position index with which words are constructed character by character. As a way of avoiding counting a partial word, the thread initially examines whether the index at which it currently is in an **alpha-numeric sequence** here **the non alpha-numberic sequenc is the “**\**n”** which indicated next line is in the middle of the sequence; in that case, the index is advanced until the index reaches a word boundary. This loop then reads characters in the slice assigned, adding alphanumeric characters to the current word, and changing them to lowercase to enforce the counting of cases being insensitive to the case. Upon coming to a non-alphanumeric character, the word that has been formed is terminated at the end and safely inserted into the global word list.

The function also takes precautions with the boundary conditions in the end of the slice where a word can be longer than the range assigned to the thread. Once the initial loop is done, another loop starts again reading characters beyond the slice point until a **delimiter** is located, which makes sure that a word cut between two slices is counted once by the current thread. Should there be a meaningful word in the buffer at the end of this process then it is completed and inserted into the world collection. This functionality ensures that word counts are made correctly without redundancy or data races by making sure that the index adjustment is done very carefully, word construction is done very carefully, and updates are synchronized even when multiple threads are using the same shared buffer.

A screenshot of a computer code

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*Figure 7 Process slice function*

## The add\_word() function

This function performs the work of safely updating the data structure of words frequencies in the world in a multithreaded setting. When a thread recognizes a complete word it calls **add\_word()**, which initially takes out a **mutex lock** to be able to exclusively access the shared words array and variable word count to avoid **race conditions**. The function then conducts a linear search of the available entries in order to determine whether the word already exists; in this case of a match the appropriate frequency count is increased and the mutex returned. When the word is not found and there was still space, the feature will add the new word to the array, set its count to one, and the total number of unique words. Lastly, **the mutex is released** and only a single thread is allowed to update the shared data at any point in time and keeps the concurrent execution **correct and consistent**.

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AI-generated content may be incorrect.*Figure 8 The add word function*

# Matrices Arthemetics Using OpenMP

## 2.1. Input Validation

Endptr checks and strtod() function are utilized in the program. This will ensure that every letter in a token is a valid part of a number. The software identifies an error and removes the memory already in use and proceeds to the next pair of matrices when it comes across a non-numeric token. This will ensure integrity of output and prevent the system to process garbage data.

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*Figure 9 Input validiation and file handeling*

## 2.2. Memory Management and Safety

This part is concerned with the safe creation, deallocation and access of matrices in memory, and the reading and writing of files. The **allocate\_matrix** function is a dynamic means of constructing a 2D array of doubles and detects allocation failures and the **deallocate\_matrix** is used to free all memory allocated to avoid leakage. In managing files, load matrix uses the read token to read out a complete matrix of the input file with the dimensions and contents being valid integers and read token reads numeric values in a file and ignores commas, spaces and new lines. Similarly,the **display\_matrix** to file is a form of representation that numbers extensive numbers and elegantly receives NaNs when writing a matrix to a file. A combination of these processes ensures uniform file I/O and high memory usage within the application.

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AI-generated content may be incorrect.*Figure 10 Memory allocation*

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*Figure 11 Load matrix to memory*

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*Figure 12 Display Matric*

## 2.3.OpenMP Parallelisation and Thread Capping

This program uses OpenMP to parallelize computationally intensive tasks across multiple threads, allowing for the simultaneous execution of loops for matrix arithmetic, multiplication, and transposition. Thread capping is used to prevent the creation of more threads than the relevant matrix dimension in order to reduce overhead and ensure efficient resource use. By **dynamically adjusting** the number of threads in accordance with matrix size, the approach optimizes performance without unnecessary context switching or memory contention, striking a balance between speed and system stability. This architecture scales effectively with matrix dimensions while ensuring safe and predictable parallel execution.

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AI-generated content may be incorrect.*Figure 13 Utility Functions*

## 2.4.Matrix Calculations

### 2.4.1. Addition

The **matrix addition** process involves the addition of two matrices of same dimensions element by element. The function calculates the sum of the matching elements through the iteration of the row and column using simultaneous threads, after memory allocation of the ultimate matrix. To avoid corrupt operations, addition is not performed when the matrices of the input are of dissimilar shapes. This technique involves the OpenMP threading that can be used to integrate secure memory handling with efficient computation.

### 2.4.2. Subtraction

**Element-wise Matrix subtraction** refers to the operation of subtraction between two matrices of equal size, similar to addition. The difference between the respective elements of the input matrices is calculated to obtain the elements of the output matrix, and a new result matrix is allotted. Whereas dimension checks prevent any not valid practices, parallelisation is employed to accelerate computation. This will ensure that accuracy and performance is maintained.

### 2.4.3. Element wise multiplication

### 

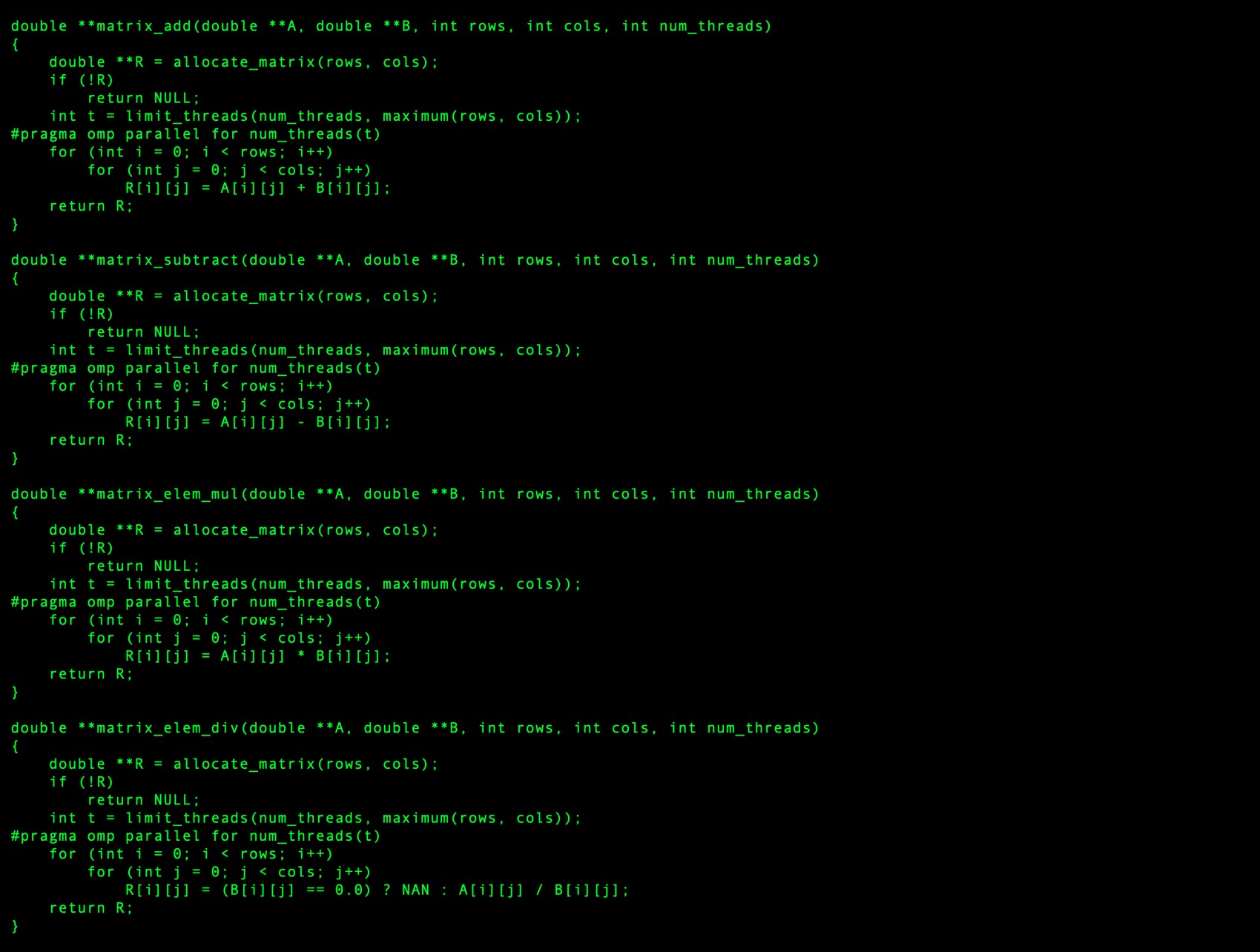
**Element-wise multiplication** is the computation of the product of similar elements in two matrices of the same size. The process multiplies every element of the former matrix by the element of the latter one after assigning a new matrix to contain the outcomes. OpenMP parallel threads are used to improve performance and shape validation used to maintain stability in the program to ensure that only compatible matrices are multiplied.

### 2.4.4. Element wise division

The **Division** of corresponding elements of two matrices with the same dimensions is determined by element-wise division. Each element is divided into a result matrix, with a check to assign NaN in the event that the division is by zero. Performance can be increased by processing multiple rows at once thanks to parallelization, which also makes sure that invalid operations—like division by zero or mismatched shapes—are handled safely.

### 2.4.5. Transpose

The **Transpose** operation transforms rows into columns and vice versa. Each element is copied to its transposed position and a new matrix is assigned with the dimensions reversed. To increase processing speed, OpenMP parallelisation is used row-wise. This operation is essential for later operations like matrix multiplication and is always valid regardless of the original matrix structure.

*Figure 14 Matrix calculation 1*

### *2.4.6.* Dot Product (Multiplication*)*

When the number of columns in the first matrix is equal to the number of rows in the second, **matrix multiplication** calculates the dot product of the two matrices. Each element is calculated as the sum of products of related elements in the row of the first matrix and the column of the second, and a result matrix with the proper output dimensions is assigned. While shape validation guarantees that only compatible matrices are multiplied, preserving correctness, parallelisation is used across rows to speed up computation.

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## 2.5.Handling Edge Cases and Division

Decoupling the operations was the last logic improvement to make sure that a failure in one would not halt the process as a whole. Mismatched dimensions for addition could completely halt the processing of that matrix pair in the base code.The updated program independently verifies each operation's requirements. Additionally, a particular check for "division by zero" was added for element-wise division. When a divisor is zero, the program specifically gives that cell a NaN (Not a Number) value. This keeps the program from crashing with a floating-point exception and guarantees that the output complies with standards for scientific computing.

# Password Cracking Using CUDA

## Generate encrypted password in the CPU

In this process the **PasswordGenerator.c** file generates password in **format aa11** where first two character is always lower cased alphabet and last 2 is always numeric valus. Then the 4 letter password is encrypted to a 10 letter password with simple asci conversion techniques. This password is than stored in a textfile.

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Figure 16 PasswordGenerator.c

## Hash the encrypted password using SHA512 Algorithm

In this process the build in function from **crypt()** is used to hash the password which wash encrypted in above step. This function comes from **crypt.h header file.** What happens here is the program goes through each line of the initially encrypted password and than hashes it and store to a output text file as specified in the comman line.

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Figure 17 EncryptSHA512.c

## Hash all the possible password into SHA512.

For the first time we utilized the power GPU to calcualte all the possible combination of our passwords and than encrypt it usign the same way like we did in the first step. So now we’ll have all the possible combination of **encrypted but not hashed passwords** which is **67600** in our case. Than as shown in **Figure ,** the same **CPU function** is used to hash the 67600 passwords. Thus finally we’ll have 67600 properly hashed passwords usign the same algorithm as the target file.

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Figure 18 Password Generation Kernel

## Crack the password and store to file

**Brute-force** search is initially performed on the GPU with all possible passwords generated correspondingly to the passwords by all possible password combinations and the encrypted/ hash values are stored in a large look-up table of 67,600 encrypted hash values. All the items in the table are distinct passwords based on a predetermined pattern and encrypted password. During the cracking, the CUDA kernel is executed to ensure that each thread in the GPUs has a single target hash. The thread then performs a comparison of its target hash against all the precalculated hashes in the table in sequence, the brute-force step of comparison. In case of a match, the matching table index is transformed back to the original plaintext password and the result is stored in the output; when no match is found after searching the entire table, a dummy(XXX) is stored instead. This scheme decouples the generation step of brute-force with the comparison step, thus enabling the costly password space to be calculable only once and then reused successfully to crack numerous target hashes at once.

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Figure 19 Crack Password kernel

## Memory deallocation – freeing memory appropriately

Lastly, all the GPU and CPU memory is freed to make sure memory leak doesn’t happen.

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Figure 20 Memory Deallocation

# Sobel Edege Detection Using CUDA

## Read image into CPU memory and store appropriately

In this step I have read the image file into CPU memory using then **lodepng\_decode32\_file()** function. This function decodes the image to its raw pixels form (RGBA for each pixels) and also gives the **width and heigth in pixels**. Later in the process, we’ll need to convert back the pixels value to image so we’ve created a memory space named **h\_out\_rgba** using the maclloc() functionwhich will be used later**.**

A screen shot of a computer program

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Figure 21 loadign image to cpu memory

## Allocate GPU memory according to image size and transfer data to GPU

### Allocating GPU Memory

Here **cudMalloc()** is used to allocate memory in the GPU, we have allocated memory for input rbga, gray converted, edeges detected, and output rgba for loding raw decoded rgba, storing converted gray values (one per pixel), detected edeges (one per pixel), output rgba to store the final manipulated rgba value respectively. In my case the **hck.png image is of 464 x 108 pixels** and as a singel **unsigned character holds 1 byte** of memory so the memory allocation looks soething like this as showin in table below:

|  |  |
| --- | --- |
| Input RGBA | 200448 bytes |
| GrayScale | 50112 bytes |
| Edeges | 50112 bytes |
| OutPut RGBA2 | 200448 bytes |

### Data Transfer to GPU

Now we have allocated sufficient memory in the GPU, we need to transfer data to it. We just transfer the **raw decoded pixel value** to the GPU as all other memory address will be filled by GPU itself by performing pixel manupulation. **CudeMemcpy()** function is used to transfer the data to the GPU we need to keep in mind the last argument is the direaction and it must be **cudaMemcpyHostToDevice** as we are copying from host(CPU) to device (GPU). How we have the required data in the device.

A computer screen shot of a program code

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Figure 22 Memory Allocation to GPU

## Edges detected correctly via the CUDA kernel function

### RBGA to GrayScale Kernel

In this kernel we pass all the raw rgba value and it converts to **grayscale intensity** value using the human **eye adaptive formula for grayscale based on luminiosity weights** rather than a normal average method we used to use in the past.

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Figure 23 rgb to grayscale kernel

### Edege Detection Kernel

This kernel performs the main logic of edege detection. Here we have two gradient **Gx and Gy** which is used to mease the rate of change of intensities in horizontal and vertical direction respectively.I t wokrs by multipling each value of the **gradient kernel** with its corrosponding grayscale value, and this step is repeated for each and every value of the grayscale intensity. So now we have both the gradiet in horizontal and vertical direction, then, **the formula**  is used for the effective magnitude of intensity change. Lastly, the edeges values are populated to the **d\_edges** variable.

A screen shot of a computer program

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Figure 24 sobel edege detection kernel

### Detected edeges to final RGBA kernel

We had one value per pexel in the gray scale intensity value now this kernel populates the value to all the RGB to make a perfect set of RGBA value**,** the A (transparency) is set to 255 for a perfectly opaque image. This makes sure we can later use the perfect set of RGBA to generatre output edege detected image.

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Figure 25 graysacel to rgba kernel

## Transfer Edege detected value safely to the CPU

The **cudeMemcpy()** function is used with direction argument **cudaMemcpyDeviceToHost** to copy the final manipulated edege detected pixel value to the **h\_out\_rgba.** Now our Host(CPU) has the final sets of pixel value which could be easily encoded to a png image using the lodepng.

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Figure 26 Final RGBA value copy to host

## Making final image and memory deallocation

Finally the **lodepng\_encode32\_file()** function is used to encode the pixel back to png image. This gives us a final png image named **sobel-final-image.png**. Lastly, we free all the memory used by the GPU as well as CPU to prevent memory leaking.

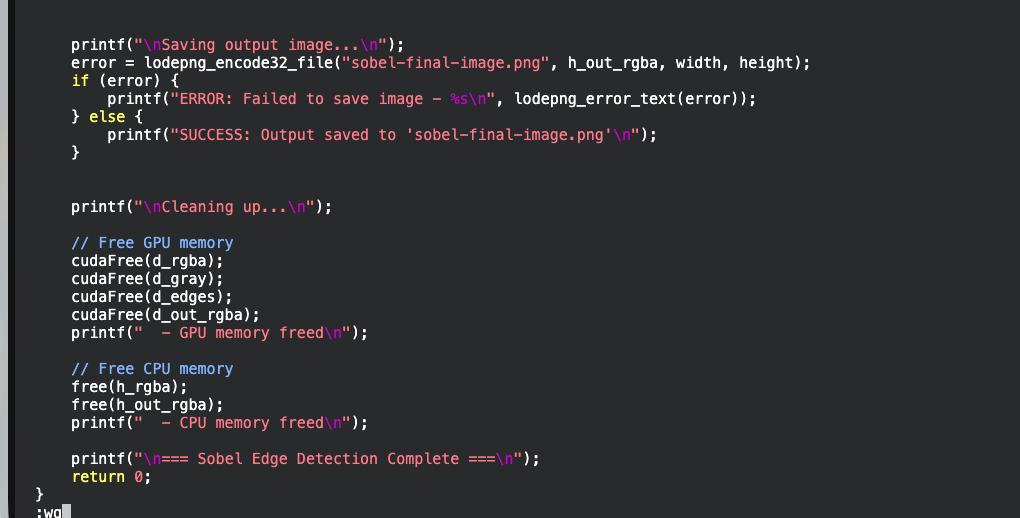


Figure 27 memory deallocation

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Figure 28 output commandline

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Figure 29 output image

# Conclusion

Lastly, it can be concluded that this portfolio has shown that parallel and heterogeneous computing methods can dramatically enhance performance, scalability and reliability in solving computational intensive problems. The project emphasizes thorough thread coordination, synchronization and boundary management to achieve the correctness in shared-memory parallelism through the POSIX-based word occurrence counter. The matrix arithmetic tasks with the help of OpenMP also note out the significance of safe memory management, input validation and adaptive thread usage to provide efficient and stable parallel execution by the CPU. CUDA password cracking task on the GPU side both demonstrates the power of massive parallelism to greatly reduce brute-force computation time by decoupling the generation and comparison steps, and the Sobel edge detector task demonstrates the successful application of both the GPU memory and kernel implementation as well as data transfers to a real-world image processing task. All these implementations collectively support the foundations of core concepts of high-performance computing, including concurrency, synchronization, memory efficiency and architecture-sensitivity, and show that modern multi-core CPU and graphic card hardware can be used to solve previously impractical or inefficient problems that are possible only in purely sequential programs.