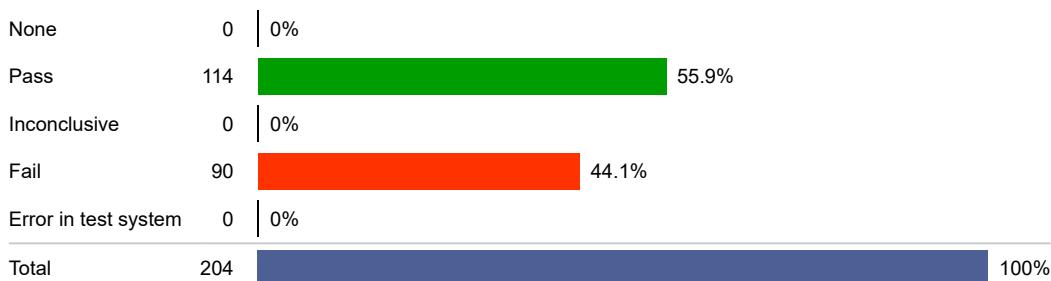


# Report\_Test\_Configuration\_1

## Test Configuration 1 [1 .. 204]

Fail

### Test Case Results



### General Information

Test begin: 08/14/2024 02:23:22 PM +05:30

Test end: 08/14/2024 02:28:50 PM +05:30

Duration: 00:05:28

### Tester

Windows Login Name: sj925870.ttl

### Test Setup

Version:

CANoe.J1939.CAN.LIN.Scope /pro 17.3.91 (SP3)

Configuration:

D:\Sachin Jadhav\Kratos\_EV\Kratos EV IVN Config\_Code\Config\Kratos EV IVN Testing.cfg

Database KRATOS\_DBC\_08-05-2024 1 (2) on CAN1, CAN2, CAN3, CAN4: D:\Sachin Jadhav\Kratos\_EV\KRATOS\_DBC\_08-05-2024 1 (2).dbc (2024-08-06 11:20:30)

Test Configuration:

Test Configuration 1

Windows Computer Name:

TMLPNELTPC32283

### Hardware

#### CAN Channel Interfaces

Channel	Device	Serial number	Driver DLL version	Driver version
1	VN1630	57789	24.10.18	23.10.18
2	VN1630	57789	24.10.18	23.10.18
3	VN1630	57789	24.10.18	23.10.18
4	VN1630	57789	24.10.18	23.10.18

### Table of Contents

	<input checked="" type="checkbox"/> TestUnit1_1 [1 .. 204]	
1	<input checked="" type="checkbox"/> (PCAN CH_3 TX/ TCAN CH_2 RX_ Aux1 Comp ) x80__HD1_Debug_Currents	Pass
2	<input checked="" type="checkbox"/> x80__HD2_Debug_Voltages	Fail
3	<input checked="" type="checkbox"/> x80__HD3_Debug_Temperatures_A	Pass
4	<input checked="" type="checkbox"/> x80__HD4_Debug_Temperatures_B	Pass
5	<input checked="" type="checkbox"/> x80__HD5_Debug_Control_Data_PMAC	Pass
6	<input checked="" type="checkbox"/> x80__HS1_Torque_Feedback	Pass
7	<input checked="" type="checkbox"/> x80__HS2_Status_Feedback	Pass
8	<input checked="" type="checkbox"/> x80__HS3_Temperature_Feedback	Pass
9	<input checked="" type="checkbox"/> x80__HS4_Fault_Info	Pass
10	<input checked="" type="checkbox"/> x80__HS5_Encoder_Info	Fail
11	<input checked="" type="checkbox"/> (PCAN CH_3 TX/ TCAN CH_2 RX_ Aux2_Ste)x81__HD1_Debug_Currents	Pass

# Report\_Test\_Configuration\_1

12	x81_HD2_Debug_Voltages	Pass
13	x81_HD3_Debug_Temperatures_A	Pass
14	x81_HD4_Debug_Temperatures_B	Pass
15	x81_HD5_Debug_Control_Data_PMAC	Pass
16	x81_HS1_Torque_Feedback	Pass
17	x81_HS2_Status_Feedback	Fail
18	x81_HS3_Temperature_Feedback	Pass
19	x81_HS4_Fault_Info	Pass
20	x81_HS5_Encoder_Info	Pass
21	(ICAN CH 1TX/ ICAN 1 RX BAC) BAC_door_lock_horn_status	Pass
22	(PCAN CH_3 TX/ TCAN CH_2 RX_ BCS) BCS1_EVCU1	Pass
23	(PCAN CH 3 BMS) BMS_chgrequestparameter	Pass
24	BMS_Pack_Branch1Cell_Voltages	Fail
25	BMS_Pack_Branch1cellTemperature	Pass
26	BMS_Pack_Branch2Cell_Voltages	Fail
27	BMS_Pack_Branch2cellTemperature	Pass
28	BMS_Pack_Branch3Cell_Voltages	Fail
29	BMS_Pack_Branch3cellTemperature	Pass
30	BMS_Pack_Data1	Pass
31	BMS_Pack_Data2	Fail
32	BMS_Pack_Data3	Pass
33	BMS_Pack_Data4	Pass
34	BMS_Pack_DischCurrent_Limits	Fail
35	BMS_Pack_Flt1	Fail
36	BMS_Pack_Flt2	Pass
37	BMS_Pack_Flt3	Pass
38	BMS_Pack_Info	Pass
39	BMS_Pack_Slave_Data	Fail
40	BMS_Pack_Status1	Fail
41	BMS_Pack_Status2	Fail
42	BMS_Pack_TimeToFaults	Fail
43	BMS1_Inletandoutlettemperature	Pass
44	BMS2_Inletandoutlettemperature	Pass
45	BMS2_Otherstemperature	Pass
46	E2P_Data1	Pass
47	E2P_Data2	Pass
48	E2P_Data3	Pass
49	E2P_Data4	Pass
50	E2P_Data5	Pass
51	E2P_Data6	Pass
52	E2P_Data7	Pass

# Report\_Test\_Configuration\_1

53	E2P_Data8	Pass
54	E2P_Data9	Pass
55	E2P_Data10	Pass
56	(ICAN Ch1Tx/ICAN Ch1 Rx CAN_Switch) CANSwitchProprietary	Pass
57	(PCAN CH 3TX/ TCAN CH 2 RX DCDC) DCDC_Status1	Pass
58	DCDC_Status2	Fail
59	(ICAN CH 1 TX DMS/TCAN CH 2RX) ACKM_DMS (CY - 0)	Fail
60	CI_DMS (CY - 0)	Fail
61	DM1_DMS	Pass
62	DM2_DMS (CY - 0)	Fail
63	DM4_DMS (CY - 0)	Fail
64	prop_DMS	Pass
65	Proprietary_DMS_6 (CY - 0)	Fail
66	Proprietary_DMS_2 (CY - 0)	Fail
67	Proprietary_SW_HW_version (CY - 0)	Fail
68	SOFT_DMS (CY - 0)	Fail
69	(PCAN Ch 3 TXDNR/ PCAN 3 RX ECVU1)TC1_255	Fail
70	(PCAN CH 3 TX ELub /PCAN CH 3 RX)ELubePumpData1	Fail
71	(CCAN Ch 4 TX EBS/ TCAN CH 2 RX) ACK (CY -0)	Fail
72	CCSV1_ABS	Fail
73	CI (CY - 0)	Fail
74	CI_EBS (CY - 0)	Fail
75	CVW (CY - 0)	Fail
76	DM1	Fail
77	DM4 (CY - 0)	Fail
78	EBC2	Pass
79	EBC1	Pass
80	EBC3	Fail
81	EBC4 (CT-0)	Fail
82	EBC5	Fail
83	ECUID (CT -0)	Fail
84	EMC1_EM1	Fail
85	RQSTRequestGlobal (CT-0)	Fail
86	SAS_CAL (CT - 0)	Fail
87	VDC1 (CY -0)	Fail
88	VDC2	Pass
89	VDHR	Fail
90	(PCAN Ch 3 Tx/ TCAN Ch 2 Rx EVCU1)AMB	Fail
91	AnalogInputs	Pass
92	Command_message	Fail
93	DTE_1	Fail

# Report\_Test\_Configuration\_1

94	DTE_2	Fail
95	EC1_EM1	Pass
96	EEC2	Fail
97	EEC3_EM1	Fail
98	EMRC1_EM1	Fail
99	EnergyConsumption	Pass
100	ETC2	Pass
101	ETC7	Pass
102	EV_Faults	Pass
103	EV_Flags_Status	Pass
104	EVCU_E_Lube_control	Pass
105	EVCU_IC_Msg_1	Fail
106	EVCU_IC_Msg_2	Pass
107	EVCUBCS	Pass
108	HCU_Command1	Fail
109	HV_Battery_SOC	Pass
110	HV_Battery_Temp	Pass
111	Master_To_Slave_1	Fail
112	Master_To_Slave_2	Fail
113	Master_To_Slave_4	Fail
114	Master_To_Slave_3	Fail
115	Master_To_Slave_Wk_Up	Fail
116	Prop_VCU_Immobiliser (CT 0)	Fail
117	Pump_command	Fail
118	pumpControl_H_P1	Pass
119	RC_EM1	Pass
120	System_Message (RX is NOT defined DB)	Pass
121	TP_CM (RX is NOT defined in DB)	Pass
122	TP_DT	Pass
123	Transmissio_Error	Pass
124	Transmission_Control	Pass
125	UI1	Pass
126	UI2	Fail
127	VCU_2PCM_MSG1	Fail
128	VCUREquest	Fail
129	VEP1	Fail
130	VmuCommand	Fail
131	VmuInfo1 (CT - 0)	Fail
132	(PCAN CH 3 TX Gradient Sens/TCAN Ch 2 RX) Gradient_Sensor_Angle	Fail
133	Gradient_SensorError	Fail
134	(CCAN CH 4 TX AC Comp/ PCAN CH 3 RX AC Comp)AC_Compressor_Info1	Pass

# Report\_Test\_Configuration\_1

135	(ICAN CH 1 TX IC/TCAN CH 2 RX)AIR1	Pass
136	CCVS1_IC	Pass
137	CM1	Fail
138	Prop_IC_FuelAND_Maint (CT - 10000)	Fail
139	Prop_IC_RunInfo	Pass
140	Prop_IC_TripA (CT -10000)	Fail
141	Prop_IC_TripB (CT - 10000)	Fail
142	prop_TPMS_features_rqst_Copy_1	Pass
143	RESET (CT - 0 )	Pass
144	(PCAN CH 3 Tx MCU/ TCAN RX) TXMcuFastCommandResponse	Pass
145	McuInfo1	Pass
146	McuInfo2	Fail
147	McuMotorRotationInfo1 (CT - 0)	Fail
148	McuMotorRotationInfo2 (CT -0)	Fail
149	McuOnEvent1	Pass
150	McuProtocolVersion (CT - 0)	Fail
151	McuSlowCommandResponse	Fail
152	McuSoftwareVersion	Pass
153	McuSpecification1 (CT -0)	Fail
154	McuThermal1	Fail
155	McuThermal2	Pass
156	McuTorqueSpeedModeVoltSpec1 (CT - 0)	Fail
157	McuVoltageModeVoltSpec1 (CT - 0)	Fail
158	(PCAN CH 3 TX TCP/PCAN CH3 RX) pumpDebug	Pass
159	pumpStatus1_P1_H	Fail
160	pumpStatus2_P1_H	Pass
161	(TCAN CH 2 TX TCU/PCAN CH 3 RX)ACKM_TCU (CT -0)	Fail
162	CI_TCU (CT - 0)	Fail
163	DM2_TCU (CT -0)	Fail
164	prop_TPMS_features_rqst	Pass
165	Proprietary_DMS_1 (CT -0)	Fail
166	Proprietary_DMS3 (CT - 0)	Fail
167	Proprietary_DMS4 (CT - 0)	Fail
168	Proprietary_Tilt_Angle	Pass
169	SOFT_TCU (CT - 0)	Fail
170	SSI ( Remark- Rx node is NOT defined DB)	Pass
171	TD (Remark - received on I & P CAN but Failed)	Fail
172	(CCAN CH 4 TX TPMS/ RX)ACKM_TPMS	Pass
173	CI_TPMS	Pass
174	DM1_TPMS	Fail
175	DM2_TPMS	Pass

# Report\_Test\_Configuration\_1

176	✓ DM4_TPMS	Pass
177	✓ Prop_Sensor_ID_Response	Pass
178	✓ Prop_TPMS_learn_response	Pass
179	✓ SOFT_TPMS	Pass
180	✓ TIRE1	Pass
181	✓ TIRE2	Pass
182	✓ TPMS_data_1	Pass
183	✓ TPMS_data_2	Pass
184	✓ TPMS_Leakage_rate_1	Pass
185	✓ TPMS_Leakage_rate_2	Pass
186	✓ TPMS_Leakage_rate_3	Pass
187	✓ TPMS_Leakage_rate_4	Pass
188	✓ TPMS_Leakage_rate_5	Pass
189	✓ TPMS_Leakage_rate_6	Fail
190	✓ TPMS_Pressure_1	Pass
191	✓ TPMS_Pressure_2	Pass
192	✓ TPMS_Pressure_3	Pass
193	✓ TPMS_Status_1	Pass
194	✓ TPMS_Status_2	Pass
195	✓ TPMS_Status_3	Pass
196	✓ TPMS_Temperature_1	Pass
197	✓ TPMS_Temperature_2	Pass
198	✓ (ICAN CH 1 TX VECU/ TCU RX)CBCU_Proprietary	Pass
199	✓ CCVS1_VECU	Pass
200	✓ DC1	Pass
201	✓ EBC1_VECU	Pass
202	✓ Front_CBCU_Output_Status	Pass
203	✓ LD (CT - 0)	Fail
204	✓ RPAS_Master_Controller	Fail

## TestUnit1\_1 [1 .. 204]

Fail

### General Information

Name: TestUnit1\_1  
 Description:  
 Version:  
 File: D:\Downloads\HIL\EV\_Kratos\Cycle Time\Kratos\_Cycle\TestUnit1\TestUnit1.vtuexe  
 Last modification: 2024-08-14, 14:22:29

### >Main Part

Time Stamp	Title	Verdict
9.766668	✓ 1. (PCAN CH_3 TX/ TCAN CH_2 RX_Aux1 Comp ) x80__HD1_Debug_Currents	Pass
9.966668	✗ 2. x80__HD2_Debug_Voltages	Fail
10.166668	✓ 3. x80__HD3_Debug_Temperatures_A	Pass

# Report\_Test\_Configuration\_1

12.166668	4. x80__HD4_Debug_Temperatures_B	Pass
14.166668	5. x80__HD5_Debug_Control_Data_PMAC	Pass
14.366668	6. x80__HS1_Torque_Feedback	Pass
14.566668	7. x80__HS2_Status_Feedback	Pass
14.766668	8. x80__HS3_Temperature_Feedback	Pass
14.966668	9. x80__HS4_Fault_Info	Pass
16.966668	10. x80__HS5_Encoder_Info	Fail
17.166668	11. (PCAN CH_3 TX/ TCAN CH_2 RX_ Aux2_Ste)x81__HD1_Debug_Currents	Pass
17.366668	12. x81__HD2_Debug_Voltages	Pass
17.566668	13. x81__HD3_Debug_Temperatures_A	Pass
19.566668	14. x81__HD4_Debug_Temperatures_B	Pass
21.566668	15. x81__HD5_Debug_Control_Data_PMAC	Pass
21.766668	16. x81__HS1_Torque_Feedback	Pass
21.966668	17. x81__HS2_Status_Feedback	Fail
22.166668	18. x81__HS3_Temperature_Feedback	Pass
22.366668	19. x81__HS4_Fault_Info	Pass
24.366668	20. x81__HS5_Encoder_Info	Pass
24.566668	21. (ICAN CH 1TX/ ICAN 1 RX BAC) BAC_door_lock_horn_status	Pass
24.766668	22. (PCAN CH_3 TX/ TCAN CH_2 RX_ BCS) BCS1_EVCU1	Pass
26.766668	23. (PCAN CH 3 BMS) BMS_chgrequestparameter	Pass
27.766668	24. BMS_Pack_Branch1Cell_Voltages	Fail
27.866668	25. BMS_Pack_Branch1cellTemperature	Pass
29.866668	26. BMS_Pack_Branch2Cell_Voltages	Fail
29.966668	27. BMS_Pack_Branch2cellTemperature	Pass
31.966668	28. BMS_Pack_Branch3Cell_Voltages	Fail
32.066668	29. BMS_Pack_Branch3cellTemperature	Pass
34.066668	30. BMS_Pack_Data1	Pass
34.266668	31. BMS_Pack_Data2	Fail
35.266668	32. BMS_Pack_Data3	Pass
35.466668	33. BMS_Pack_Data4	Pass
35.666668	34. BMS_Pack_DischCurrent_Limits	Fail
35.866668	35. BMS_Pack_Flt1	Fail
36.066668	36. BMS_Pack_Flt2	Pass
36.266668	37. BMS_Pack_Flt3	Pass
36.466668	38. BMS_Pack_Info	Pass
38.466668	39. BMS_Pack_Slave_Data	Fail
39.466668	40. BMS_Pack_Status1	Fail
39.566668	41. BMS_Pack_Status2	Fail
39.666668	42. BMS_Pack_TimeToFaults	Fail
40.666668	43. BMS1_Inletandoutlettemperature	Pass
41.666668	44. BMS2_Inletandoutlettemperature	Pass

# Report\_Test\_Configuration\_1

42.666668	45. BMS2_Otherstemperature	Pass
43.666668	46. E2P_Data1	Pass
45.666668	47. E2P_Data2	Pass
47.666668	48. E2P_Data3	Pass
49.666668	49. E2P_Data4	Pass
51.666668	50. E2P_Data5	Pass
53.666668	51. E2P_Data6	Pass
55.666668	52. E2P_Data7	Pass
57.666668	53. E2P_Data8	Pass
59.666668	54. E2P_Data9	Pass
61.666668	55. E2P_Data10	Pass
63.666668	56. (ICAN Ch1Tx/ICAN Ch1 Rx CAN_Switch) CANSwitchProprietary	Pass
65.666668	57. (PCAN CH 3TX/ TCAN CH 2 RX DCDC) DCDC_Status1	Pass
66.666668	58. DCDC_Status2	Fail
67.666668	59. (ICAN CH 1 TX DMS/TCAN CH 2RX) ACKM_DMS (CY - 0)	Fail
67.686668	60. CI_DMS (CY - 0)	Fail
67.706668	61. DM1_DMS	Pass
69.706668	62. DM2_DMS (CY - 0)	Fail
69.906668	63. DM4_DMS (CY - 0)	Fail
70.106668	64. prop_DMS	Pass
70.606668	65. Propreitary_DMS_6 (CY - 0)	Fail
70.706668	66. Proprietary_DMS_2 (CY - 0)	Fail
70.806668	67. Proprietary_SW_HW_version (CY - 0)	Fail
70.906668	68. SOFT_DMS (CY - 0)	Fail
71.006668	69. (PCAN Ch 3 TXDNR/ PCAN 3 RX ECVU1)TC1_255	Fail
71.106668	70. (PCAN CH 3 TX ELub /PCAN CH 3 RX)ELubePumpData1	Fail
71.206668	71. (CCAN Ch 4 TX EBS/ TCAN CH 2 RX) ACK (CY -0)	Fail
71.306668	72. CCVS1_ABS	Fail
71.506668	73. CI (CY- 0)	Fail
71.606668	74. CI_EBS (CY - 0)	Fail
71.706668	75. CVW (CY - 0)	Fail
71.806668	76. DM1	Fail
73.806668	77. DM4 (CY - 0)	Fail
74.006668	78. EBC2	Pass
74.206668	79. EBC1	Pass
74.406668	80. EBC3	Fail
74.606668	81. EBC4 (CT-0)	Fail
74.806668	82. EBC5	Fail
75.006668	83. ECUID (CT -0)	Fail
75.206668	84. EMC1_EM1	Fail
75.406668	85. RQSTRequestGlobal (CT-0)	Fail

# Report\_Test\_Configuration\_1

75.446668	86. SAS_CAL (CT - 0)	Fail
75.486668	87. VDC1 (CY -0)	Fail
75.526668	88. VDC2	Pass
75.546668	89. VDHR	Fail
77.546668	90. (PCAN Ch 3 Tx/ TCAN Ch 2 Rx EVCU1)AMB	Fail
78.046668	91. AnalogInputs	Pass
78.246668	92. Command_message	Fail
78.346668	93. DTE_1	Fail
78.446668	94. DTE_2	Fail
78.546668	95. EC1_EM1	Pass
88.546668	96. EEC2	Fail
88.646668	97. EEC3_EM1	Fail
89.146668	98. EMRC1_EM1	Fail
89.166668	99. EnergyConsumption	Pass
91.166668	100. ETC2	Pass
91.366668	101. ETC7	Pass
91.566668	102. EV_Faults	Pass
93.566668	103. EV_Flags_Status	Pass
93.586668	104. EVCU_E_Lube_control	Pass
93.786668	105. EVCU_IC_Msg_1	Fail
94.286668	106. EVCU_IC_Msg_2	Pass
94.786668	107. EVCUBCS	Pass
96.786668	108. HCU_Command1	Fail
97.786668	109. HV_Battery_SOC	Pass
99.786668	110. HV_Battery_Temp	Pass
99.986668	111. Master_To_Slave_1	Fail
100.086668	112. Master_To_Slave_2	Fail
100.186668	113. Master_To_Slave_4	Fail
100.286668	114. Master_To_Slave_3	Fail
100.386668	115. Master_To_Slave_Wk_Up	Fail
100.486668	116. Prop_VCU_Immobilizer (CT 0)	Fail
100.586668	117. Pump_command	Fail
101.586668	118. pumpControl_H_P1	Pass
103.586668	119. RC_EM1	Pass
113.586668	120. System_Message (RX is NOT defined DB)	Pass
113.786668	121. TP_CM (RX is NOT defined in DB)	Pass
123.786668	122. TP_DT	Pass
133.786668	123. Transmissio_Error	Pass
134.786668	124. Transmission_Control	Pass
134.806668	125. UI1	Pass
135.006668	126. UI2	Fail

# Report\_Test\_Configuration\_1

135.206668	127. VCU_2PCM_MSG1	Fail
135.606668	128. VCUREquest	Fail
135.706668	129. VEP1	Fail
136.206668	130. VmuCommand	Fail
136.226668	131. VmuInfo1 (CT - 0)	Fail
136.246668	132. (PCAN CH 3 TX Gradient Sens/TCAN Ch 2 RX) Gradient_Sensor_Angle	Fail
136.266668	133. Gradient_SensorError	Fail
136.366668	134. (CCAN CH 4 TX AC Comp/ PCAN CH 3 RX AC Comp)AC_Compressor_Info1	Pass
136.566668	135. (ICAN CH 1 TX IC/TCAN CH 2 RX)AIR1	Pass
138.566668	136. CCVS1_IC	Pass
138.766668	137. CM1	Fail
140.766668	138. Prop_IC_FuelAND_Maint (CT - 10000)	Fail
160.766668	139. Prop_IC_RunInfo	Pass
160.966668	140. Prop_IC_TripA (CT -10000)	Fail
180.966668	141. Prop_IC_TripB (CT - 10000)	Fail
200.966668	142. prop_TPMS_features_rqst_Copy_1	Pass
201.166668	143. RESET (CT - 0 )	Pass
201.186668	144. (PCAN CH 3 Tx MCU/ TCAN RX) TXMcuFastCommandResponse	Pass
201.206668	145. McuInfo1	Pass
201.406668	146. McuInfo2	Fail
201.426668	147. McuMotorRotationInfo1 (CT - 0)	Fail
201.446668	148. McuMotorRotationInfo2 (CT -0)	Fail
201.466668	149. McuOnEvent1	Pass
201.666668	150. McuProtocolVersion (CT - 0)	Fail
201.866668	151. McuSlowCommandResponse	Fail
202.066668	152. McuSoftwareVersion	Pass
202.266668	153. McuSpecification1 (CT -0)	Fail
202.466668	154. McuThermal1	Fail
202.666668	155. McuThermal2	Pass
202.866668	156. McuTorqueSpeedModeVoltSpec1 (CT - 0)	Fail
202.886668	157. McuVoltageModeVoltSpec1 (CT - 0)	Fail
202.906668	158. (PCAN CH 3 TX TCP/PCAN CH3 RX) pumpDebug	Pass
204.906668	159. pumpStatus1_P1_H	Fail
206.906668	160. pumpStatus2_P1_H	Pass
210.906668	161. (TCAN CH 2 TX TCU/PCAN CH 3 RX)ACKM_TCU (CT -0)	Fail
211.106668	162. CI_TCU (CT - 0)	Fail
211.306668	163. DM2_TCU (CT -0)	Fail
211.506668	164. prop_TPMS_features_rqst	Pass
211.706668	165. Proprietary_DMS_1 (CT -0)	Fail
211.726668	166. Proprietary_DMS3 (CT - 0)	Fail
211.746668	167. Proprietary_DMS4 (CT - 0)	Fail

# Report\_Test\_Configuration\_1

211.766668	168. Proprietary_Tilt_Angle	Pass
213.766668	169. SOFT_TCU (CT - 0)	Fail
213.786668	170. SSI ( Remark- Rx node is NOT defined DB)	Pass
213.806668	171. TD (Remark - received on I & P CAN but Failed)	Fail
213.906668	172. (CCAN CH 4 TX TPMS/ RX)ACKM_TPMS	Pass
214.106668	173. CI_TPMS	Pass
214.306668	174. DM1_TPMS	Fail
216.306668	175. DM2_TPMS	Pass
216.506668	176. DM4_TPMS	Pass
216.706668	177. Prop_Sensor_ID_Response	Pass
217.706668	178. Prop_TPMS_learn_response	Pass
219.706668	179. SOFT_TPMS	Pass
219.906668	180. TIRE1	Pass
220.906668	181. TIRE2	Pass
221.906668	182. TPMS_data_1	Pass
222.106668	183. TPMS_data_2	Pass
222.306668	184. TPMS_Leakage_rate_1	Pass
232.306668	185. TPMS_Leakage_rate_2	Pass
242.306668	186. TPMS_Leakage_rate_3	Pass
252.306668	187. TPMS_Leakage_rate_4	Pass
262.306668	188. TPMS_Leakage_rate_5	Pass
272.306668	189. TPMS_Leakage_rate_6	Fail
274.306668	190. TPMS_Pressure_1	Pass
284.306668	191. TPMS_Pressure_2	Pass
294.306668	192. TPMS_Pressure_3	Pass
304.306668	193. TPMS_Status_1	Pass
314.306668	194. TPMS_Status_2	Pass
324.306668	195. TPMS_Status_3	Pass
334.306668	196. TPMS_Temperature_1	Pass
334.506668	197. TPMS_Temperature_2	Pass
334.706668	198. (ICAN CH 1 TX VECU/ TCU RX)CBCU_Proprietary	Pass
335.706668	199. CCVS1_VECU	Pass
335.906668	200. DC1	Pass
336.106668	201. EBC1_VECU	Pass
336.306668	202. Front_CBCU_Output_Status	Pass
337.306668	203. LD (CT - 0)	Fail
337.326668	204. RPAS_Master_Controller	Fail

## 1. (PCAN CH\_3 TX/ TCAN CH\_2 RX\_ Aux1 Comp ) x80\_\_HD1\_Debug\_Currents

Pass

Test case begin: 08/14/2024 02:23:22 PM +05:30

Test case end: 08/14/2024 02:23:22 PM +05:30

Duration: 00:00:00

# Report\_Test\_Configuration\_1

## Background Checks

Cycle Time Absolute		Pass																			
<b>Statistic</b>																					
Runtime of the statistic		200 ms																			
Number of samples		2																			
Message ID		422117248x (0x1928FF80x)																			
Minimal measured cycle time		100.049966 ms																			
Maximal measured cycle time		100.049966 ms																			
Average cycle time		100.049966 ms																			
Failure ratio (in %)		0																			
<b>Distribution of measured times</b>																					
=<	80 ms	82 ms - 84 ms	82 ms - 86 ms	84 ms - 88 ms	86 ms - 90 ms	88 ms - 92 ms	90 ms - 94 ms	92 ms - 96 ms	94 ms - 98 ms	96 ms - 100 ms	98 ms - 102 ms	100 ms - 104 ms	102 ms - 106 ms	104 ms - 108 ms	106 ms - 110 ms	108 ms - 112 ms	110 ms - 114 ms	112 ms - 116 ms	114 ms - 118 ms	116 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

Main Part			Verdict
Time Stamp	Test Step	Title	
9.766668		Cycle Time Absolute: Check Start	
<b>Wait</b>			
9.966668	1	Waited for 200.000 ms	
9.966668		Cycle Time Absolute: Check End	

## 2. x80\_\_HD2\_Debug\_Voltages

Fail

Test case begin: 08/14/2024 02:23:22 PM +05:30

Test case end: 08/14/2024 02:23:23 PM +05:30

Duration: 00:00:01

## Background Checks

Cycle Time Absolute		Fail
<b>Statistic</b>		
Runtime of the statistic		200 ms
Number of failures		1
Number of samples		2
Message ID		422182784x (0x1929FF80x)
Minimal measured cycle time		74.956996 ms
Maximal measured cycle time		74.956996 ms
Average cycle time		74.956996 ms
Failure ratio (in %)		100

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																				
Time Stamp	Test Step	Title																		Verdict
9.966668		Cycle Time Absolute: Check Start																		
Wait																				
10.106054	Condition violation	Cycle Time Absolute: Minimum cycle time violation for CAN message 'x80__HD2_Debug_Voltages' ID = 422182784x (0x1929FF80x) on bus CAN2. Time = 74.956996 ms																		Fail
10.166668	1	Waited for 200.000 ms																		
10.166668		Test aborted due to BreakOnFail behavior.																		
10.166668		Cycle Time Absolute: Check End																		

## 3. x80\_\_HD3\_Debug\_Temperatures\_A

Pass

Test case begin: 08/14/2024 02:23:23 PM +05:30

Test case end: 08/14/2024 02:23:25 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		
Runtime of the statistic	2000 ms	
Number of samples	2	
Message ID	422248320x (0x192AFF80x)	
Minimal measured cycle time	1000.521476 ms	
Maximal measured cycle time	1000.521476 ms	
Average cycle time	1000.521476 ms	
Failure ratio (in %)	0	

### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
10.166668		Cycle Time Absolute: Check Start	
Wait			
12.166668	1	Waited for 2000.000 ms	

# Report\_Test\_Configuration\_1

12.166668

 Cycle Time Absolute: Check End

Pass

## 4. x80\_\_HD4\_Debug\_Temperatures\_B

Test case begin: 08/14/2024 02:23:25 PM +05:30

Test case end: 08/14/2024 02:23:27 PM +05:30

Duration: 00:00:02

### Background Checks

 Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	422313856x (0x192BFF80x)
Minimal measured cycle time	1000.721295 ms
Maximal measured cycle time	1000.721295 ms
Average cycle time	1000.721295 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
12.166668		 <u>Cycle Time Absolute</u> : Check Start	
 Wait			
14.166668	1	Waited for 2000.000 ms	
14.166668		 <u>Cycle Time Absolute</u> : Check End	

## 5. x80\_\_HD5\_Debug\_Control\_Data\_PMAC

Pass

Test case begin: 08/14/2024 02:23:27 PM +05:30

Test case end: 08/14/2024 02:23:27 PM +05:30

Duration: 00:00:00

### Background Checks

 Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		422379392x (0x192CFF80x)																
Minimal measured cycle time		99.834345 ms																
Maximal measured cycle time		99.834345 ms																
Average cycle time		99.834345 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
14.166668		⬇️ Cycle Time Absolute: Check Start	
14.366668	1	Waited for 200.000 ms	
14.366668		⬇️ Cycle Time Absolute: Check End	

## 6. x80\_\_HS1\_Torque\_Feedback

Pass

Test case begin: 08/14/2024 02:23:27 PM +05:30

Test case end: 08/14/2024 02:23:27 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																				Pass
Statistic																				
Runtime of the statistic		200 ms																		
Number of samples		2																		
Message ID		421068672x (0x1918FF80x)																		
Minimal measured cycle time		108.940871 ms																		
Maximal measured cycle time		108.940871 ms																		
Average cycle time		108.940871 ms																		
Failure ratio (in %)		0																		

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
14.366668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
14.566668	1	Waited for 200.000 ms	
14.566668		⬇️ Cycle Time Absolute: Check End	

## 7. x80\_\_HS2\_Status\_Feedback

Pass

Test case begin: 08/14/2024 02:23:27 PM +05:30

Test case end: 08/14/2024 02:23:27 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	421134208x (0x1919FF80x)
Minimal measured cycle time	99.441819 ms
Maximal measured cycle time	99.441819 ms
Average cycle time	99.441819 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
14.566668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
14.766668	1	Waited for 200.000 ms	
14.766668		⬇️ Cycle Time Absolute: Check End	

## 8. x80\_\_HS3\_Temperature\_Feedback

Pass

Test case begin: 08/14/2024 02:23:27 PM +05:30

Test case end: 08/14/2024 02:23:27 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		421199744x (0x191AFF80x)																
Minimal measured cycle time		100.001818 ms																
Maximal measured cycle time		100.001818 ms																
Average cycle time		100.001818 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
14.766668		⬇️ Cycle Time Absolute: Check Start	
Wait			
14.966668	1	Waited for 200.000 ms	
14.966668		⬇️ Cycle Time Absolute: Check End	

## 9. x80\_\_HS4\_Fault\_Info

Pass

Test case begin: 08/14/2024 02:23:27 PM +05:30

Test case end: 08/14/2024 02:23:29 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute																					Pass
Statistic																					
Runtime of the statistic		2000 ms																			
Number of samples		2																			
Message ID		421265280x (0x191BFF80x)																			
Minimal measured cycle time		1001.238216 ms																			
Maximal measured cycle time		1001.238216 ms																			
Average cycle time		1001.238216 ms																			
Failure ratio (in %)		0																			

Distribution of measured times																					
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
14.966668		⬇️ Cycle Time Absolute: Check Start	
Wait			
16.966668	1	Waited for 2000.000 ms	
16.966668		⬇️ Cycle Time Absolute: Check End	

## 10. x80\_HS5\_Encoder\_Info

Fail

Test case begin: 08/14/2024 02:23:29 PM +05:30

Test case end: 08/14/2024 02:23:30 PM +05:30

Duration: 00:00:01

### Background Checks

⬇️ Cycle Time Absolute		Fail																			
<strong>Statistic</strong>																					
Runtime of the statistic		200 ms																			
Number of failures		1																			
Number of samples		2																			
Message ID		421330816x (0x191CFF80x)																			
Minimal measured cycle time		77.548164 ms																			
Maximal measured cycle time		77.548164 ms																			
Average cycle time		77.548164 ms																			
Failure ratio (in %)		100																			
<strong>Distribution of measured times</strong>																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
16.966668		⬇️ Cycle Time Absolute: Check Start	
Wait			
17.108424	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'x80_HS5_Encoder_Info' ID = 421330816x (0x191CFF80x) on bus CAN2. Time = 77.548164 ms	Fail
17.166668	1	Waited for 200.000 ms	
17.166668		Test aborted due to BreakOnFail behavior.	
17.166668		⬇️ Cycle Time Absolute: Check End	

## 11. (PCAN CH\_3 TX/ TCAN CH\_2 RX\_Aux2\_Ste)x81\_HD1\_Debug\_Currents

Pass

Test case begin: 08/14/2024 02:23:30 PM +05:30

Test case end: 08/14/2024 02:23:30 PM +05:30

Duration: 00:00:00

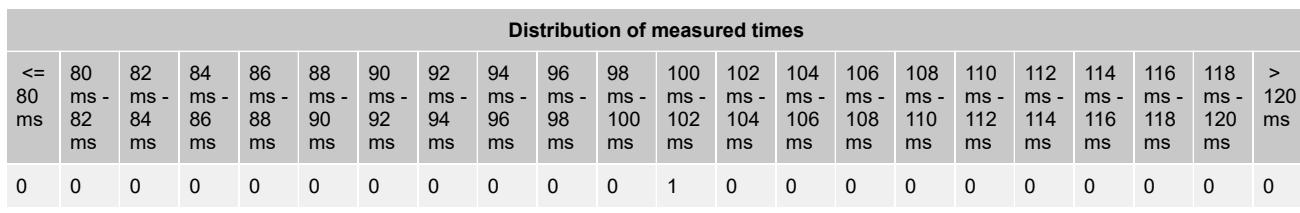
# Report\_Test\_Configuration\_1

## Background Checks

### ↙⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	422117249x (0x1928FF81x)
Minimal measured cycle time	100.245028 ms
Maximal measured cycle time	100.245028 ms
Average cycle time	100.245028 ms
Failure ratio (in %)	0



## ↙ Main Part

Time Stamp	Test Step	Title	Verdict
17.166668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
17.366668	1	Waited for 200.000 ms	
17.366668		⬇️ Cycle Time Absolute: Check End	

## 12. x81\_\_HD2\_Debug\_Voltages

Pass

Test case begin: 08/14/2024 02:23:30 PM +05:30

Test case end: 08/14/2024 02:23:30 PM +05:30

Duration: 00:00:00

## Background Checks

### ↙⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	422182785x (0x1929FF81x)
Minimal measured cycle time	106.733154 ms
Maximal measured cycle time	106.733154 ms
Average cycle time	106.733154 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																						
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
17.366668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
17.566668	1	Waited for 200.000 ms	
17.566668		⬇️ Cycle Time Absolute: Check End	

## 13. x81\_\_HD3\_Debug\_Temperatures\_A

Pass

Test case begin: 08/14/2024 02:23:30 PM +05:30

Test case end: 08/14/2024 02:23:32 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic	2000 ms	
Number of samples	2	
Message ID	422248321x (0x192AFF81x)	
Minimal measured cycle time	999.675766 ms	
Maximal measured cycle time	999.675766 ms	
Average cycle time	999.675766 ms	
Failure ratio (in %)	0	

## Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
17.566668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
19.566668	1	Waited for 2000.000 ms	
19.566668		⬇️ Cycle Time Absolute: Check End	

## 14. x81\_\_HD4\_Debug\_Temperatures\_B

Pass

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:23:32 PM +05:30

Test case end: 08/14/2024 02:23:34 PM +05:30

Duration: 00:00:02

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	422313857x (0x192BFF81x)
Minimal measured cycle time	997.798176 ms
Maximal measured cycle time	997.798176 ms
Average cycle time	997.798176 ms
Failure ratio (in %)	0

### Distribution of measured times

<=	980 ms	982 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
19.566668		Cycle Time Absolute: Check Start	
21.566668	1	Waited for 2000.000 ms	
21.566668		Cycle Time Absolute: Check End	

## 15. x81\_\_HD5\_Debug\_Control\_Data\_PMAC

Pass

Test case begin: 08/14/2024 02:23:34 PM +05:30

Test case end: 08/14/2024 02:23:34 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	422379393x (0x192CFF81x)
Minimal measured cycle time	99.91808 ms
Maximal measured cycle time	99.91808 ms
Average cycle time	99.91808 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
21.566668		⬇️ Cycle Time Absolute: Check Start	
Wait			
21.766668	1	Waited for 200.000 ms	
21.766668		⬇️ Cycle Time Absolute: Check End	

## 16. x81\_HS1\_Torque\_Feedback

Pass

Test case begin: 08/14/2024 02:23:34 PM +05:30

Test case end: 08/14/2024 02:23:34 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	421068673x (0x1918FF81x)
Minimal measured cycle time	99.89868 ms
Maximal measured cycle time	99.89868 ms
Average cycle time	99.89868 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
21.766668		⬇️ Cycle Time Absolute: Check Start	
Wait			
21.966668	1	Waited for 200.000 ms	
21.966668		⬇️ Cycle Time Absolute: Check End	

## 17. x81\_HS2\_Status\_Feedback

Fail

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:23:34 PM +05:30

Test case end: 08/14/2024 02:23:35 PM +05:30

Duration: 00:00:01

## Background Checks

### ⚠️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	1
Number of samples	2
Message ID	421134209x (0x1919FF81x)
Minimal measured cycle time	80.010333 ms
Maximal measured cycle time	80.010333 ms
Average cycle time	80.010333 ms
Failure ratio (in %)	100

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
21.966668		⬇️ Cycle Time Absolute: Check Start	
Wait			
22.114336	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'x81__HS2_Status_Feedback' ID = 421134209x (0x1919FF81x) on bus CAN2. Time = 80.010333 ms	Fail
22.166668	1	Waited for 200.000 ms	
22.166668		Test aborted due to BreakOnFail behavior.	
22.166668		⬇️ Cycle Time Absolute: Check End	

## 18. x81\_\_HS3\_Temperature\_Feedback

Pass

Test case begin: 08/14/2024 02:23:35 PM +05:30

Test case end: 08/14/2024 02:23:35 PM +05:30

Duration: 00:00:00

## Background Checks

### ⚠️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		421199745x (0x191AFF81x)																
Minimal measured cycle time		100.103841 ms																
Maximal measured cycle time		100.103841 ms																
Average cycle time		100.103841 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
22.166668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
22.366668	1	Waited for 200.000 ms	
22.366668		⬇️ Cycle Time Absolute: Check End	

## 19. x81\_\_HS4\_Fault\_Info

Pass

Test case begin: 08/14/2024 02:23:35 PM +05:30

Test case end: 08/14/2024 02:23:37 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute																					Pass
Statistic																					
Runtime of the statistic		2000 ms																			
Number of samples		2																			
Message ID		421265281x (0x191BFF81x)																			
Minimal measured cycle time		999.841411 ms																			
Maximal measured cycle time		999.841411 ms																			
Average cycle time		999.841411 ms																			
Failure ratio (in %)		0																			

Distribution of measured times																					
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
22.366668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
24.366668	1	Waited for 2000.000 ms	
24.366668		⬇️ Cycle Time Absolute: Check End	

## 20. x81\_\_HS5\_Encoder\_Info

Pass

Test case begin: 08/14/2024 02:23:37 PM +05:30

Test case end: 08/14/2024 02:23:37 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	421330817x (0x191CFF81x)
Minimal measured cycle time	107.343908 ms
Maximal measured cycle time	107.343908 ms
Average cycle time	107.343908 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
24.366668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
24.566668	1	Waited for 200.000 ms	
24.566668		⬇️ Cycle Time Absolute: Check End	

## 21. (ICAN CH 1TX/ ICAN 1 RX BAC) BAC\_door\_lock\_horn\_status

Pass

Test case begin: 08/14/2024 02:23:37 PM +05:30

Test case end: 08/14/2024 02:23:37 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																									
Runtime of the statistic		200 ms																							
Number of samples		2																							
Message ID		419368940x (0x18FF0FECx)																							
Minimal measured cycle time		100.504314 ms																							
Maximal measured cycle time		100.504314 ms																							
Average cycle time		100.504314 ms																							
Failure ratio (in %)		0																							

Distribution of measured times																									
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms				
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
24.566668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
24.766668	1	Waited for 200.000 ms	
24.766668		⬇️ Cycle Time Absolute: Check End	

## 22. (PCAN CH\_3 TX/ TCAN CH\_2 RX\_ BCS) BCS1\_EVCU1

Pass

Test case begin: 08/14/2024 02:23:37 PM +05:30

Test case end: 08/14/2024 02:23:39 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute																									
Statistic																									
Runtime of the statistic		2000 ms																							
Number of samples		2																							
Message ID		418326402x (0x18EF2782x)																							
Minimal measured cycle time		1000.20452 ms																							
Maximal measured cycle time		1000.20452 ms																							
Average cycle time		1000.20452 ms																							
Failure ratio (in %)		0																							

Distribution of measured times																									
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms				
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
24.766668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
26.766668	1	Waited for 2000.000 ms	
26.766668		⬇️ Cycle Time Absolute: Check End	

## 23. (PCAN CH 3 BMS) BMS\_chgrequestparameter

Pass

Test case begin: 08/14/2024 02:23:39 PM +05:30

Test case end: 08/14/2024 02:23:40 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	218056947x (0xCFF48F3x)
Minimal measured cycle time	490.290381 ms
Maximal measured cycle time	490.290381 ms
Average cycle time	490.290381 ms
Failure ratio (in %)	0

Distribution of measured times																					
<=	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
26.766668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
27.766668	1	Waited for 1000.000 ms	
27.766668		⬇️ Cycle Time Absolute: Check End	

## 24. BMS\_Pack\_Branch1Cell\_Voltages

Fail

Test case begin: 08/14/2024 02:23:40 PM +05:30

Test case end: 08/14/2024 02:23:40 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																			
Runtime of the statistic		100 ms																	
Number of failures		1																	
Number of samples		2																	
Message ID		218055411x (0xCFF42F3x)																	
Minimal measured cycle time		39.165839 ms																	
Maximal measured cycle time		39.165839 ms																	
Average cycle time		39.165839 ms																	
Failure ratio (in %)		100																	

Distribution of measured times																				
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms > 70 ms
0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part		Test Step																		Verdict																	
Time Stamp	Test Step	Title																																			
27.766668																																					
↓ Cycle Time Absolute: Check Start																																					
Wait																																					
27.854392	Condition violation																		Fail																		
↓ ! Cycle Time Absolute: Minimum cycle time violation for CAN message 'BMS_Pack_Branch1Cell_Voltages' ID = 218055411x (0xCFF42F3x) on bus CAN3. Time = 39.165839 ms																																					
27.866668	1																																				
27.866668	Test aborted due to BreakOnFail behavior.																																				
27.866668	↓ Cycle Time Absolute: Check End																																				

## 25. BMS\_Pack\_Branch1cellTemperature

Pass

Test case begin: 08/14/2024 02:23:40 PM +05:30

Test case end: 08/14/2024 02:23:42 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute		Statistic																		Pass
Runtime of the statistic		2000 ms																		
Number of samples		2																		
Message ID		419426803x (0x18FFF1F3x)																		
Minimal measured cycle time		999.471988 ms																		
Maximal measured cycle time		999.471988 ms																		
Average cycle time		999.471988 ms																		
Failure ratio (in %)		0																		

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 102 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
27.866668		⬇️ Cycle Time Absolute: Check Start	
Wait			
29.866668	1	Waited for 2000.000 ms	
29.866668		⬇️ Cycle Time Absolute: Check End	

## 26. BMS\_Pack\_Branch2Cell\_Voltages

Fail

Test case begin: 08/14/2024 02:23:42 PM +05:30

Test case end: 08/14/2024 02:23:42 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	2
Message ID	218055667x (0xCFF43F3x)
Minimal measured cycle time	31.036706 ms
Maximal measured cycle time	31.036706 ms
Average cycle time	31.036706 ms
Failure ratio (in %)	100

#### Distribution of measured times

<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
29.866668		⬇️ Cycle Time Absolute: Check Start	
Wait			
29.958126	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'BMS_Pack_Branch2Cell_Voltages' ID = 218055667x (0xCFF43F3x) on bus CAN2. Time = 31.036706 ms	Fail
29.966668	1	Waited for 100.000 ms	

# Report\_Test\_Configuration\_1

29.966668		Test aborted due to BreakOnFail behavior.
29.966668		<b>Cycle Time Absolute:</b> Check End

## 27. BMS\_Pack\_Branch2cellTemperature

Pass

Test case begin: 08/14/2024 02:23:42 PM +05:30

Test case end: 08/14/2024 02:23:44 PM +05:30

Duration: 00:00:02

### Background Checks

#### **Cycle Time Absolute**

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419427571x (0x18FFF4F3x)
Minimal measured cycle time	996.767588 ms
Maximal measured cycle time	996.767588 ms
Average cycle time	996.767588 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0

### >Main Part

Time Stamp	Test Step	Title	Verdict
29.966668		<b>Cycle Time Absolute:</b> Check Start	
<b>Wait</b>			
31.966668	1	Waited for 2000.000 ms	
31.966668		<b>Cycle Time Absolute:</b> Check End	

## 28. BMS\_Pack\_Branch3Cell\_Voltages

Fail

Test case begin: 08/14/2024 02:23:44 PM +05:30

Test case end: 08/14/2024 02:23:44 PM +05:30

Duration: 00:00:00

### Background Checks

#### **Cycle Time Absolute**

Fail

# Report\_Test\_Configuration\_1

Statistic																			
Runtime of the statistic		100 ms																	
Number of failures		1																	
Number of samples		1																	
Message ID		218055923x (0xCFF44F3x)																	
Minimal measured cycle time		0 ms																	
Maximal measured cycle time		0 ms																	
Average cycle time		0 ms																	
Failure ratio (in %)		0																	

Distribution of measured times																			
<=	30 ms	32 ms	32 ms	34 ms	34 ms	36 ms	36 ms	38 ms	38 ms	40 ms	40 ms	42 ms	42 ms	44 ms	44 ms	46 ms	46 ms	48 ms	48 ms
30 ms	32 ms	32 ms	34 ms	34 ms	36 ms	36 ms	38 ms	38 ms	40 ms	40 ms	42 ms	42 ms	44 ms	44 ms	46 ms	46 ms	48 ms	48 ms	> 70 ms

Main Part																																					
Time Stamp	Test Step	Title																		Verdict																	
31.966668		 Cycle Time Absolute: Check Start																																			
 Wait																																					
32.031668																				Fail																	
Condition violation at 32.026668		 Cycle Time Absolute: Maximum cycle time violation for CAN message 'BMS_Pack_Branch3Cell_Voltages' ID = 218055923x (0xCFF44F3x) on bus CAN2.																																			
32.066668		1																																			
32.066668		Waited for 100.000 ms																																			
32.066668		Test aborted due to BreakOnFail behavior.																																			
32.066668		 Cycle Time Absolute: Check End																																			

## 29. BMS\_Pack\_Branch3cellTemperature

Pass

Test case begin: 08/14/2024 02:23:44 PM +05:30

Test case end: 08/14/2024 02:23:46 PM +05:30

Duration: 00:00:02

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419427315x (0x18FFF3F3x)
Minimal measured cycle time	1000.908522 ms
Maximal measured cycle time	1000.908522 ms
Average cycle time	1000.908522 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
32.066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
34.066668	1	Waited for 2000.000 ms	
34.066668		⬇️ Cycle Time Absolute: Check End	

## 30. BMS\_Pack\_Data1

Pass

Test case begin: 08/14/2024 02:23:46 PM +05:30

Test case end: 08/14/2024 02:23:47 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	218072563x (0xCFF85F3x)
Minimal measured cycle time	100.305946 ms
Maximal measured cycle time	100.305946 ms
Average cycle time	100.305946 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
34.066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
34.266668	1	Waited for 200.000 ms	
34.266668		⬇️ Cycle Time Absolute: Check End	

## 31. BMS\_Pack\_Data2

Fail

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:23:47 PM +05:30

Test case end: 08/14/2024 02:23:48 PM +05:30

Duration: 00:00:01

## Background Checks

### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	1000 ms
Number of failures	1
Number of samples	2
Message ID	218073075x (0xCFF87F3x)
Minimal measured cycle time	510.509423 ms
Maximal measured cycle time	510.509423 ms
Average cycle time	510.509423 ms
Failure ratio (in %)	100

### Distribution of measured times

<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
34.266668		Cycle Time Absolute: Check Start	
<b>Wait</b>			
35.013051	Condition violation	Cycle Time Absolute: Maximum cycle time violation for CAN message 'BMS_Pack_Data2' ID = 218073075x (0xCFF87F3x) on bus CAN3. Time = 510.509423 ms	Fail
35.266668	1	Waited for 1000.000 ms	
35.266668		Test aborted due to BreakOnFail behavior.	
35.266668		Cycle Time Absolute: Check End	

## 32. BMS\_Pack\_Data3

Pass

Test case begin: 08/14/2024 02:23:48 PM +05:30

Test case end: 08/14/2024 02:23:48 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		218056691x (0xCFF47F3x)																
Minimal measured cycle time		99.591749 ms																
Maximal measured cycle time		99.591749 ms																
Average cycle time		99.591749 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
35.266668		⬇️ Cycle Time Absolute: Check Start	
Wait			
35.466668	1	Waited for 200.000 ms	
35.466668		⬇️ Cycle Time Absolute: Check End	

## 33. BMS\_Pack\_Data4

Pass

Test case begin: 08/14/2024 02:23:48 PM +05:30

Test case end: 08/14/2024 02:23:48 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																				Pass
Statistic																				
Runtime of the statistic		200 ms																		
Number of samples		2																		
Message ID		218072819x (0xCFF86F3x)																		
Minimal measured cycle time		95.66196 ms																		
Maximal measured cycle time		95.66196 ms																		
Average cycle time		95.66196 ms																		
Failure ratio (in %)		0																		

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
35.466668		⬇️ Cycle Time Absolute: Check Start	
Wait			
35.666668	1	Waited for 200.000 ms	
35.666668		⬇️ Cycle Time Absolute: Check End	

## 34. BMS\_Pack\_DischCurrent\_Limits

Fail

Test case begin: 08/14/2024 02:23:48 PM +05:30

Test case end: 08/14/2024 02:23:48 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Fail																			
<b>Statistic</b>																					
Runtime of the statistic																					
Runtime of the statistic	200 ms																				
Number of failures	3																				
Number of samples	4																				
Message ID	218099955x (0xCFFF0F3x)																				
Minimal measured cycle time	31.739387 ms																				
Maximal measured cycle time	66.573136 ms																				
Average cycle time	43.817822 ms																				
Failure ratio (in %)	100																				
<b>Distribution of measured times</b>																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
35.666668		⬇️ Cycle Time Absolute: Check Start	
Wait			
35.759042	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'BMS_Pack_DischCurrent_Limits' ID = 218099955x (0xCFFF0F3x) on bus CAN2. Time = 33.140945 ms	Fail
35.825616	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'BMS_Pack_DischCurrent_Limits' ID = 218099955x (0xCFFF0F3x) on bus CAN2. Time = 66.573136 ms	Fail
35.857355	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'BMS_Pack_DischCurrent_Limits' ID = 218099955x (0xCFFF0F3x) on bus CAN2. Time = 31.739387 ms	Fail
35.866668	1	Waited for 200.000 ms	
35.866668		Test aborted due to BreakOnFail behavior.	
35.866668		⬇️ Cycle Time Absolute: Check End	

# Report\_Test\_Configuration\_1

## 35. BMS\_Pack\_Flt1

Fail

Test case begin: 08/14/2024 02:23:48 PM +05:30

Test case end: 08/14/2024 02:23:48 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⚠️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	1
Number of samples	2
Message ID	218072307x (0xCFF84F3x)
Minimal measured cycle time	114.248336 ms
Maximal measured cycle time	114.248336 ms
Average cycle time	114.248336 ms
Failure ratio (in %)	100

Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0

### >Main Part

Time Stamp	Test Step	Title	Verdict
35.866668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
36.022681	Condition violation	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'BMS_Pack_Flt1' ID = 218072307x (0xCFF84F3x) on bus CAN3. Time = 114.248336 ms	Fail
36.066668	1	Waited for 200.000 ms	
36.066668		Test aborted due to BreakOnFail behavior.	
36.066668		⬇️ Cycle Time Absolute: Check End	

## 36. BMS\_Pack\_Flt2

Pass

Test case begin: 08/14/2024 02:23:48 PM +05:30

Test case end: 08/14/2024 02:23:49 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⚠️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		218072051x (0xCFF83F3x)																
Minimal measured cycle time		99.965422 ms																
Maximal measured cycle time		99.965422 ms																
Average cycle time		99.965422 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
36.066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
36.266668	1	Waited for 200.000 ms	
36.266668		⬇️ Cycle Time Absolute: Check End	

## 37. BMS\_Pack\_Flt3

Pass

Test case begin: 08/14/2024 02:23:49 PM +05:30

Test case end: 08/14/2024 02:23:49 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																				Pass
Statistic																				
Runtime of the statistic		200 ms																		
Number of samples		2																		
Message ID		218071795x (0xCFF82F3x)																		
Minimal measured cycle time		100.397873 ms																		
Maximal measured cycle time		100.397873 ms																		
Average cycle time		100.397873 ms																		
Failure ratio (in %)		0																		

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			Verdict
Time Stamp	Test Step	Title	
36.266668		⬇️ Cycle Time Absolute: Check Start	
Wait			
36.466668	1	Waited for 200.000 ms	
36.466668		⬇️ Cycle Time Absolute: Check End	

## 38. BMS\_Pack\_Info

Pass

Test case begin: 08/14/2024 02:23:49 PM +05:30

Test case end: 08/14/2024 02:23:51 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute		Pass																		
Statistic																				
Runtime of the statistic																				
Number of samples																				
Message ID																				
Minimal measured cycle time																				
Maximal measured cycle time																				
Average cycle time																				
Failure ratio (in %)																				
Distribution of measured times																				
<= 980 ms - > 1020 ms																				
980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

Main Part			Verdict
Time Stamp	Test Step	Title	
36.466668		⬇️ Cycle Time Absolute: Check Start	
Wait			
38.466668	1	Waited for 2000.000 ms	
38.466668		⬇️ Cycle Time Absolute: Check End	

## 39. BMS\_Pack\_Slave\_Data

Fail

Test case begin: 08/14/2024 02:23:51 PM +05:30

Test case end: 08/14/2024 02:23:52 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute		Fail
Background Checks		

# Report\_Test\_Configuration\_1

Statistic																				
Runtime of the statistic	1000 ms																			
Number of failures	1																			
Number of samples	2																			
Message ID	218073587x (0xCFF89F3x)																			
Minimal measured cycle time	510.186222 ms																			
Maximal measured cycle time	510.186222 ms																			
Average cycle time	510.186222 ms																			
Failure ratio (in %)	100																			
Distribution of measured times																				
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms > 520 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0

Main Part		Test Log																	
Time Stamp	Test Step	Title																	Verdict
38.466668		 <b>Cycle Time Absolute:</b> Check Start																	
Wait																			
39.013349	Condition violation	 <b>Cycle Time Absolute:</b> Maximum cycle time violation for CAN message 'BMS_Pack_Slave_Data' ID = 218073587x (0xCFF89F3x) on bus CAN3. Time = 510.186222 ms																	Fail
39.466668	1	Waited for 1000.000 ms																	
39.466668		Test aborted due to BreakOnFail behavior.																	
39.466668		 <b>Cycle Time Absolute:</b> Check End																	

## 40. BMS\_Pack\_Status1

Fail

Test case begin: 08/14/2024 02:23:52 PM +05:30

Test case end: 08/14/2024 02:23:52 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Verdict
Statistic		
Runtime of the statistic		100 ms
Number of failures		2
Number of samples		2
Message ID		218074611x (0xCFF8DF3x)
Minimal measured cycle time		25.531726 ms
Maximal measured cycle time		25.531726 ms
Average cycle time		25.531726 ms
Failure ratio (in %)		100

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part		Time Stamp		Test Step		Title																		Verdict
		39.466668				Cycle Time Absolute: Check Start																		
Wait																								
39.531668		Condition violation at 39.526668				Cycle Time Absolute: Maximum cycle time violation for CAN message 'BMS_Pack_Status1' ID = 218074611x (0xCFF8DF3x) on bus CAN2.																		Fail
39.557847		Condition violation				Cycle Time Absolute: Minimum cycle time violation for CAN message 'BMS_Pack_Status1' ID = 218074611x (0xCFF8DF3x) on bus CAN2. Time = 25.531726 ms																		Fail
39.566668		1				Waited for 100.000 ms																		
39.566668						Test aborted due to BreakOnFail behavior.																		
39.566668						Cycle Time Absolute: Check End																		

## 41. BMS\_Pack\_Status2

Fail

Test case begin: 08/14/2024 02:23:52 PM +05:30

Test case end: 08/14/2024 02:23:52 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Time Stamp		Test Step		Title																		Verdict																						
Statistic																																														
Runtime of the statistic																																														
Number of failures																																														
Number of samples																																														
Message ID																																														
Minimal measured cycle time																																														
Maximal measured cycle time																																														
Average cycle time																																														
Failure ratio (in %)																																														

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp		Test Step		Title																				Verdict
39.566668				Cycle Time Absolute: Check Start																				

# Report\_Test\_Configuration\_1

Wait		
39.656902	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'BMS_Pack_Status2' ID = 218074867x (0xCFF8EF3x) on bus CAN2. Time = 31.009209 ms Fail
39.666668	1	Waited for 100.000 ms
39.666668		Test aborted due to BreakOnFail behavior.
39.666668		⬇️ <b>Cycle Time Absolute:</b> Check End

## 42. BMS\_Pack\_TimeToFaults

Fail

Test case begin: 08/14/2024 02:23:52 PM +05:30

Test case end: 08/14/2024 02:23:53 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute		Fail																			
<b>Statistic</b>																					
Runtime of the statistic																					
Number of failures																					
Number of samples																					
Message ID																					
Minimal measured cycle time																					
Maximal measured cycle time																					
Average cycle time																					
Failure ratio (in %)																					
<b>Distribution of measured times</b>																					
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
39.666668		⬇️ <b>Cycle Time Absolute:</b> Check Start	
Wait			
40.504642	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'BMS_Pack_TimeToFaults' ID = 218075123x (0xCFF8FF3x) on bus CAN3. Time = 487.683873 ms	Fail
40.666668	1	Waited for 1000.000 ms	
40.666668		Test aborted due to BreakOnFail behavior.	
40.666668		⬇️ <b>Cycle Time Absolute:</b> Check End	

## 43. BMS1\_Inletandoutlettemperature

Pass

Test case begin: 08/14/2024 02:23:53 PM +05:30

Test case end: 08/14/2024 02:23:54 PM +05:30

Duration: 00:00:01

### Background Checks

# Report\_Test\_Configuration\_1

Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic		1000 ms																			
Number of samples		2																			
Message ID		419384051x (0x18FF4AF3x)																			
Minimal measured cycle time		490.229844 ms																			
Maximal measured cycle time		490.229844 ms																			
Average cycle time		490.229844 ms																			
Failure ratio (in %)		0																			
Distribution of measured times																					
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
40.666668		Cycle Time Absolute: Check Start	
Wait			
41.666668	1	Waited for 1000.000 ms	
41.666668		Cycle Time Absolute: Check End	

## 44. BMS2\_Inletandoutlettemperature

Pass

Test case begin: 08/14/2024 02:23:54 PM +05:30

Test case end: 08/14/2024 02:23:55 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic		1000 ms																			
Number of samples		2																			
Message ID		419384563x (0x18FF4CF3x)																			
Minimal measured cycle time		490.280477 ms																			
Maximal measured cycle time		490.280477 ms																			
Average cycle time		490.280477 ms																			
Failure ratio (in %)		0																			
Distribution of measured times																					
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
41.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
42.666668	1	Waited for 1000.000 ms	
42.666668		⬇️ Cycle Time Absolute: Check End	

## 45. BMS2\_Otherstemperature

Pass

Test case begin: 08/14/2024 02:23:55 PM +05:30

Test case end: 08/14/2024 02:23:56 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	419385075x (0x18FF4EF3x)
Minimal measured cycle time	490.398386 ms
Maximal measured cycle time	490.398386 ms
Average cycle time	490.398386 ms
Failure ratio (in %)	0

Distribution of measured times																					
<=	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
42.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
43.666668	1	Waited for 1000.000 ms	
43.666668		⬇️ Cycle Time Absolute: Check End	

## 46. E2P\_Data1

Pass

Test case begin: 08/14/2024 02:23:56 PM +05:30

Test case end: 08/14/2024 02:23:58 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic									
Runtime of the statistic		2000 ms							
Number of samples		2							
Message ID		419383795x (0x18FF49F3x)							
Minimal measured cycle time		1000.708061 ms							
Maximal measured cycle time		1000.708061 ms							
Average cycle time		1000.708061 ms							
Failure ratio (in %)		0							

Distribution of measured times																									
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms				
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
43.666668		⬇️ Cycle Time Absolute: Check Start	
45.666668	1	Waited for 2000.000 ms	
45.666668		⬇️ Cycle Time Absolute: Check End	

## 47. E2P\_Data2

Pass

Test case begin: 08/14/2024 02:23:58 PM +05:30

Test case end: 08/14/2024 02:24:00 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute																										Pass
Statistic										Distribution of measured times																
Runtime of the statistic		2000 ms																								
Number of samples		2																								
Message ID		419384307x (0x18FF4BF3x)																								
Minimal measured cycle time		999.642735 ms																								
Maximal measured cycle time		999.642735 ms																								
Average cycle time		999.642735 ms																								
Failure ratio (in %)		0																								
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms					
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
45.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
47.666668	1	Waited for 2000.000 ms	
47.666668		⬇️ Cycle Time Absolute: Check End	

## 48. E2P\_Data3

Pass

Test case begin: 08/14/2024 02:24:00 PM +05:30

Test case end: 08/14/2024 02:24:02 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

##### Statistic

Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419384819x (0x18FF4DF3x)
Minimal measured cycle time	999.639163 ms
Maximal measured cycle time	999.639163 ms
Average cycle time	999.639163 ms
Failure ratio (in %)	0

##### Distribution of measured times

<= 980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
47.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
49.666668	1	Waited for 2000.000 ms	
49.666668		⬇️ Cycle Time Absolute: Check End	

## 49. E2P\_Data4

Pass

Test case begin: 08/14/2024 02:24:02 PM +05:30

Test case end: 08/14/2024 02:24:04 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic									
Runtime of the statistic		2000 ms							
Number of samples		2							
Message ID		419385331x (0x18FF4FF3x)							
Minimal measured cycle time		997.335858 ms							
Maximal measured cycle time		997.335858 ms							
Average cycle time		997.335858 ms							
Failure ratio (in %)		0							

Distribution of measured times																									
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms				
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
49.666668		⬇️ Cycle Time Absolute: Check Start	
Wait			
51.666668	1	Waited for 2000.000 ms	
51.666668		⬇️ Cycle Time Absolute: Check End	

## 50. E2P\_Data5

Pass

Test case begin: 08/14/2024 02:24:04 PM +05:30

Test case end: 08/14/2024 02:24:06 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute		Pass																						
<b>Statistic</b>																								
Runtime of the statistic																								
Number of samples																								
Message ID																								
Minimal measured cycle time																								
Maximal measured cycle time																								
Average cycle time																								
Failure ratio (in %)																								
<b>Distribution of measured times</b>																								
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms			
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
51.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
53.666668	1	Waited for 2000.000 ms	
53.666668		⬇️ Cycle Time Absolute: Check End	

## 51. E2P\_Data6

Pass

Test case begin: 08/14/2024 02:24:06 PM +05:30

Test case end: 08/14/2024 02:24:08 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419406835x (0x18FFA3F3x)
Minimal measured cycle time	1000.862982 ms
Maximal measured cycle time	1000.862982 ms
Average cycle time	1000.862982 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
53.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
55.666668	1	Waited for 2000.000 ms	
55.666668		⬇️ Cycle Time Absolute: Check End	

## 52. E2P\_Data7

Pass

Test case begin: 08/14/2024 02:24:08 PM +05:30

Test case end: 08/14/2024 02:24:10 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419407347x (0x18FFA5F3x)
Minimal measured cycle time	999.593125 ms
Maximal measured cycle time	999.593125 ms
Average cycle time	999.593125 ms
Failure ratio (in %)	0

Distribution of measured times																								
<=	980 ms	982 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms		
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
55.666668		⬇️ Cycle Time Absolute: Check Start	
Wait			
57.666668	1	Waited for 2000.000 ms	
57.666668		⬇️ Cycle Time Absolute: Check End	

## 53. E2P\_Data8

Pass

Test case begin: 08/14/2024 02:24:10 PM +05:30

Test case end: 08/14/2024 02:24:12 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute		Pass																						
<b>Statistic</b>																								
Runtime of the statistic																								
Number of samples	2																							
Message ID	419407091x (0x18FFA4F3x)																							
Minimal measured cycle time	999.977418 ms																							
Maximal measured cycle time	999.977418 ms																							
Average cycle time	999.977418 ms																							
Failure ratio (in %)	0																							
Distribution of measured times																								
<=	980 ms	982 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms		
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
57.666668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
59.666668	1	Waited for 2000.000 ms	
59.666668		⬇️ Cycle Time Absolute: Check End	

## 54. E2P\_Data9

Pass

Test case begin: 08/14/2024 02:24:12 PM +05:30

Test case end: 08/14/2024 02:24:14 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419407603x (0x18FFA6F3x)
Minimal measured cycle time	997.491248 ms
Maximal measured cycle time	997.491248 ms
Average cycle time	997.491248 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 980 ms - 982 ms	980 ms - 984 ms	982 ms - 986 ms	984 ms - 988 ms	986 ms - 990 ms	988 ms - 992 ms	990 ms - 994 ms	992 ms - 996 ms	994 ms - 998 ms	996 ms - 1000 ms	998 ms - 1002 ms	1000 ms - 1004 ms	1002 ms - 1006 ms	1004 ms - 1008 ms	1006 ms - 1010 ms	1008 ms - 1012 ms	1010 ms - 1014 ms	1012 ms - 1016 ms	1014 ms - 1018 ms	1016 ms - 1020 ms	1018 ms - 1020 ms > 1020 ms
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
59.666668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
61.666668	1	Waited for 2000.000 ms	
61.666668		⬇️ Cycle Time Absolute: Check End	

## 55. E2P\_Data10

Pass

Test case begin: 08/14/2024 02:24:14 PM +05:30

Test case end: 08/14/2024 02:24:16 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic									
Runtime of the statistic		2000 ms							
Number of samples		2							
Message ID		419406579x (0x18FFA2F3x)							
Minimal measured cycle time		999.246584 ms							
Maximal measured cycle time		999.246584 ms							
Average cycle time		999.246584 ms							
Failure ratio (in %)		0							

Distribution of measured times																									
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms				
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
61.666668		⬇️ Cycle Time Absolute: Check Start	
Wait			
63.666668	1	Waited for 2000.000 ms	
63.666668		⬇️ Cycle Time Absolute: Check End	

## 56. (ICAN Ch1Tx/ICAN Ch1 Rx CAN\_Switch) CANSwitchProprietary

Pass

Test case begin: 08/14/2024 02:24:16 PM +05:30

Test case end: 08/14/2024 02:24:18 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute																									
Statistic										Distribution of measured times															
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms				
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	
Runtime of the statistic										2000 ms															
Number of samples										2															
Message ID										419390513x (0x18FF6431x)															
Minimal measured cycle time										1000.116725 ms															
Maximal measured cycle time										1000.116725 ms															
Average cycle time										1000.116725 ms															
Failure ratio (in %)										0															

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
63.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
65.666668	1	Waited for 2000.000 ms	
65.666668		⬇️ Cycle Time Absolute: Check End	

## 57. (PCAN CH 3TX/ TCAN CH 2 RX DCDC) DCDC\_Status1

Pass

Test case begin: 08/14/2024 02:24:18 PM +05:30

Test case end: 08/14/2024 02:24:19 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	402706575x (0x1800D08Fx)
Minimal measured cycle time	490.304764 ms
Maximal measured cycle time	490.304764 ms
Average cycle time	490.304764 ms
Failure ratio (in %)	0

Distribution of measured times																					
<=	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
65.666668		⬇️ Cycle Time Absolute: Check Start	
↳ Wait			
66.666668	1	Waited for 1000.000 ms	
66.666668		⬇️ Cycle Time Absolute: Check End	

## 58. DCDC\_Status2

Fail

Test case begin: 08/14/2024 02:24:19 PM +05:30

Test case end: 08/14/2024 02:24:20 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⬇️ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	1000 ms
Number of failures	1
Number of samples	2
Message ID	402772111x (0x1801D08Fx)
Minimal measured cycle time	485.285175 ms
Maximal measured cycle time	485.285175 ms
Average cycle time	485.285175 ms
Failure ratio (in %)	100

Distribution of measured times																								
<=	480 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms			
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
66.666668		⬇ Cycle Time Absolute: Check Start	
Wait			
67.504668	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'DCDC_Status2' ID = 402772111x (0x1801D08Fx) on bus CAN2. Time = 485.285175 ms	Fail
67.666668	1	Waited for 1000.000 ms	
67.666668		Test aborted due to BreakOnFail behavior.	
67.666668		⬇ Cycle Time Absolute: Check End	

## 59. (ICAN CH 1 TX DMS/TCAN CH 2RX) ACKM\_DMS (CY - 0) Fail

Test case begin: 08/14/2024 02:24:20 PM +05:30

Test case end: 08/14/2024 02:24:20 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute	
Statistic	
Runtime of the statistic	20 ms
Number of failures	1
Number of samples	2
Message ID	417922887x (0x18E8FF47x)
Minimal measured cycle time	10.265298 ms
Maximal measured cycle time	10.265298 ms
Average cycle time	10.265298 ms
Failure ratio (in %)	100

# Report\_Test\_Configuration\_1

Distribution of measured times																		
<= 0.1 ms	0.1 ms - 1 ms	1 ms - 1.9 ms	1.9 ms - 2.8 ms	2.8 ms - 3.7 ms	3.7 ms - 4.6 ms	4.6 ms - 5.5 ms	5.5 ms - 6.4 ms	6.4 ms - 7.3 ms	7.3 ms - 8.2 ms	8.2 ms - 9.1 ms	9.1 ms - 10 ms	10 ms - 10.9 ms	10.9 ms - 11.8 ms	11.8 ms - 12.7 ms	12.7 ms - 13.6 ms	13.6 ms - 14.5 ms	> 14.5 ms	
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	

Main Part																		
Time Stamp	Test Step			Title														
67.666668				 Cycle Time Absolute: Check Start														
Wait																		
67.681768	Condition violation			 ! Cycle Time Absolute: Maximum cycle time violation for CAN message 'ACKM_DMS' ID = 417922887x (0x18E8FF47x) on bus CAN1. Time = 10.265298 ms														
67.686668	1			Waited for 20.000 ms														
67.686668				Test aborted due to BreakOnFail behavior.														
67.686668				 Cycle Time Absolute: Check End														

## 60. CI\_DMS (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:20 PM +05:30

Test case end: 08/14/2024 02:24:20 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute																		
Statistic																		
Runtime of the statistic	20 ms																	
Number of failures	1																	
Number of samples	2																	
Message ID	419359559x (0x18FEEB47x)																	
Minimal measured cycle time	13.529893 ms																	
Maximal measured cycle time	13.529893 ms																	
Average cycle time	13.529893 ms																	
Failure ratio (in %)	100																	
Distribution of measured times																		
<= 0.1 ms	0.1 ms - 1 ms	1 ms - 1.9 ms	1.9 ms - 2.8 ms	2.8 ms - 3.7 ms	3.7 ms - 4.6 ms	4.6 ms - 5.5 ms	5.5 ms - 6.4 ms	6.4 ms - 7.3 ms	7.3 ms - 8.2 ms	8.2 ms - 9.1 ms	9.1 ms - 10 ms	10 ms - 10.9 ms	10.9 ms - 11.8 ms	11.8 ms - 12.7 ms	12.7 ms - 13.6 ms	13.6 ms - 14.5 ms	> 14.5 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	

Main Part																			
Time Stamp	Test Step			Title															Verdict
67.686668				 Cycle Time Absolute: Check Start															
Wait																			

# Report\_Test\_Configuration\_1

67.705320	Condition violation	<b>Cycle Time Absolute:</b> Maximum cycle time violation for CAN message 'CI_DMS' ID = 419359559x (0x18FEEB47x) on bus CAN1. Time = 13.529893 ms	Fail
67.706668	1	Waited for 20.000 ms	
67.706668		Test aborted due to BreakOnFail behavior.	
67.706668		<b>Cycle Time Absolute:</b> Check End	

## 61. DM1\_DMS

Pass

Test case begin: 08/14/2024 02:24:20 PM +05:30

Test case end: 08/14/2024 02:24:22 PM +05:30

Duration: 00:00:02

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419351111x (0x18FECA47x)
Minimal measured cycle time	1000.175934 ms
Maximal measured cycle time	1000.175934 ms
Average cycle time	1000.175934 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
67.706668		<b>Cycle Time Absolute:</b> Check Start	
Wait			
69.706668	1	Waited for 2000.000 ms	
69.706668		<b>Cycle Time Absolute:</b> Check End	

## 62. DM2\_DMS (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:22 PM +05:30

Test case end: 08/14/2024 02:24:22 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																			
Runtime of the statistic		200 ms																	
Number of failures		19																	
Number of samples		20																	
Message ID		419351367x (0x18FECB47x)																	
Minimal measured cycle time		5.369654 ms																	
Maximal measured cycle time		13.108626 ms																	
Average cycle time		10.238433 ms																	
Failure ratio (in %)		100																	

Distribution of measured times																					
<=	85 ms	86 ms - 87 ms	87 ms - 88 ms	88 ms - 89 ms	89 ms - 90 ms	90 ms - 91 ms	91 ms - 92 ms	92 ms - 93 ms	93 ms - 94 ms	94 ms - 95 ms	95 ms - 96 ms	96 ms - 97 ms	97 ms - 98 ms	98 ms - 99 ms	99 ms - 100 ms	100 ms - 101 ms	101 ms - 102 ms	102 ms - 103 ms	103 ms - 104 ms	104 ms - 105 ms	> 105 ms
19	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part		Test Step																		Verdict
Time Stamp		Test Step																		Verdict
69.706668		Cycle Time Absolute: Check Start																		Pass
Wait																				
69.721797	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.582194 ms																Fail
69.731839	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.04232 ms																Fail
69.742071	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.231544 ms																Fail
69.753010	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.939314 ms																Fail
69.761769	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 8.758361 ms																Fail
69.772153	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.384298 ms																Fail
69.783200	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 11.047567 ms																Fail
69.792904	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 9.703528 ms																Fail
69.806013	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 13.108626 ms																Fail
69.811382	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 5.369654 ms																Fail
69.821778	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.395626 ms																Fail
69.831233	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 9.455427 ms																Fail
69.842283	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 11.049317 ms																Fail
69.852714	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.431049 ms																Fail
69.862021	Condition violation			Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 9.307408 ms																Fail

# Report\_Test\_Configuration\_1

69.872626	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.605304 ms	Fail
69.882890	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 10.264044 ms	Fail
69.892663	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 9.77278 ms	Fail
69.905745	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM2_DMS' ID = 419351367x (0x18FECB47x) on bus CAN1. Time = 13.081875 ms	Fail
69.906668	1	Waited for 200.000 ms	
69.906668		Test aborted due to BreakOnFail behavior.	
69.906668		<b>⬇️!</b> <b>Cycle Time Absolute:</b> Check End	

## 63. DM4\_DMS (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:22 PM +05:30

Test case end: 08/14/2024 02:24:22 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️! Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	19
Number of samples	20
Message ID	419351879x (0x18FECD47x)
Minimal measured cycle time	6.355183 ms
Maximal measured cycle time	14.276909 ms
Average cycle time	10.145513 ms
Failure ratio (in %)	100

#### Distribution of measured times

<= 85 ms	85 ms - 86 ms	86 ms - 87 ms	87 ms - 88 ms	88 ms - 89 ms	89 ms - 90 ms	90 ms - 91 ms	91 ms - 92 ms	92 ms - 93 ms	93 ms - 94 ms	94 ms - 95 ms	95 ms - 96 ms	96 ms - 97 ms	97 ms - 98 ms	98 ms - 99 ms	99 ms - 100 ms	100 ms - 101 ms	101 ms - 102 ms	102 ms - 103 ms	103 ms - 104 ms	104 ms - 105 ms	> 105 ms
19	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### >Main Part

Time Stamp	Test Step	Title	Verdict
69.906668		<b>⬇️!</b> <b>Cycle Time Absolute:</b> Check Start	
<b>Wait</b>			
69.922026	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 8.729375 ms	Fail
69.932209	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.18237 ms	Fail
69.942368	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.159541 ms	Fail
69.953027	Condition violation	<b>⬇️!</b> <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.659056 ms	Fail

# Report\_Test\_Configuration\_1

69.962331	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 9.303876 ms	Fail
69.972961	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.629305 ms	Fail
69.983179	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.218043 ms	Fail
69.993090	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 9.911284 ms	Fail
70.007367	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 14.276909 ms	Fail
70.013722	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 6.355183 ms	Fail
70.022536	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 8.813752 ms	Fail
70.032630	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.09443 ms	Fail
70.042668	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.038178 ms	Fail
70.052927	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.258545 ms	Fail
70.062445	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 9.517662 ms	Fail
70.072302	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 9.857283 ms	Fail
70.082541	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.239044 ms	Fail
70.092886	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 10.345546 ms	Fail
70.106062	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4_DMS' ID = 419351879x (0x18FECD47x) on bus CAN1. Time = 13.175378 ms	Fail
70.106668	1	Waited for 200.000 ms	
70.106668		Test aborted due to BreakOnFail behavior.	
70.106668		 <b>Cycle Time Absolute:</b> Check End	

## 64. prop\_DMS

Pass

Test case begin: 08/14/2024 02:24:22 PM +05:30

Test case end: 08/14/2024 02:24:23 PM +05:30

Duration: 00:00:01

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	500 ms
Number of samples	2
Message ID	218075719x (0xCFF9247x)
Minimal measured cycle time	250.429946 ms
Maximal measured cycle time	250.429946 ms
Average cycle time	250.429946 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																						
<= 230 ms	230 ms - 232 ms	232 ms - 234 ms	234 ms - 236 ms	236 ms - 238 ms	238 ms - 240 ms	240 ms - 242 ms	242 ms - 244 ms	244 ms - 246 ms	246 ms - 248 ms	248 ms - 250 ms	250 ms - 252 ms	252 ms - 254 ms	254 ms - 256 ms	256 ms - 258 ms	258 ms - 260 ms	260 ms - 262 ms	262 ms - 264 ms	264 ms - 266 ms	266 ms - 268 ms	268 ms - 270 ms	> 270 ms	
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
70.106668		⬇️ Cycle Time Absolute: Check Start	
Wait			
70.606668	1	Waited for 500.000 ms	
70.606668		⬇️ Cycle Time Absolute: Check End	

## 65. Propreitary\_DMS\_6 (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:23 PM +05:30

Test case end: 08/14/2024 02:24:23 PM +05:30

Duration: 00:00:00

## Background Checks

### ⬇️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	9
Number of samples	10
Message ID	419375175x (0x18FF2847x)
Minimal measured cycle time	8.690186 ms
Maximal measured cycle time	12.963872 ms
Average cycle time	10.244113 ms
Failure ratio (in %)	100

### Distribution of measured times

<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms
9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
70.606668		⬇️ Cycle Time Absolute: Check Start	
Wait			
70.622653	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'Propreitary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 8.690186 ms	Fail
70.632496	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'Propreitary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 9.84222 ms	Fail

# Report\_Test\_Configuration\_1

70.642689	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 10.193292 ms	Fail
70.652830	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 10.140884 ms	Fail
70.662783	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 9.952942 ms	Fail
70.673194	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 10.411299 ms	Fail
70.683461	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 10.267044 ms	Fail
70.693196	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 9.735279 ms	Fail
70.706160	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_6' ID = 419375175x (0x18FF2847x) on bus CAN1. Time = 12.963872 ms	Fail
70.706668	1	Waited for 100.000 ms	
70.706668		Test aborted due to BreakOnFail behavior.	
70.706668		<b>Cycle Time Absolute:</b> Check End	

## 66. Proprietary\_DMS\_2 (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:23 PM +05:30

Test case end: 08/14/2024 02:24:23 PM +05:30

Duration: 00:00:00

### Background Checks

<b>Cycle Time Absolute</b>		Fail																			
<b>Statistic</b>																					
Runtime of the statistic	100 ms																				
Number of failures	9																				
Number of samples	10																				
Message ID	419403847x (0x18FF9847x)																				
Minimal measured cycle time	8.780533 ms																				
Maximal measured cycle time	13.275635 ms																				
Average cycle time	10.312627 ms																				
Failure ratio (in %)	100																				
<b>Distribution of measured times</b>																					
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms
9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

<b>Main Part</b>			
Time Stamp	Test Step	Title	Verdict
70.706668		<b>Cycle Time Absolute:</b> Check Start	
<b>Wait</b>			
70.723892	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 10.067038 ms	Fail

# Report\_Test\_Configuration\_1

70.733321	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 9.429849 ms	Fail
70.743549	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 10.227043 ms	Fail
70.755115	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 11.566456 ms	Fail
70.763896	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 8.780533 ms	Fail
70.773584	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 9.688528 ms	Fail
70.784231	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 10.647055 ms	Fail
70.793363	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 9.131514 ms	Fail
70.806638	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_DMS_2' ID = 419403847x (0x18FF9847x) on bus CAN1. Time = 13.275635 ms	Fail
70.806668	1	Waited for 100.000 ms	
70.806668		Test aborted due to BreakOnFail behavior.	
70.806668		<b>Cycle Time Absolute:</b> Check End	

## 67. Proprietary\_SW\_HW\_version (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:23 PM +05:30

Test case end: 08/14/2024 02:24:23 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	9
Number of samples	10
Message ID	419402823x (0x18FF9447x)
Minimal measured cycle time	7.200209 ms
Maximal measured cycle time	10.561307 ms
Average cycle time	9.735495 ms
Failure ratio (in %)	100

#### Distribution of measured times

<=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	66 ms	68 ms	> 70 ms
9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

### Main Part

Time Stamp	Test Step	Title	Verdict
70.806668		<b>Cycle Time Absolute:</b> Check Start	
Wait			

# Report\_Test\_Configuration\_1

70.814131	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 7.200209 ms	Fail
70.824400	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 10.269126 ms	Fail
70.833658	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 9.258128 ms	Fail
70.843847	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 10.189046 ms	Fail
70.854266	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 10.418552 ms	Fail
70.863838	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 9.572261 ms	Fail
70.874399	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 10.561307 ms	Fail
70.884669	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 10.269797 ms	Fail
70.894550	Condition violation	 <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Proprietary_SW_HW_version' ID = 419402823x (0x18FF9447x) on bus CAN1. Time = 9.881037 ms	Fail
70.906668	1	Waited for 100.000 ms	
70.906668		Test aborted due to BreakOnFail behavior.	
70.906668		 <b>Cycle Time Absolute:</b> Check End	

## 68. SOFT\_DMS (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:23 PM +05:30

Test case end: 08/14/2024 02:24:23 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	9
Number of samples	10
Message ID	419355207x (0x18FEDA47x)
Minimal measured cycle time	7.716974 ms
Maximal measured cycle time	10.577557 ms
Average cycle time	9.719464 ms
Failure ratio (in %)	100

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
70.906668		⬇️ Cycle Time Absolute: Check Start	
<b>Wait</b>			
70.914900	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 7.716974 ms	Fail
70.924613	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 9.71264 ms	Fail
70.933951	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 9.338271 ms	Fail
70.944066	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 10.115043 ms	Fail
70.954489	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 10.423303 ms	Fail
70.964086	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 9.59631 ms	Fail
70.974663	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 10.577557 ms	Fail
70.984934	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 10.271297 ms	Fail
70.994658	Condition violation	⬇️ <b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'SOFT_DMS' ID = 419355207x (0x18FEDA47x) on bus CAN1. Time = 9.723782 ms	Fail
71.006668	1	Waited for 100.000 ms	
71.006668		Test aborted due to BreakOnFail behavior.	
71.006668		⬇️ Cycle Time Absolute: Check End	

## 69. (PCAN Ch 3 TXDNR/ PCAN 3 RX ECVU1)TC1\_255

Fail

Test case begin: 08/14/2024 02:24:23 PM +05:30

Test case end: 08/14/2024 02:24:23 PM +05:30

Duration: 00:00:00

## Background Checks

### ⬇️ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																			
Runtime of the statistic		100 ms																	
Number of failures		1																	
Number of samples		2																	
Message ID		201392901x (0xC010305x)																	
Minimal measured cycle time		24.646372 ms																	
Maximal measured cycle time		24.646372 ms																	
Average cycle time		24.646372 ms																	
Failure ratio (in %)		100																	

Distribution of measured times																			
<=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	> 70 ms
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
71.006668		⬇ Cycle Time Absolute: Check Start	
Wait			
71.052238	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'TC1_255' ID = 201392901x (0xC010305x) on bus CAN3. Time = 24.646372 ms	Fail
71.106668	1	Waited for 100.000 ms	
71.106668		Test aborted due to BreakOnFail behavior.	
71.106668		⬇ Cycle Time Absolute: Check End	

## 70. (PCAN CH 3 TX ELub /PCAN CH 3 RX)ELubePumpData1

Fail

Test case begin: 08/14/2024 02:24:23 PM +05:30

Test case end: 08/14/2024 02:24:24 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute	
Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	1
Message ID	419375918x (0x18FF2B2Ex)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																								
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms			
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part		Test Log																								
Time Stamp	Test Step	Title																								Verdict
71.106668		Cycle Time Absolute: Check Start																							Pass	
Wait																										
71.171668	Condition violation at 71.166668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'ELubePumpData1' ID = 419375918x (0x18FF2B2Ex) on bus CAN3.																							Fail	
71.206668	1	Waited for 100.000 ms																							Pass	
71.206668		Test aborted due to BreakOnFail behavior.																							Pass	
71.206668		Cycle Time Absolute: Check End																								Pass

## 71. (CCAN Ch 4 TX EBS/ TCAN CH 2 RX) ACK (CY -0)

Fail

Test case begin: 08/14/2024 02:24:24 PM +05:30

Test case end: 08/14/2024 02:24:24 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Statistic	Verdict																						
Runtime of the statistic	100 ms																								
Number of failures	1																								
Message ID	417922827x (0x18E8FF0Bx)																								
Minimal measured cycle time	0 ms																								
Maximal measured cycle time	0 ms																								
Average cycle time	0 ms																								
Failure ratio (in %)	0																								
Distribution of measured times																									
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms				
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part		Test Log																								
Time Stamp	Test Step	Title																								Verdict
71.206668		Cycle Time Absolute: Check Start																							Pass	
Wait																										
71.271668	Condition violation at 71.266668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'ACK' ID = 417922827x (0x18E8FF0Bx) on bus CAN2.																							Fail	

# Report\_Test\_Configuration\_1

71.306668	1	Waited for 100.000 ms
71.306668		Test aborted due to BreakOnFail behavior.
71.306668		⬇ Cycle Time Absolute: Check End

## 72. CCVS1\_ABS

Fail

Test case begin: 08/14/2024 02:24:24 PM +05:30

Test case end: 08/14/2024 02:24:24 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	1
Message ID	419361035x (0x18FEF10Bx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
71.306668		⬇ Cycle Time Absolute: Check Start	
<b>Wait</b>			
71.421668	Condition violation at 71.416668	⬇ Cycle Time Absolute: Maximum cycle time violation for CAN message 'CCVS1_ABS' ID = 419361035x (0x18FEF10Bx) on bus CAN2.	Fail
71.506668	1	Waited for 200.000 ms	
71.506668		Test aborted due to BreakOnFail behavior.	
71.506668		⬇ Cycle Time Absolute: Check End	

## 73. CI (CY- 0)

Fail

Test case begin: 08/14/2024 02:24:24 PM +05:30

Test case end: 08/14/2024 02:24:24 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																									
Runtime of the statistic		100 ms																							
Number of failures		1																							
Message ID		419359499x (0x18FEEB0Bx)																							
Minimal measured cycle time		0 ms																							
Maximal measured cycle time		0 ms																							
Average cycle time		0 ms																							
Failure ratio (in %)		0																							
Distribution of measured times																									
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms				
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
71.5066668		⬇️ Cycle Time Absolute: Check Start	
71.571668	Condition violation at 71.566668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'CI' ID = 419359499x (0x18FEEB0Bx) on bus CAN4.	Fail
71.606668	1	Waited for 100.000 ms	
71.606668		Test aborted due to BreakOnFail behavior.	
71.606668		⬇️ Cycle Time Absolute: Check End	

## 74. CI\_EBS (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:24 PM +05:30

Test case end: 08/14/2024 02:24:24 PM +05:30

Duration: 00:00:00

## Background Checks

⬇️! Cycle Time Absolute	Fail
Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Message ID	419359742x (0x18FEEBFEx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part		Distribution of measured times																							
Time Stamp	Test Step	Title																							Verdict
71.6066668		Cycle Time Absolute: Check Start																							
Wait																									
71.6716668	Condition violation at 71.6666668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'CI_EBS' ID = 419359742x (0x18FEEBFEx) on bus CAN2.																							Fail
71.7066668	1	Waited for 100.000 ms																							
71.7066668		Test aborted due to BreakOnFail behavior.																							
71.7066668		Cycle Time Absolute: Check End																							

## 75. CVW (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:24 PM +05:30

Test case end: 08/14/2024 02:24:24 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Statistic	Fail
Runtime of the statistic	100 ms		
Number of failures	1		
Message ID	419328011x (0x18FE700Bx)		
Minimal measured cycle time	0 ms		
Maximal measured cycle time	0 ms		
Average cycle time	0 ms		
Failure ratio (in %)	0		

### Distribution of measured times

<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part		Distribution of measured times																								
Time Stamp	Test Step	Title																							Verdict	
71.7066668		Cycle Time Absolute: Check Start																								
Wait																										
71.7716668	Condition violation at 71.7666668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'CVW' ID = 419328011x (0x18FE700Bx) on bus CAN2.																							Fail	

# Report\_Test\_Configuration\_1

71.806668	1	Waited for 100.000 ms
71.806668		Test aborted due to BreakOnFail behavior.
71.806668		 <b>Cycle Time Absolute:</b> Check End

## 76. DM1

Fail

Test case begin: 08/14/2024 02:24:24 PM +05:30

Test case end: 08/14/2024 02:24:26 PM +05:30

Duration: 00:00:02

### Background Checks

#### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	2000 ms
Number of failures	1
Message ID	419351051x (0x18FECA0Bx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
71.806668		 <b>Cycle Time Absolute:</b> Check Start	
 Wait			
72.821668	Condition violation at 72.816668	 <b>Cycle Time Absolute:</b> Maximum cycle time violation for CAN message 'DM1' ID = 419351051x (0x18FECA0Bx) on bus CAN2.	Fail
73.806668	1	Waited for 2000.000 ms	
73.806668		Test aborted due to BreakOnFail behavior.	
73.806668		 <b>Cycle Time Absolute:</b> Check End	

## 77. DM4 (CY - 0)

Fail

Test case begin: 08/14/2024 02:24:26 PM +05:30

Test case end: 08/14/2024 02:24:26 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																			
Runtime of the statistic		200 ms																	
Number of failures		19																	
Number of samples		20																	
Message ID		419351819x (0x18FECD0Bx)																	
Minimal measured cycle time		5.953806 ms																	
Maximal measured cycle time		14.811553 ms																	
Average cycle time		10.229742 ms																	
Failure ratio (in %)		100																	

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
19	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part																					
Time Stamp	Test Step	Title																			Verdict
73.806668		 Cycle Time Absolute: Check Start																			
Wait																					
73.821639	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.583642 ms																		Fail
73.831553	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.913995 ms																		Fail
73.841226	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.673612 ms																		Fail
73.851613	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 10.386837 ms																		Fail
73.861251	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.637612 ms																		Fail
73.871399	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 10.147767 ms																		Fail
73.881105	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.706645 ms																		Fail
73.891611	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 10.505293 ms																		Fail
73.905385	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 13.774463 ms																		Fail
73.911339	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 5.953806 ms																		Fail
73.921227	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.887994 ms																		Fail
73.931451	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 10.224316 ms																		Fail
73.941433	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.981981 ms																		Fail
73.951559	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 10.12622 ms																		Fail
73.961197	Condition violation		 Cycle Time Absolute: Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.637628 ms																		Fail

# Report\_Test\_Configuration\_1

73.971395	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 10.1978 ms	Fail
73.981289	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 9.894619 ms	Fail
73.991609	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 10.319319 ms	Fail
74.006420	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'DM4' ID = 419351819x (0x18FECD0Bx) on bus CAN2. Time = 14.811553 ms	Fail
74.006668	1	Waited for 200.000 ms	
74.006668		Test aborted due to BreakOnFail behavior.	
74.006668		<b>Cycle Time Absolute:</b> Check End	

## 78. EBC2

Pass

Test case begin: 08/14/2024 02:24:26 PM +05:30

Test case end: 08/14/2024 02:24:27 PM +05:30

Duration: 00:00:01

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419348235x (0x18FEBF0Bx)
Minimal measured cycle time	100.427563 ms
Maximal measured cycle time	100.427563 ms
Average cycle time	100.427563 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
74.006668		<b>Cycle Time Absolute:</b> Check Start	
Wait			
74.206668	1	Waited for 200.000 ms	
74.206668		<b>Cycle Time Absolute:</b> Check End	

## 79. EBC1

Pass

Test case begin: 08/14/2024 02:24:27 PM +05:30

Test case end: 08/14/2024 02:24:27 PM +05:30

Duration: 00:00:00

### Background Checks

# Report\_Test\_Configuration\_1

## Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	418382091x (0x18F0010Bx)
Minimal measured cycle time	99.614167 ms
Maximal measured cycle time	99.614167 ms
Average cycle time	99.614167 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
74.2066668		Cycle Time Absolute: Check Start	
<b>Wait</b>			
74.4066668	1	Waited for 200.000 ms	
74.4066668		Cycle Time Absolute: Check End	

## 80. EBC3

Fail

Test case begin: 08/14/2024 02:24:27 PM +05:30

Test case end: 08/14/2024 02:24:27 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	11
Number of samples	12
Message ID	419343627x (0x18FEAD0Bx)
Minimal measured cycle time	0.556015 ms
Maximal measured cycle time	22.759306 ms
Average cycle time	16.670893 ms
Failure ratio (in %)	100

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
74.406668		⬇ Cycle Time Absolute: Check Start	
<b>Wait</b>			
74.440741	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 20.205312 ms	Fail
74.460740	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 19.999136 ms	Fail
74.481053	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 20.313566 ms	Fail
74.503813	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 22.759306 ms	Fail
74.504369	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 0.556515 ms	Fail
74.520696	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 16.32622 ms	Fail
74.540840	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 20.144639 ms	Fail
74.560866	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 20.025402 ms	Fail
74.581051	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 20.185062 ms	Fail
74.603359	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 22.308653 ms	Fail
74.603915	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC3' ID = 419343627x (0x18FEAD0Bx) on bus CAN1. Time = 0.556015 ms	Fail
74.606668	1	Waited for 200.000 ms	
74.606668		Test aborted due to BreakOnFail behavior.	
74.606668		⬇ Cycle Time Absolute: Check End	

## 81. EBC4 (CT-0)

Fail

Test case begin: 08/14/2024 02:24:27 PM +05:30

Test case end: 08/14/2024 02:24:27 PM +05:30

Duration: 00:00:00

## Background Checks

### ⬇ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	200 ms
Number of failures	1
Message ID	486452235x (0x1CFEAC0Bx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

Distribution of measured times																					
<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
74.606668		⬇️ Cycle Time Absolute: Check Start	
<b>Wait</b>			
74.721668	Condition violation at 74.716668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'EBC4' ID = 486452235x (0x1CFEAC0Bx) on bus CAN2.	Fail
74.806668	1	Waited for 200.000 ms	
74.806668		Test aborted due to BreakOnFail behavior.	
74.806668		⬇️ Cycle Time Absolute: Check End	

## 82. EBC5

Fail

Test case begin: 08/14/2024 02:24:27 PM +05:30

Test case end: 08/14/2024 02:24:27 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Fail
<b>Statistic</b>		
Runtime of the statistic		
Number of failures		
Number of samples		
Message ID		
Minimal measured cycle time		
Maximal measured cycle time		
Average cycle time		
Failure ratio (in %)		

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
74.8066668		⬇️ Cycle Time Absolute: Check Start	
<b>Wait</b>			
75.004504	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'EBC5' ID = 419283979x (0x18FDC40Bx) on bus CAN1. Time = 2.87083 ms	Fail
75.0066668	1	Waited for 200.000 ms	
75.0066668		Test aborted due to BreakOnFail behavior.	
75.0066668		⬇️ Cycle Time Absolute: Check End	

## 83. ECUID (CT -0)

Fail

Test case begin: 08/14/2024 02:24:27 PM +05:30

Test case end: 08/14/2024 02:24:28 PM +05:30

Duration: 00:00:01

## Background Checks

Cycle Time Absolute		Fail
<b>Statistic</b>		
Runtime of the statistic		
Number of failures	1	
Message ID	419284235x (0x18FDC50Bx)	
Minimal measured cycle time	0 ms	
Maximal measured cycle time	0 ms	
Average cycle time	0 ms	
Failure ratio (in %)	0	

## Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
75.0066668		⬇️ Cycle Time Absolute: Check Start	
<b>Wait</b>			
75.121668	Condition violation at 75.116668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'ECUID' ID = 419284235x (0x18FDC50Bx) on bus CAN4.	Fail

# Report\_Test\_Configuration\_1

75.206668	1	Waited for 200.000 ms
75.206668		Test aborted due to BreakOnFail behavior.
75.206668		⬇ Cycle Time Absolute: Check End

## 84. EMC1\_EM1

Fail

Test case begin: 08/14/2024 02:24:28 PM +05:30

Test case end: 08/14/2024 02:24:28 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	1
Message ID	216999691x (0xCEF270Bx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
75.206668		⬇ Cycle Time Absolute: Check Start	
<b>Wait</b>			
75.321668	Condition violation at 75.316668	⬇ Cycle Time Absolute: Maximum cycle time violation for CAN message 'EMC1_EM1' ID = 216999691x (0xCEF270Bx) on bus CAN3.	Fail
75.406668	1	Waited for 200.000 ms	
75.406668		Test aborted due to BreakOnFail behavior.	
75.406668		⬇ Cycle Time Absolute: Check End	

## 85. RQSTRequestGlobal (CT-0)

Fail

Test case begin: 08/14/2024 02:24:28 PM +05:30

Test case end: 08/14/2024 02:24:28 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																			
Runtime of the statistic		40 ms																	
Number of failures		1																	
Message ID		418054143x (0x18EAFFFFx)																	
Minimal measured cycle time		0 ms																	
Maximal measured cycle time		0 ms																	
Average cycle time		0 ms																	
Failure ratio (in %)		0																	

Distribution of measured times																					
<=	18 ms	18.2 ms	18.4 ms	18.6 ms	18.8 ms	19 ms	19.2 ms	19.4 ms	19.6 ms	19.8 ms	20 ms	20.2 ms	20.4 ms	20.6 ms	20.8 ms	21 ms	21.2 ms	21.4 ms	21.6 ms	21.8 ms	> 22 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
75.406668		⬇️ Cycle Time Absolute: Check Start	
<b>Wait</b>			
75.432668	Condition violation at 75.427668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'RQSTRequestGlobal' ID = 418054143x (0x18EAFFFFx) on bus CAN4.	Fail
75.446668	1	Waited for 40.000 ms	
75.446668		Test aborted due to BreakOnFail behavior.	
75.446668		⬇️ Cycle Time Absolute: Check End	

## 86. SAS\_CAL (CT - 0)

Fail

Test case begin: 08/14/2024 02:24:28 PM +05:30

Test case end: 08/14/2024 02:24:28 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Fail
<b>Statistic</b>		
Runtime of the statistic	40 ms	
Number of failures	1	
Message ID	418374667x (0x18EFE40Bx)	
Minimal measured cycle time	0 ms	
Maximal measured cycle time	0 ms	
Average cycle time	0 ms	
Failure ratio (in %)	0	

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 18 ms	18 ms - 18.2 ms	18.2 ms - 18.4 ms	18.4 ms - 18.6 ms	18.6 ms - 18.8 ms	18.8 ms - 19 ms	19 ms - 19.2 ms	19.2 ms - 19.4 ms	19.4 ms - 19.6 ms	19.6 ms - 19.8 ms	19.8 ms - 20 ms	20 ms - 20.2 ms	20.2 ms - 20.4 ms	20.4 ms - 20.6 ms	20.6 ms - 20.8 ms	20.8 ms - 21 ms	21 ms - 21.2 ms	21.2 ms - 21.4 ms	21.4 ms - 21.6 ms	21.6 ms - 21.8 ms	21.8 ms - 22 ms	> 22 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																																						
Time Stamp	Test Step	Title																		Verdict																		
75.4466668	Cycle Time Absolute: Check Start																																					
Wait																																						
75.4726668	Condition violation at 75.4676668  Cycle Time Absolute: Maximum cycle time violation for CAN message 'SAS_CAL' ID = 418374667x (0x18EFE40Bx) on bus CAN2.																			Fail																		
75.4866668	1 Waited for 40.000 ms																																					
75.4866668	Test aborted due to BreakOnFail behavior.																																					
75.4866668	Cycle Time Absolute: Check End																																					

## 87. VDC1 (CY -0)

Fail

Test case begin: 08/14/2024 02:24:28 PM +05:30

Test case end: 08/14/2024 02:24:28 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Fail																			
Statistic																					
Runtime of the statistic		40 ms																			
Number of failures		1																			
Message ID		419319563x (0x18FE4F0Bx)																			
Minimal measured cycle time		0 ms																			
Maximal measured cycle time		0 ms																			
Average cycle time		0 ms																			
Failure ratio (in %)		0																			
Distribution of measured times																					
<= 18 ms	18 ms - 18.2 ms	18.2 ms - 18.4 ms	18.4 ms - 18.6 ms	18.6 ms - 18.8 ms	18.8 ms - 19 ms	19 ms - 19.2 ms	19.2 ms - 19.4 ms	19.4 ms - 19.6 ms	19.6 ms - 19.8 ms	19.8 ms - 20 ms	20 ms - 20.2 ms	20.2 ms - 20.4 ms	20.4 ms - 20.6 ms	20.6 ms - 20.8 ms	20.8 ms - 21 ms	21 ms - 21.2 ms	21.2 ms - 21.4 ms	21.4 ms - 21.6 ms	21.6 ms - 21.8 ms	21.8 ms - 22 ms	> 22 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																																						
Time Stamp	Test Step	Title																		Verdict																		
75.4866668	Cycle Time Absolute: Check Start																																					
Wait																																						
75.5126668	Condition violation at 75.5076668  Cycle Time Absolute: Maximum cycle time violation for CAN message 'VDC1' ID = 419319563x (0x18FE4F0Bx) on bus CAN2.																			Fail																		

# Report\_Test\_Configuration\_1

75.526668	1	Waited for 40.000 ms
75.526668		Test aborted due to BreakOnFail behavior.
75.526668		 <b>Cycle Time Absolute:</b> Check End

## 88. VDC2

Pass

Test case begin: 08/14/2024 02:24:28 PM +05:30

Test case end: 08/14/2024 02:24:28 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	20 ms
Number of samples	2
Message ID	418384139x (0x18F0090Bx)
Minimal measured cycle time	9.945994 ms
Maximal measured cycle time	9.945994 ms
Average cycle time	9.945994 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
75.526668		 <b>Cycle Time Absolute:</b> Check Start	
75.546668	1	Waited for 20.000 ms	
75.546668		 <b>Cycle Time Absolute:</b> Check End	

## 89. VDHR

Fail

Test case begin: 08/14/2024 02:24:28 PM +05:30

Test case end: 08/14/2024 02:24:30 PM +05:30

Duration: 00:00:02

### Background Checks

#### Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	2000 ms
Number of failures	1
Message ID	419348974x (0x18FEC1EEEx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

Distribution of measured times																								
<=	980 ms	982 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms		
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
75.5466668		⬇️ Cycle Time Absolute: Check Start	
<b>Wait</b>			
76.5616668	Condition violation at 76.5566668	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'VDHR' ID = 419348974x (0x18FEC1EEEx) on bus CAN2.	Fail
77.5466668	1	Waited for 2000.000 ms	
77.5466668		Test aborted due to BreakOnFail behavior.	
77.5466668		⬇️ Cycle Time Absolute: Check End	

## 90. (PCAN Ch 3 Tx/ TCAN Ch 2 Rx EVCU1)AMB

Fail

Test case begin: 08/14/2024 02:24:30 PM +05:30

Test case end: 08/14/2024 02:24:30 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Fail
<b>Statistic</b>		
Runtime of the statistic		
Number of failures		
Number of samples		
Message ID		
Minimal measured cycle time		
Maximal measured cycle time		
Average cycle time		
Failure ratio (in %)		

# Report\_Test\_Configuration\_1

Distribution of measured times																						
<= 230 ms	230 ms - 232 ms	232 ms - 234 ms	234 ms - 236 ms	236 ms - 238 ms	238 ms - 240 ms	240 ms - 242 ms	242 ms - 244 ms	244 ms - 246 ms	246 ms - 248 ms	248 ms - 250 ms	250 ms - 252 ms	252 ms - 254 ms	254 ms - 256 ms	256 ms - 258 ms	258 ms - 260 ms	260 ms - 262 ms	262 ms - 264 ms	264 ms - 266 ms	266 ms - 268 ms	268 ms - 270 ms	> 270 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part																						
Time Stamp		Test Step		Title																		Verdict
77.5466668				Cycle Time Absolute: Check Start																		
Wait																						
78.018410		Condition violation at 78.013410		Cycle Time Absolute: Maximum cycle time violation for CAN message 'AMB' ID = 419362087x (0x18FEF527x) on bus CAN2.		Fail																
78.0466668		1		Waited for 500.000 ms																		
78.0466668				Test aborted due to BreakOnFail behavior.																		
78.0466668				Cycle Time Absolute: Check End																		

## 91. AnalogInputs

Pass

Test case begin: 08/14/2024 02:24:30 PM +05:30

Test case end: 08/14/2024 02:24:31 PM +05:30

Duration: 00:00:01

### Background Checks

#### Cycle Time Absolute

Pass

Statistic																					
Runtime of the statistic		200 ms																			
Number of samples		2																			
Message ID		419378471x (0x18FF3527x)																			
Minimal measured cycle time		100.437368 ms																			
Maximal measured cycle time		100.437368 ms																			
Average cycle time		100.437368 ms																			
Failure ratio (in %)		0																			

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp		Test Step		Title																		Verdict
78.0466668				Cycle Time Absolute: Check Start																		
Wait																						
78.2466668		1		Waited for 200.000 ms																		
78.2466668				Cycle Time Absolute: Check End																		

# Report\_Test\_Configuration\_1

## 92. Command\_message

Fail

Test case begin: 08/14/2024 02:24:31 PM +05:30

Test case end: 08/14/2024 02:24:31 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⚠️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	2
Message ID	419388455x (0x18FF5C27x)
Minimal measured cycle time	60.119114 ms
Maximal measured cycle time	60.119114 ms
Average cycle time	60.119114 ms
Failure ratio (in %)	100

Distribution of measured times																					
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	

### >Main Part

Time Stamp	Test Step	Title	Verdict
78.246668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
78.314229	Condition violation	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'Command_message' ID = 419388455x (0x18FF5C27x) on bus CAN3. Time = 60.119114 ms	Fail
78.346668	1	Waited for 100.000 ms	
78.346668		Test aborted due to BreakOnFail behavior.	
78.346668		⬇️ Cycle Time Absolute: Check End	

## 93. DTE\_1

Fail

Test case begin: 08/14/2024 02:24:31 PM +05:30

Test case end: 08/14/2024 02:24:31 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⚠️ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	2
Message ID	419378727x (0x18FF3627x)
Minimal measured cycle time	61.165895 ms
Maximal measured cycle time	61.165895 ms
Average cycle time	61.165895 ms
Failure ratio (in %)	100

Distribution of measured times																					
<=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	66 ms	68 ms	> 70 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
78.346668		⬇ Cycle Time Absolute: Check Start	
<b>Wait</b>			
78.418734	Condition violation	⬇ Cycle Time Absolute: Maximum cycle time violation for CAN message 'DTE_1' ID = 419378727x (0x18FF3627x) on bus CAN1. Time = 61.165895 ms	Fail
78.446668	1	Waited for 100.000 ms	
78.446668		Test aborted due to BreakOnFail behavior.	
78.446668		⬇ Cycle Time Absolute: Check End	

## 94. DTE\_2

Fail

Test case begin: 08/14/2024 02:24:31 PM +05:30

Test case end: 08/14/2024 02:24:31 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Fail
<b>Statistic</b>		
Runtime of the statistic	100 ms	
Number of failures	1	
Number of samples	2	
Message ID	419379239x (0x18FF3827x)	
Minimal measured cycle time	0 ms	
Maximal measured cycle time	0 ms	
Average cycle time	0 ms	
Failure ratio (in %)	100	

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part		Distribution of measured times																							
Time Stamp	Test Step	Title																							Verdict
78.446668		Cycle Time Absolute: Check Start																							
Wait																									
78.526994	Condition violation at 78.521994	Cycle Time Absolute: Maximum cycle time violation for CAN message 'DTE_2' ID = 419379239x (0x18FF3827x) on bus CAN2.																							Fail
78.546668	1	Waited for 100.000 ms																							
78.546668		Test aborted due to BreakOnFail behavior.																							
78.546668		Cycle Time Absolute: Check End																							

## 95. EC1\_EM1

Pass

Test case begin: 08/14/2024 02:24:31 PM +05:30

Test case end: 08/14/2024 02:24:41 PM +05:30

Duration: 00:00:10

### Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		10000 ms
Number of samples		2
Message ID		419357479x (0x18FEE327x)
Minimal measured cycle time		4998.598249 ms
Maximal measured cycle time		4998.598249 ms
Average cycle time		4998.598249 ms
Failure ratio (in %)		0
Distribution of measured times		
<= 363 ms		3630 ms - 3722 ms
0 ms		3722 ms - 3814 ms
363 ms		3814 ms - 3906 ms
0 ms		3906 ms - 3998 ms
363 ms		3998 ms - 4090 ms
0 ms		4090 ms - 4182 ms
363 ms		4182 ms - 4274 ms
0 ms		4274 ms - 4366 ms
363 ms		4366 ms - 4458 ms
0 ms		4458 ms - 4550 ms
363 ms		4550 ms - 4642 ms
0 ms		4642 ms - 4734 ms
363 ms		4734 ms - 4826 ms
0 ms		4826 ms - 4918 ms
363 ms		4918 ms - 5010 ms
0 ms		5010 ms - 5102 ms
363 ms		5102 ms - 5194 ms
0 ms		5194 ms - 5286 ms
363 ms		5286 ms - 5378 ms
0 ms		5378 ms - 5470 ms
> 363 ms		> 5470 ms

Main Part		Distribution of measured times																								
Time Stamp	Test Step	Title																							Verdict	
78.546668		Cycle Time Absolute: Check Start																								
Wait																										
88.546668	1	Waited for 10000.000 ms																								
88.546668		Cycle Time Absolute: Check End																								

# Report\_Test\_Configuration\_1

## 96. EEC2

Fail

Test case begin: 08/14/2024 02:24:41 PM +05:30

Test case end: 08/14/2024 02:24:41 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⚠️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	2
Message ID	217056039x (0xCF00327x)
Minimal measured cycle time	64.950193 ms
Maximal measured cycle time	64.950193 ms
Average cycle time	64.950193 ms
Failure ratio (in %)	100

Distribution of measured times																					
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0

### >Main Part

Time Stamp	Test Step	Title	Verdict
88.5466668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
88.628727	Condition violation	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'EEC2' ID = 217056039x (0xCF00327x) on bus CAN2. Time = 64.950193 ms	Fail
88.6466668	1	Waited for 100.000 ms	
88.6466668		Test aborted due to BreakOnFail behavior.	
88.6466668		⬇️ Cycle Time Absolute: Check End	

## 97. EEC3\_EM1

Fail

Test case begin: 08/14/2024 02:24:41 PM +05:30

Test case end: 08/14/2024 02:24:42 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⚠️ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	500 ms
Number of failures	1
Number of samples	2
Message ID	419356455x (0x18FEDF27x)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	100

Distribution of measured times																					
<=	230 ms	232 ms - 234 ms	234 ms - 236 ms	236 ms - 238 ms	238 ms - 240 ms	240 ms - 242 ms	242 ms - 244 ms	244 ms - 246 ms	246 ms - 248 ms	248 ms - 250 ms	250 ms - 252 ms	252 ms - 254 ms	254 ms - 256 ms	256 ms - 258 ms	258 ms - 260 ms	260 ms - 262 ms	262 ms - 264 ms	264 ms - 266 ms	266 ms - 268 ms	268 ms - 270 ms	> 270 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
88.646668		⬇️ Cycle Time Absolute: Check Start	
Wait			
89.016911	Condition violation at 89.011911	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'EEC3_EM1' ID = 419356455x (0x18FEDF27x) on bus CAN2.	Fail
89.146668	1	Waited for 500.000 ms	
89.146668		Test aborted due to BreakOnFail behavior.	
89.146668		⬇️ Cycle Time Absolute: Check End	

## 98. EMRC1\_EM1

Fail

Test case begin: 08/14/2024 02:24:42 PM +05:30

Test case end: 08/14/2024 02:24:42 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute	
Statistic	
Runtime of the statistic	20 ms
Number of failures	2
Number of samples	2
Message ID	419373095x (0x18FF2027x)
Minimal measured cycle time	2.816913 ms
Maximal measured cycle time	2.816913 ms
Average cycle time	2.816913 ms
Failure ratio (in %)	100

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																				
Time Stamp	Test Step	Title																		Verdict
89.146668		Cycle Time Absolute: Check Start																		
Wait																				
89.162668	Condition violation at 89.157668  Cycle Time Absolute: Maximum cycle time violation for CAN message 'EMRC1_EM1' ID = 419373095x (0x18FF2027x) on bus CAN2.																			Fail
89.166183	Condition violation  Cycle Time Absolute: Minimum cycle time violation for CAN message 'EMRC1_EM1' ID = 419373095x (0x18FF2027x) on bus CAN2. Time = 2.816913 ms																			Fail
89.166668	1 Waited for 20.000 ms																			
89.166668	Test aborted due to BreakOnFail behavior.																			
89.166668	Cycle Time Absolute: Check End																			

## 99. EnergyConsumption

Pass

Test case begin: 08/14/2024 02:24:42 PM +05:30

Test case end: 08/14/2024 02:24:44 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		2000 ms
Number of samples		2
Message ID		419377959x (0x18FF3327x)
Minimal measured cycle time		997.362959 ms
Maximal measured cycle time		997.362959 ms
Average cycle time		997.362959 ms
Failure ratio (in %)		0

### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
89.166668		Cycle Time Absolute: Check Start	
Wait			

# Report\_Test\_Configuration\_1

91.166668	1	Waited for 2000.000 ms
91.166668		⬇ Cycle Time Absolute: Check End

## 100. ETC2

Pass

Test case begin: 08/14/2024 02:24:44 PM +05:30

Test case end: 08/14/2024 02:24:44 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	418383107x (0x18F00503x)
Minimal measured cycle time	100.011231 ms
Maximal measured cycle time	100.011231 ms
Average cycle time	100.011231 ms
Failure ratio (in %)	0

#### Distribution of measured times

<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### >Main Part

Time Stamp	Test Step	Title	Verdict
91.166668		⬇ Cycle Time Absolute: Check Start	
↙ Wait			
91.366668	1	Waited for 200.000 ms	
91.366668		⬇ Cycle Time Absolute: Check End	

## 101. ETC7

Pass

Test case begin: 08/14/2024 02:24:44 PM +05:30

Test case end: 08/14/2024 02:24:44 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		419318275x (0x18FE4A03x)																
Minimal measured cycle time		108.669232 ms																
Maximal measured cycle time		108.669232 ms																
Average cycle time		108.669232 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
91.366668		⬇️ Cycle Time Absolute: Check Start	
Wait			
91.566668	1	Waited for 200.000 ms	
91.566668		⬇️ Cycle Time Absolute: Check End	

## 102. EV\_Faults

Pass

Test case begin: 08/14/2024 02:24:44 PM +05:30

Test case end: 08/14/2024 02:24:46 PM +05:30

Duration: 00:00:02

## Background Checks

Cycle Time Absolute																					Pass
Statistic																					
Runtime of the statistic		2000 ms																			
Number of samples		2																			
Message ID		419380263x (0x18FF3C27x)																			
Minimal measured cycle time		999.831429 ms																			
Maximal measured cycle time		999.831429 ms																			
Average cycle time		999.831429 ms																			
Failure ratio (in %)		0																			

Distribution of measured times																					
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
91.566668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
93.566668	1	Waited for 2000.000 ms	
93.566668		⬇️ Cycle Time Absolute: Check End	

## 103. EV\_Flags\_Status

Pass

Test case begin: 08/14/2024 02:24:46 PM +05:30

Test case end: 08/14/2024 02:24:46 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	20 ms
Number of samples	2
Message ID	419380775x (0x18FF3E27x)
Minimal measured cycle time	10.036099 ms
Maximal measured cycle time	10.036099 ms
Average cycle time	10.036099 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
93.566668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
93.586668	1	Waited for 20.000 ms	
93.586668		⬇️ Cycle Time Absolute: Check End	

## 104. EVCU\_E\_Lube\_control

Pass

Test case begin: 08/14/2024 02:24:46 PM +05:30

Test case end: 08/14/2024 02:24:46 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	218049831x (0xCFF2D27x)
Minimal measured cycle time	99.67118 ms
Maximal measured cycle time	99.67118 ms
Average cycle time	99.67118 ms
Failure ratio (in %)	0

Distribution of measured times																						
<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms	
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
93.5866668		⬇️ Cycle Time Absolute: Check Start	
93.7866668	1	Waited for 200.000 ms	
93.7866668		⬇️ Cycle Time Absolute: Check End	

## 105. EVCU\_IC\_Msg\_1

Fail

Test case begin: 08/14/2024 02:24:46 PM +05:30

Test case end: 08/14/2024 02:24:47 PM +05:30

Duration: 00:00:01

## Background Checks

Cycle Time Absolute		Fail
Statistic		
Runtime of the statistic		500 ms
Number of failures		1
Number of samples		2
Message ID		419368231x (0x18FF0D27x)
Minimal measured cycle time		226.984939 ms
Maximal measured cycle time		226.984939 ms
Average cycle time		226.984939 ms
Failure ratio (in %)		100

Distribution of measured times																						
<=	230 ms	232 ms	234 ms	236 ms	238 ms	240 ms	242 ms	244 ms	246 ms	248 ms	250 ms	252 ms	254 ms	256 ms	258 ms	260 ms	262 ms	264 ms	266 ms	268 ms	> 270 ms	
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
93.786668		⬇️ Cycle Time Absolute: Check Start	
Wait			
94.253947	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'EVCU_IC_Msg_1' ID = 419368231x (0x18FF0D27x) on bus CAN2. Time = 226.984939 ms	Fail
94.286668	1	Waited for 500.000 ms	
94.286668		Test aborted due to BreakOnFail behavior.	
94.286668		⬇️ Cycle Time Absolute: Check End	

## 106. EVCU\_IC\_Msg\_2

Pass

Test case begin: 08/14/2024 02:24:47 PM +05:30

Test case end: 08/14/2024 02:24:47 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic	500 ms																				
Number of samples	2																				
Message ID	352261927x (0x14FF1727x)																				
Minimal measured cycle time	242.692668 ms																				
Maximal measured cycle time	242.692668 ms																				
Average cycle time	242.692668 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 230 ms	230 ms - 232 ms	232 ms - 234 ms	234 ms - 236 ms	236 ms - 238 ms	238 ms - 240 ms	240 ms - 242 ms	242 ms - 244 ms	244 ms - 246 ms	246 ms - 248 ms	248 ms - 250 ms	250 ms - 252 ms	252 ms - 254 ms	254 ms - 256 ms	256 ms - 258 ms	258 ms - 260 ms	260 ms - 262 ms	262 ms - 264 ms	264 ms - 266 ms	266 ms - 268 ms	268 ms - 270 ms	> 270 ms
0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
94.286668		⬇️ Cycle Time Absolute: Check Start	
Wait			
94.786668	1	Waited for 500.000 ms	
94.786668		⬇️ Cycle Time Absolute: Check End	

## 107. EVCUBCS

Pass

Test case begin: 08/14/2024 02:24:47 PM +05:30

Test case end: 08/14/2024 02:24:49 PM +05:30

Duration: 00:00:02

# Report\_Test\_Configuration\_1

## Background Checks

### ↙⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	418349607x (0x18EF8227x)
Minimal measured cycle time	999.357176 ms
Maximal measured cycle time	999.357176 ms
Average cycle time	999.357176 ms
Failure ratio (in %)	0

Distribution of measured times																									
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## ↙ Main Part

Time Stamp	Test Step	Title	Verdict
94.786668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
96.786668	1	Waited for 2000.000 ms	
96.786668		⬇️ Cycle Time Absolute: Check End	

## 108. HCU\_Command1

Fail

Test case begin: 08/14/2024 02:24:49 PM +05:30

Test case end: 08/14/2024 02:24:50 PM +05:30

Duration: 00:00:01

## Background Checks

### ↙⬇️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	1000 ms
Number of failures	1
Number of samples	2
Message ID	338493223x (0x142CFF27x)
Minimal measured cycle time	484.588148 ms
Maximal measured cycle time	484.588148 ms
Average cycle time	484.588148 ms
Failure ratio (in %)	100

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms		
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part																						
Time Stamp	Test Step	Title																			Verdict	
96.7866668		Cycle Time Absolute: Check Start																				
Wait																						
97.509376	Condition violation	! Cycle Time Absolute: Minimum cycle time violation for CAN message 'HCU_Command1' ID = 338493223x (0x142CFF27x) on bus CAN2. Time = 484.588148 ms																			Fail	
97.7866668	1	Waited for 1000.000 ms																				
97.7866668		Test aborted due to BreakOnFail behavior.																				
97.7866668		Cycle Time Absolute: Check End																				

## 109. HV\_Battery\_SOC

Pass

Test case begin: 08/14/2024 02:24:50 PM +05:30

Test case end: 08/14/2024 02:24:52 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic		2000 ms																			
Number of samples		2																			
Message ID		419208999x (0x18FC9F27x)																			
Minimal measured cycle time		999.482164 ms																			
Maximal measured cycle time		999.482164 ms																			
Average cycle time		999.482164 ms																			
Failure ratio (in %)		0																			
Distribution of measured times																					
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

Main Part																					
Time Stamp	Test Step	Title																			Verdict
97.7866668		Cycle Time Absolute: Check Start																			
Wait																					
99.7866668	1	Waited for 2000.000 ms																			
99.7866668		Cycle Time Absolute: Check End																			

# Report\_Test\_Configuration\_1

## 110. HV\_Battery\_Temp

Pass

Test case begin: 08/14/2024 02:24:52 PM +05:30

Test case end: 08/14/2024 02:24:52 PM +05:30

Duration: 00:00:00

### Background Checks

#### ↙ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419219751x (0x18FCC927x)
Minimal measured cycle time	99.770937 ms
Maximal measured cycle time	99.770937 ms
Average cycle time	99.770937 ms
Failure ratio (in %)	0

#### Distribution of measured times

<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

### >Main Part

Time Stamp	Test Step	Title	Verdict
99.786668		⬇ Cycle Time Absolute: Check Start	
99.986668	1	Waited for 200.000 ms	
99.986668		⬇ Cycle Time Absolute: Check End	

## 111. Master\_To\_Slave\_1

Fail

Test case begin: 08/14/2024 02:24:52 PM +05:30

Test case end: 08/14/2024 02:24:52 PM +05:30

Duration: 00:00:00

### Background Checks

#### ↙ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	1
Message ID	419373351x (0x18FF2127x)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

Distribution of measured times																								
<=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	66 ms	68 ms	> 70 ms			
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
99.986668		⬇️ Cycle Time Absolute: Check Start	
Wait			
100.051668	Condition violation at 100.046668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'Master_To_Slave_1' ID = 419373351x (0x18FF2127x) on bus CAN2.	Fail
100.086668	1	Waited for 100.000 ms	
100.086668		Test aborted due to BreakOnFail behavior.	
100.086668		⬇️_ Cycle Time Absolute: Check End	

## 112. Master\_To\_Slave\_2

Fail

Test case begin: 08/14/2024 02:24:52 PM +05:30

Test case end: 08/14/2024 02:24:53 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute	
Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	2
Message ID	419373863x (0x18FF2327x)
Minimal measured cycle time	31.635667 ms
Maximal measured cycle time	31.635667 ms
Average cycle time	31.635667 ms
Failure ratio (in %)	100

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part		Distribution of measured times																							
Time Stamp	Test Step	Title																							Verdict
100.086668		Cycle Time Absolute: Check Start																							
Wait																									
100.159377	Condition violation	Cycle Time Absolute: Minimum cycle time violation for CAN message 'Master_To_Slave_2' ID = 419373863x (0x18FF2327x) on bus CAN2. Time = 31.635667 ms																							Fail
100.186668	1	Waited for 100.000 ms																							
100.186668		Test aborted due to BreakOnFail behavior.																							
100.186668		Cycle Time Absolute: Check End																							

## 113. Master\_To\_Slave\_4

Fail

Test case begin: 08/14/2024 02:24:53 PM +05:30

Test case end: 08/14/2024 02:24:53 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Fail																					
Statistic																							
Runtime of the statistic		100 ms																					
Number of failures		1																					
Number of samples		2																					
Message ID		419374887x (0x18FF2727x)																					
Minimal measured cycle time		34.478606 ms																					
Maximal measured cycle time		34.478606 ms																					
Average cycle time		34.478606 ms																					
Failure ratio (in %)		100																					
Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part		Distribution of measured times																							
Time Stamp	Test Step	Title																							Verdict
100.186668		Cycle Time Absolute: Check Start																							
Wait																									

# Report\_Test\_Configuration\_1

100.262844	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Master_To_Slave_4' ID = 419374887x (0x18FF2727x) on bus CAN2. Time = 34.478606 ms	Fail
100.286668	1	Waited for 100.000 ms	
100.286668		Test aborted due to BreakOnFail behavior.	
100.286668		<b>Cycle Time Absolute:</b> Check End	

## 114. Master\_To\_Slave\_3

Fail

Test case begin: 08/14/2024 02:24:53 PM +05:30

Test case end: 08/14/2024 02:24:53 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	2
Message ID	419374375x (0x18FF2527x)
Minimal measured cycle time	31.935602 ms
Maximal measured cycle time	31.935602 ms
Average cycle time	31.935602 ms
Failure ratio (in %)	100

#### Distribution of measured times

<=	30 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
100.286668		<b>Cycle Time Absolute:</b> Check Start	
Wait			
100.360242	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Master_To_Slave_3' ID = 419374375x (0x18FF2527x) on bus CAN2. Time = 31.935602 ms	Fail
100.386668	1	Waited for 100.000 ms	
100.386668		Test aborted due to BreakOnFail behavior.	
100.386668		<b>Cycle Time Absolute:</b> Check End	

## 115. Master\_To\_Slave\_Wk\_Up

Fail

Test case begin: 08/14/2024 02:24:53 PM +05:30

Test case end: 08/14/2024 02:24:53 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																			
Runtime of the statistic		100 ms																	
Number of failures		1																	
Number of samples		2																	
Message ID		419375655x (0x18FF2A27x)																	
Minimal measured cycle time		31.293667 ms																	
Maximal measured cycle time		31.293667 ms																	
Average cycle time		31.293667 ms																	
Failure ratio (in %)		100																	

Distribution of measured times																			
<=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	> 70 ms
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
100.386668		⬇ Cycle Time Absolute: Check Start	
Wait			
100.460445	Condition violation	⬇ Cycle Time Absolute: Minimum cycle time violation for CAN message 'Master_To_Slave_Wk_Up' ID = 419375655x (0x18FF2A27x) on bus CAN2. Time = 31.293667 ms	Fail
100.486668	1	Waited for 100.000 ms	
100.486668		Test aborted due to BreakOnFail behavior.	
100.486668		⬇ Cycle Time Absolute: Check End	

## 116. Prop\_VCU\_Immobilizer (CT 0)

Fail

Test case begin: 08/14/2024 02:24:53 PM +05:30

Test case end: 08/14/2024 02:24:53 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute	
Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Message ID	417071143x (0x18DC0027x)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
100.486668		⬇️ Cycle Time Absolute: Check Start	
Wait			
100.551668	Condition violation at 100.546668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'Prop_VCU_Immobilizer' ID = 417071143x (0x18DC0027x) on bus CAN3.	Fail
100.586668	1	Waited for 100.000 ms	
100.586668		Test aborted due to BreakOnFail behavior.	
100.586668		⬇️_ Cycle Time Absolute: Check End	

## 117. Pump\_command

Fail

Test case begin: 08/14/2024 02:24:53 PM +05:30

Test case end: 08/14/2024 02:24:54 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute		Fail																				
Statistic																						
Runtime of the statistic		1000 ms																				
Number of failures		1																				
Number of samples		2																				
Message ID		418353191x (0x18EF9027x)																				
Minimal measured cycle time		485.349648 ms																				
Maximal measured cycle time		485.349648 ms																				
Average cycle time		485.349648 ms																				
Failure ratio (in %)		100																				
Distribution of measured times																						
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms	
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
100.586668		⬇️ Cycle Time Absolute: Check Start	
Wait			

# Report\_Test\_Configuration\_1

101.504423	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Pump_command' ID = 418353191x (0x18EF9027x) on bus CAN2. Time = 485.349648 ms	Fail
101.586668	1	Waited for 1000.000 ms	
101.586668		Test aborted due to BreakOnFail behavior.	
101.586668		<b>Cycle Time Absolute:</b> Check End	

## 118. pumpControl\_H\_P1

Pass

Test case begin: 08/14/2024 02:24:54 PM +05:30

Test case end: 08/14/2024 02:24:56 PM +05:30

Duration: 00:00:02

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	418359550x (0x18EFA8FEx)
Minimal measured cycle time	999.719461 ms
Maximal measured cycle time	999.719461 ms
Average cycle time	999.719461 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
101.586668		<b>Cycle Time Absolute:</b> Check Start	
Wait			
103.586668	1	Waited for 2000.000 ms	
103.586668		<b>Cycle Time Absolute:</b> Check End	

## 119. RC\_EM1

Pass

Test case begin: 08/14/2024 02:24:56 PM +05:30

Test case end: 08/14/2024 02:25:06 PM +05:30

Duration: 00:00:10

### Background Checks

#### Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419356967x (0x18FEE127x)
Minimal measured cycle time	4999.447405 ms
Maximal measured cycle time	4999.447405 ms
Average cycle time	4999.447405 ms
Failure ratio (in %)	0

Distribution of measured times																								
<=	3630 ms	3722 ms	3814 ms	3906 ms	3998 ms	4090 ms	4182 ms	4274 ms	4366 ms	4458 ms	4550 ms	4642 ms	4734 ms	4826 ms	4918 ms	5010 ms	5102 ms	5194 ms	5286 ms	5378 ms	> 5470 ms			
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
103.5866668		⬇️ Cycle Time Absolute: Check Start	
Wait			
113.5866668	1	Waited for 10000.000 ms	
113.5866668		⬇️ Cycle Time Absolute: Check End	

## 120. System\_Message (RX is NOT defined DB)

Pass

Test case begin: 08/14/2024 02:25:06 PM +05:30

Test case end: 08/14/2024 02:25:06 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		200 ms
Number of samples		1
Message ID		1633 (0x661)
Minimal measured cycle time		0 ms
Maximal measured cycle time		0 ms
Average cycle time		0 ms
Failure ratio (in %)		0

Distribution of measured times																								
<=	180 ms	182 ms	184 ms	186 ms	188 ms	190 ms	192 ms	194 ms	196 ms	198 ms	200 ms	202 ms	204 ms	206 ms	208 ms	210 ms	212 ms	214 ms	216 ms	218 ms	> 220 ms			
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			Verdict
Time Stamp	Test Step	Title	
113.586668		⬇️ Cycle Time Absolute: Check Start	
Wait			
113.786668	1	Waited for 200.000 ms	
113.786668		⬇️ Cycle Time Absolute: Check End	

## 121. TP\_CM (RX is NOT defined in DB)

Pass

Test case begin: 08/14/2024 02:25:06 PM +05:30

Test case end: 08/14/2024 02:25:16 PM +05:30

Duration: 00:00:10

### Background Checks

⬇️ Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic 10000 ms																					
Number of samples 2																					
Message ID 418184999x (0x18ECFF27x)																					
Minimal measured cycle time 4999.900889 ms																					
Maximal measured cycle time 4999.900889 ms																					
Average cycle time 4999.900889 ms																					
Failure ratio (in %) 0																					
Distribution of measured times																					
<= 3630 ms - 0 ms	3630 ms - 3722 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 5470 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0

Main Part			Verdict
Time Stamp	Test Step	Title	
113.786668		⬇️ Cycle Time Absolute: Check Start	
Wait			
123.786668	1	Waited for 10000.000 ms	
123.786668		⬇️ Cycle Time Absolute: Check End	

## 122. TP\_DT

Pass

Test case begin: 08/14/2024 02:25:16 PM +05:30

Test case end: 08/14/2024 02:25:26 PM +05:30

Duration: 00:00:10

### Background Checks

⬇️ Cycle Time Absolute		Pass
Background Checks		

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	418119463x (0x18EBFF27x)
Minimal measured cycle time	5000.213999 ms
Maximal measured cycle time	5000.213999 ms
Average cycle time	5000.213999 ms
Failure ratio (in %)	0

Distribution of measured times																									
<=	3630 ms - 0 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 5470 ms				
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
123.786668		⬇️ Cycle Time Absolute: Check Start	
Wait			
133.786668	1	Waited for 10000.000 ms	
133.786668		⬇️ Cycle Time Absolute: Check End	

## 123. Transmissio\_Error

Pass

Test case begin: 08/14/2024 02:25:26 PM +05:30

Test case end: 08/14/2024 02:25:27 PM +05:30

Duration: 00:00:01

## Background Checks

⬇️ Cycle Time Absolute	Pass
------------------------	------

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	419405209x (0x18FF9D99x)
Minimal measured cycle time	490.412579 ms
Maximal measured cycle time	490.412579 ms
Average cycle time	490.412579 ms
Failure ratio (in %)	0

Distribution of measured times																									
<=	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms				
0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
133.786668		⬇️ Cycle Time Absolute: Check Start	
Wait			
134.786668	1	Waited for 1000.000 ms	
134.786668		⬇️ Cycle Time Absolute: Check End	

## 124. Transmission\_Control

Pass

Test case begin: 08/14/2024 02:25:27 PM +05:30

Test case end: 08/14/2024 02:25:27 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic	20 ms																				
Number of samples	1																				
Message ID	218043545x (0xCFF1499x)																				
Minimal measured cycle time	0 ms																				
Maximal measured cycle time	0 ms																				
Average cycle time	0 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
134.786668		⬇️ Cycle Time Absolute: Check Start	
Wait			
134.806668	1	Waited for 20.000 ms	
134.806668		⬇️ Cycle Time Absolute: Check End	

## 125. UI1

Pass

Test case begin: 08/14/2024 02:25:27 PM +05:30

Test case end: 08/14/2024 02:25:27 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Pass
⬇️ Cycle Time Absolute		

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	424247335x (0x19498027x)
Minimal measured cycle time	100.272795 ms
Maximal measured cycle time	100.272795 ms
Average cycle time	100.272795 ms
Failure ratio (in %)	0

Distribution of measured times																										
<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms					
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
134.8066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
135.0066668	1	Waited for 200.000 ms	
135.0066668		⬇️ Cycle Time Absolute: Check End	

## 126. UI2

Fail

Test case begin: 08/14/2024 02:25:27 PM +05:30

Test case end: 08/14/2024 02:25:28 PM +05:30

Duration: 00:00:01

## Background Checks

Cycle Time Absolute		Fail
<b>Statistic</b>		
Runtime of the statistic		
200 ms		
Number of failures		
1		
Number of samples		
2		
Message ID		
424247591x (0x19498127x)		
Minimal measured cycle time		
75.937965 ms		
Maximal measured cycle time		
75.937965 ms		
Average cycle time		
75.937965 ms		
Failure ratio (in %)		
100		

Distribution of measured times																										
<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms					
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
135.006668		⬇️ Cycle Time Absolute: Check Start	
Wait			
135.118659	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'UI2' ID = 424247591x (0x19498127x) on bus CAN2. Time = 75.937965 ms	Fail
135.206668	1	Waited for 200.000 ms	
135.206668		Test aborted due to BreakOnFail behavior.	
135.206668		⬇️ Cycle Time Absolute: Check End	

## 127. VCU\_2PCM\_MSG1

Fail

Test case begin: 08/14/2024 02:25:28 PM +05:30

Test case end: 08/14/2024 02:25:28 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Fail																			
Statistic																					
Runtime of the statistic																					
Runtime of the statistic	400 ms																				
Number of failures	3																				
Number of samples	4																				
Message ID	418325031x (0x18EF2227x)																				
Minimal measured cycle time	99.711184 ms																				
Maximal measured cycle time	104.728176 ms																				
Average cycle time	101.608912 ms																				
Failure ratio (in %)	100																				
Distribution of measured times																					
<= 180 ms	180 ms - 182 ms	182 ms - 184 ms	184 ms - 186 ms	186 ms - 188 ms	188 ms - 190 ms	190 ms - 192 ms	192 ms - 194 ms	194 ms - 196 ms	196 ms - 198 ms	198 ms - 200 ms	200 ms - 202 ms	202 ms - 204 ms	204 ms - 206 ms	206 ms - 208 ms	208 ms - 210 ms	210 ms - 212 ms	212 ms - 214 ms	214 ms - 216 ms	216 ms - 218 ms	218 ms - 220 ms	> 220 ms
3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
135.206668		⬇️ Cycle Time Absolute: Check Start	
Wait			
135.312128	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'VCU_2PCM_MSG1' ID = 418325031x (0x18EF2227x) on bus CAN4. Time = 99.711184 ms	Fail
135.412515	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'VCU_2PCM_MSG1' ID = 418325031x (0x18EF2227x) on bus CAN4. Time = 100.387376 ms	Fail
135.517244	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'VCU_2PCM_MSG1' ID = 418325031x (0x18EF2227x) on bus CAN4. Time = 104.728176 ms	Fail
135.606668	1	Waited for 400.000 ms	
135.606668		Test aborted due to BreakOnFail behavior.	

# Report\_Test\_Configuration\_1

135.606668

 Cycle Time Absolute: Check End

## 128. VCUREquest

Fail

Test case begin: 08/14/2024 02:25:28 PM +05:30

Test case end: 08/14/2024 02:25:28 PM +05:30

Duration: 00:00:00

### Background Checks

 Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Number of samples	2
Message ID	83853351x (0x4FF8027x)
Minimal measured cycle time	31.587888 ms
Maximal measured cycle time	31.587888 ms
Average cycle time	31.587888 ms
Failure ratio (in %)	100

### Distribution of measured times

<=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	66 ms	68 ms	> 70 ms
=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	66 ms	68 ms	> 70 ms
0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

### Main Part

Time Stamp	Test Step	Title	Verdict
135.606668		 Cycle Time Absolute: Check Start	
 Wait			
135.660680	Condition violation	 Cycle Time Absolute: Minimum cycle time violation for CAN message 'VCUREquest' ID = 83853351x (0x4FF8027x) on bus CAN2. Time = 31.587888 ms	Fail
135.706668	1	Waited for 100.000 ms	
135.706668		Test aborted due to BreakOnFail behavior.	
135.706668		 Cycle Time Absolute: Check End	

## 129. VEP1

Fail

Test case begin: 08/14/2024 02:25:28 PM +05:30

Test case end: 08/14/2024 02:25:29 PM +05:30

Duration: 00:00:01

### Background Checks

 Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	500 ms
Number of failures	1
Number of samples	2
Message ID	419362599x (0x18FEF727x)
Minimal measured cycle time	262.193511 ms
Maximal measured cycle time	262.193511 ms
Average cycle time	262.193511 ms
Failure ratio (in %)	100

Distribution of measured times																						
<=	230 ms	232 ms - 232 ms	232 ms - 234 ms	234 ms - 236 ms	236 ms - 238 ms	238 ms - 240 ms	240 ms - 242 ms	242 ms - 244 ms	244 ms - 246 ms	246 ms - 248 ms	248 ms - 250 ms	250 ms - 252 ms	252 ms - 254 ms	254 ms - 256 ms	256 ms - 258 ms	258 ms - 260 ms	260 ms - 262 ms	262 ms - 264 ms	264 ms - 266 ms	266 ms - 268 ms	268 ms - 270 ms	> 270 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0

Main Part																						
Time Stamp	Test Step	Title																			Verdict	
135.706668		 <b>Cycle Time Absolute:</b> Check Start																				
Wait																						
136.016013	Condition violation	 <b>Cycle Time Absolute:</b> Maximum cycle time violation for CAN message 'VEP1' ID = 419362599x (0x18FEF727x) on bus CAN1. Time = 262.193511 ms																				<span style="background-color: red; color: white;">Fail</span>
136.206668	1	Waited for 500.000 ms																				
136.206668		Test aborted due to BreakOnFail behavior.																				
136.206668		 <b>Cycle Time Absolute:</b> Check End																				

## 130. VmuCommand

Fail

Test case begin: 08/14/2024 02:25:29 PM +05:30

Test case end: 08/14/2024 02:25:29 PM +05:30

Duration: 00:00:00

### Background Checks

 Cycle Time Absolute		Fail
Statistic		
Runtime of the statistic		20 ms
Number of failures		1
Message ID		82833191x (0x4EFEF27x)
Minimal measured cycle time		0 ms
Maximal measured cycle time		0 ms
Average cycle time		0 ms
Failure ratio (in %)		0

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																				
Time Stamp	Test Step	Title																		Verdict
136.206668		Cycle Time Absolute: Check Start																		
Wait																				
136.222668	Condition violation at 136.217668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'VmCommand' ID = 82833191x (0x4EFEF27x) on bus CAN2.																		Fail
136.222668	1	Waited for 20.000 ms																		
136.222668		Test aborted due to BreakOnFail behavior.																		
136.222668		Cycle Time Absolute: Check End																		

## 131. VmulInfo1 (CT - 0)

Fail

Test case begin: 08/14/2024 02:25:29 PM +05:30

Test case end: 08/14/2024 02:25:29 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Fail
Statistic		
Runtime of the statistic		20 ms
Number of failures		1
Message ID		419364903x (0x18FF0027x)
Minimal measured cycle time		0 ms
Maximal measured cycle time		0 ms
Average cycle time		0 ms
Failure ratio (in %)		0

### Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																				
Time Stamp	Test Step	Title																		Verdict
136.222668		Cycle Time Absolute: Check Start																		
Wait																				
136.242668	Condition violation at 136.237668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'VmCommand' ID = 82833191x (0x4EFEF27x) on bus CAN2.																		Fail

# Report\_Test\_Configuration\_1

136.246668	1	Waited for 20.000 ms
136.246668		Test aborted due to BreakOnFail behavior.
136.246668		⬇ Cycle Time Absolute: Check End

## 132. (PCAN CH 3 TX Gradient Sens/TCAN Ch 2 RX) Gradient\_Sensor\_Angle

Fail

Test case begin: 08/14/2024 02:25:29 PM +05:30

Test case end: 08/14/2024 02:25:29 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	20 ms
Number of failures	1
Number of samples	2
Message ID	218042018x (0xCFF0EA2x)
Minimal measured cycle time	1.967806 ms
Maximal measured cycle time	1.967806 ms
Average cycle time	1.967806 ms
Failure ratio (in %)	100

#### Distribution of measured times

<=	8 ms	8.2 ms	8.4 ms	8.6 ms	8.8 ms	9 ms	9.2 ms	9.4 ms	9.6 ms	9.8 ms	10 ms	10.2 ms	10.4 ms	10.6 ms	10.8 ms	11 ms	11.2 ms	11.4 ms	11.6 ms	11.8 ms	> 12 ms
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
136.246668		⬇ Cycle Time Absolute: Check Start	
<b>Wait</b>			
136.261391	Condition violation	⬇ ! Cycle Time Absolute: Minimum cycle time violation for CAN message 'Gradient_Sensor_Angle' ID = 218042018x (0xCFF0EA2x) on bus CAN3. Time = 1.967806 ms	Fail
136.266668	1	Waited for 20.000 ms	
136.266668		Test aborted due to BreakOnFail behavior.	
136.266668		⬇ Cycle Time Absolute: Check End	

## 133. Gradient\_SensorError

Fail

Test case begin: 08/14/2024 02:25:29 PM +05:30

Test case end: 08/14/2024 02:25:29 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇ Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																									
Runtime of the statistic		100 ms																							
Number of failures		1																							
Message ID		419422370x (0x18FFE0A2x)																							
Minimal measured cycle time		0 ms																							
Maximal measured cycle time		0 ms																							
Average cycle time		0 ms																							
Failure ratio (in %)		0																							
Distribution of measured times																									
<=	30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	66 ms	68 ms	70 ms	>	70 ms		
30 ms	32 ms	34 ms	36 ms	38 ms	40 ms	42 ms	44 ms	46 ms	48 ms	50 ms	52 ms	54 ms	56 ms	58 ms	60 ms	62 ms	64 ms	66 ms	68 ms	70 ms					
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
136.266668		⬇️ Cycle Time Absolute: Check Start	
Wait			
136.331668	Condition violation at 136.326668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'Gradient_SensorError' ID = 419422370x (0x18FFE0A2x) on bus CAN3.	Fail
136.366668	1	Waited for 100.000 ms	
136.366668		Test aborted due to BreakOnFail behavior.	
136.366668		⬇️ Cycle Time Absolute: Check End	

## 134. (CCAN CH 4 TX AC Comp/ PCAN CH 3 RX AC Comp)AC\_Compressor\_Info1

Pass

Test case begin: 08/14/2024 02:25:29 PM +05:30

Test case end: 08/14/2024 02:25:29 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		200 ms
Number of samples		2
Message ID		419414297x (0x18FFC119x)
Minimal measured cycle time		104.872175 ms
Maximal measured cycle time		104.872175 ms
Average cycle time		104.872175 ms
Failure ratio (in %)		0

# Report\_Test\_Configuration\_1

Distribution of measured times																						
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms	
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

Main Part																							
Time Stamp	Test Step	Title	Verdict																				
136.366668		⬇️ Cycle Time Absolute: Check Start																				Verdict	
Wait																							
136.566668	1	Waited for 200.000 ms																				Pass	
136.566668		⬇️ Cycle Time Absolute: Check End																				Pass	

## 135. (ICAN CH 1 TX IC/TCAN CH 2 RX)AIR1

Pass

Test case begin: 08/14/2024 02:25:29 PM +05:30

Test case end: 08/14/2024 02:25:31 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute																							Pass	
Statistic																							Pass	
Runtime of the statistic	2000 ms																							
Number of samples	2																							
Message ID	419343895x (0x18FEAE17x)																							
Minimal measured cycle time	1004.318839 ms																							
Maximal measured cycle time	1004.318839 ms																							
Average cycle time	1004.318839 ms																							
Failure ratio (in %)	0																							
Distribution of measured times																								
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms			
0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0		

Main Part																							Verdict
Time Stamp	Test Step	Title	Verdict																				
136.566668		⬇️ Cycle Time Absolute: Check Start																				Pass	
Wait																							
138.566668	1	Waited for 2000.000 ms																				Pass	
138.566668		⬇️ Cycle Time Absolute: Check End																				Pass	

## 136. CCVS1\_IC

Pass

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:25:31 PM +05:30

Test case end: 08/14/2024 02:25:31 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419361047x (0x18FEF117x)
Minimal measured cycle time	104.767674 ms
Maximal measured cycle time	104.767674 ms
Average cycle time	104.767674 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
138.566668		Cycle Time Absolute: Check Start	
Wait			
138.766668	1	Waited for 200.000 ms	
138.766668		Cycle Time Absolute: Check End	

## 137. CM1

Fail

Test case begin: 08/14/2024 02:25:31 PM +05:30

Test case end: 08/14/2024 02:25:33 PM +05:30

Duration: 00:00:02

## Background Checks

### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	2000 ms
Number of failures	1
Number of samples	2
Message ID	417333271x (0x18E00017x)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	100

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
138.766668		⬇️ Cycle Time Absolute: Check Start	
Wait			
139.871583	Condition violation at 139.866583	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'CM1' ID = 417333271x (0x18E00017x) on bus CAN2.	Fail
140.766668	1	Waited for 2000.000 ms	
140.766668		Test aborted due to BreakOnFail behavior.	
140.766668		⬇️_ Cycle Time Absolute: Check End	

## 138. Prop\_IC\_FuelAND\_Maint (CT - 10000)

Fail

Test case begin: 08/14/2024 02:25:33 PM +05:30

Test case end: 08/14/2024 02:25:53 PM +05:30

Duration: 00:00:20

### Background Checks

Cycle Time Absolute		Fail
Statistic		
Runtime of the statistic		20000 ms
Number of failures		3
Number of samples		4
Message ID		419386391x (0x18FF5417x)
Minimal measured cycle time		568.342912 ms
Maximal measured cycle time		9431.908637 ms
Average cycle time		3528.421552 ms
Failure ratio (in %)		100

### Distribution of measured times

<= 998 ms	9980 ms - 9982 ms	9982 ms - 9984 ms	9984 ms - 9986 ms	9986 ms - 9988 ms	9988 ms - 9990 ms	9990 ms - 9992 ms	9992 ms - 9994 ms	9994 ms - 9996 ms	9996 ms - 9998 ms	9998 ms - 10000 ms	10000 ms - 10002 ms	10002 ms - 10004 ms	10004 ms - 10006 ms	10006 ms - 10008 ms	10008 ms - 10010 ms	10010 ms - 10012 ms	10012 ms - 10014 ms	10014 ms - 10016 ms	10016 ms - 10018 ms	10018 ms - 10020 ms	> 10020 ms
3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
140.766668		⬇️ Cycle Time Absolute: Check Start	
Wait			

# Report\_Test\_Configuration\_1

150.569726	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Prop_IC_FuelAND_Maint' ID = 419386391x (0x18FF5417x) on bus CAN2. Time = 568.342912 ms	Fail
160.001634	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Prop_IC_FuelAND_Maint' ID = 419386391x (0x18FF5417x) on bus CAN2. Time = 9431.908637 ms	Fail
160.586647	Condition violation	<b>Cycle Time Absolute:</b> Minimum cycle time violation for CAN message 'Prop_IC_FuelAND_Maint' ID = 419386391x (0x18FF5417x) on bus CAN2. Time = 585.013107 ms	Fail
160.766668	1	Waited for 20000.000 ms	
160.766668		Test aborted due to BreakOnFail behavior.	
160.766668		<b>Cycle Time Absolute:</b> Check End	

## 139. Prop\_IC\_RunInfo

Pass

Test case begin: 08/14/2024 02:25:53 PM +05:30

Test case end: 08/14/2024 02:25:53 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419385623x (0x18FF5117x)
Minimal measured cycle time	100.445474 ms
Maximal measured cycle time	100.445474 ms
Average cycle time	100.445474 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
160.766668		<b>Cycle Time Absolute:</b> Check Start	
Wait			
160.966668	1	Waited for 200.000 ms	
160.966668		<b>Cycle Time Absolute:</b> Check End	

## 140. Prop\_IC\_TripA (CT -10000)

Fail

Test case begin: 08/14/2024 02:25:53 PM +05:30

Test case end: 08/14/2024 02:26:13 PM +05:30

Duration: 00:00:20

### Background Checks

#### Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	20000 ms
Number of failures	3
Number of samples	4
Message ID	419385879x (0x18FF5217x)
Minimal measured cycle time	485.546449 ms
Maximal measured cycle time	9513.898509 ms
Average cycle time	3498.894859 ms
Failure ratio (in %)	100

Distribution of measured times																									
<= 9980 ms	9980 ms - 9982 ms	9982 ms - 9984 ms	9984 ms - 9986 ms	9986 ms - 9988 ms	9988 ms - 9990 ms	9990 ms - 9992 ms	9992 ms - 9994 ms	9994 ms - 9996 ms	9996 ms - 9998 ms	9998 ms - 10000 ms	10000 ms - 10002 ms	10002 ms - 10004 ms	10004 ms - 10006 ms	10006 ms - 10008 ms	10008 ms - 10010 ms	10010 ms - 10012 ms	10012 ms - 10014 ms	10014 ms - 10016 ms	10016 ms - 10018 ms	10018 ms - 10020 ms	> 10020 ms				
3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part																																																	
Time Stamp	Test Step	Title																								Verdict																							
160.966668		 Cycle Time Absolute: Check Start																																															
Wait																																																	
170.487684	 Cycle Time Absolute: Condition violation Minimum cycle time violation for CAN message 'Prop_IC_TripA' ID = 419385879x (0x18FF5217x) on bus CAN2. Time = 485.546449 ms																									Fail																							
180.001583	 Cycle Time Absolute: Condition violation Minimum cycle time violation for CAN message 'Prop_IC_TripA' ID = 419385879x (0x18FF5217x) on bus CAN2. Time = 9513.898509 ms																									Fail																							
180.498822	 Cycle Time Absolute: Condition violation Minimum cycle time violation for CAN message 'Prop_IC_TripA' ID = 419385879x (0x18FF5217x) on bus CAN2. Time = 497.23962 ms																									Fail																							
180.966668	1																																																
180.966668	Test aborted due to BreakOnFail behavior.																																																
180.966668	 Cycle Time Absolute: Check End																																																

## 141. Prop\_IC\_TripB (CT - 10000)

Fail

Test case begin: 08/14/2024 02:26:13 PM +05:30

Test case end: 08/14/2024 02:26:33 PM +05:30

Duration: 00:00:20

### Background Checks

 Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	20000 ms
Number of failures	3
Number of samples	4
Message ID	419386135x (0x18FF5317x)
Minimal measured cycle time	622.06747 ms
Maximal measured cycle time	9377.889904 ms
Average cycle time	3547.130092 ms
Failure ratio (in %)	100

Distribution of measured times																									
<= 9980 ms	9980 ms - 9982 ms	9982 ms - 9984 ms	9984 ms - 9986 ms	9986 ms - 9988 ms	9988 ms - 9990 ms	9990 ms - 9992 ms	9992 ms - 9994 ms	9994 ms - 9996 ms	9996 ms - 9998 ms	9998 ms - 10000 ms	10000 ms - 10002 ms	10002 ms - 10004 ms	10004 ms - 10006 ms	10006 ms - 10008 ms	10008 ms - 10010 ms	10010 ms - 10012 ms	10012 ms - 10014 ms	10014 ms - 10016 ms	10016 ms - 10018 ms	10018 ms - 10020 ms	> 10020 ms				
3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
180.966668		⬇️ Cycle Time Absolute: Check Start	
Wait			
190.624439	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'Prop_IC_TripB' ID = 419386135x (0x18FF5317x) on bus CAN2. Time = 622.06747 ms	Fail
200.002329	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'Prop_IC_TripB' ID = 419386135x (0x18FF5317x) on bus CAN2. Time = 9377.889904 ms	Fail
200.643762	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'Prop_IC_TripB' ID = 419386135x (0x18FF5317x) on bus CAN2. Time = 641.432904 ms	Fail
200.966668	1	Waited for 20000.000 ms	
200.966668		Test aborted due to BreakOnFail behavior.	
200.966668		⬇️ Cycle Time Absolute: Check End	

## 142. prop\_TPMS\_features\_rqst\_Copy\_1

Pass

Test case begin: 08/14/2024 02:26:33 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:01

### Background Checks

⬇️! Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		419392279x (0x18FF6B17x)																
Minimal measured cycle time		98.767675 ms																
Maximal measured cycle time		98.767675 ms																
Average cycle time		98.767675 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
200.966668		⬇️ Cycle Time Absolute: Check Start	
Wait			
201.166668	1	Waited for 200.000 ms	
201.166668		⬇️ Cycle Time Absolute: Check End	

## 143. RESET (CT - 0 )

Pass

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																					Pass
Statistic																					
Runtime of the statistic		20 ms																			
Number of samples		2																			
Message ID		484311063x (0x1CDE0017x)																			
Minimal measured cycle time		10.539805 ms																			
Maximal measured cycle time		10.539805 ms																			
Average cycle time		10.539805 ms																			
Failure ratio (in %)		0																			

Distribution of measured times																					
<=	8 ms	8.2 ms	8.4 ms	8.6 ms	8.8 ms	9 ms	9.2 ms	9.4 ms	9.6 ms	9.8 ms	10 ms	10.2 ms	10.4 ms	10.6 ms	10.8 ms	11 ms	11.2 ms	11.4 ms	11.6 ms	11.8 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
201.166668		⬇️ Cycle Time Absolute: Check Start	
Wait			
201.186668	1	Waited for 20.000 ms	
201.186668		⬇️ Cycle Time Absolute: Check End	

## 144. (PCAN CH 3 Tx MCU/ TCAN RX) TXMcuFastCommandResponse

Pass

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

### Background Checks

⬇️ Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic	20 ms																				
Number of samples	1																				
Message ID	419365103x (0x18FF00EFx)																				
Minimal measured cycle time	0 ms																				
Maximal measured cycle time	0 ms																				
Average cycle time	0 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
201.186668		⬇️ Cycle Time Absolute: Check Start	
Wait			
201.206668	1	Waited for 20.000 ms	
201.206668		⬇️ Cycle Time Absolute: Check End	

## 145. McuInfo1

Pass

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

### Background Checks

⬇️ Cycle Time Absolute		Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		419365615x (0x18FF02EFx)																
Minimal measured cycle time		98.179079 ms																
Maximal measured cycle time		98.179079 ms																
Average cycle time		98.179079 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
201.2066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
201.4066668	1	Waited for 200.000 ms	
201.4066668		⬇️ Cycle Time Absolute: Check End	

## 146. McuInfo2

Fail

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

## Background Checks

⬇️ Cycle Time Absolute	Fail
------------------------	------

Statistic																		
Runtime of the statistic		20 ms																
Number of failures		1																
Message ID		419365871x (0x18FF03EFx)																
Minimal measured cycle time		0 ms																
Maximal measured cycle time		0 ms																
Average cycle time		0 ms																
Failure ratio (in %)		0																

## Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
201.406668		⬇️ Cycle Time Absolute: Check Start	
Wait			
201.422668	Condition violation at 201.417668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuInfo2' ID = 419365871x (0x18FF03EFx) on bus CAN2.	Fail
201.426668	1	Waited for 20.000 ms	
201.426668		Test aborted due to BreakOnFail behavior.	
201.426668		⬇️_ Cycle Time Absolute: Check End	

## 147. McuMotorRotationInfo1 (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Fail																			
<b>Statistic</b>																					
Runtime of the statistic	20 ms																				
Number of failures	1																				
Message ID	419367919x (0x18FF0BEFx)																				
Minimal measured cycle time	0 ms																				
Maximal measured cycle time	0 ms																				
Average cycle time	0 ms																				
Failure ratio (in %)	0																				
<b>Distribution of measured times</b>																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
201.426668		⬇️ Cycle Time Absolute: Check Start	
Wait			
201.442668	Condition violation at 201.437668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuMotorRotationInfo1' ID = 419367919x (0x18FF0BEFx) on bus CAN2.	Fail
201.446668	1	Waited for 20.000 ms	
201.446668		Test aborted due to BreakOnFail behavior.	
201.446668		⬇️_ Cycle Time Absolute: Check End	

## 148. McuMotorRotationInfo2 (CT -0)

Fail

Test case begin: 08/14/2024 02:26:34 PM +05:30

# Report\_Test\_Configuration\_1

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	20 ms
Number of failures	1
Message ID	419368175x (0x18FF0CEFx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
201.4466668		⬇️ Cycle Time Absolute: Check Start	
Wait			
201.4626668	Condition violation at 201.457668	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuMotorRotationInfo2' ID = 419368175x (0x18FF0CEFx) on bus CAN2.	Fail
201.4666668	1	Waited for 20.000 ms	
201.4666668		Test aborted due to BreakOnFail behavior.	
201.4666668		⬇️ Cycle Time Absolute: Check End	

## 149. McuOnEvent1

Pass

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419366383x (0x18FF05EFx)
Minimal measured cycle time	92.368427 ms
Maximal measured cycle time	92.368427 ms
Average cycle time	92.368427 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																						
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms	
0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part																							
Time Stamp	Test Step	Title	Verdict																				
201.4666668		Cycle Time Absolute: Check Start																					
Wait																							
201.6666668	1	Waited for 200.000 ms																					
201.6666668		Cycle Time Absolute: Check End																					

## 150. McuProtocolVersion (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute																							
Statistic																							
Runtime of the statistic	200 ms																						
Number of failures	1																						
Message ID	419366895x (0x18FF07EFx)																						
Minimal measured cycle time	0 ms																						
Maximal measured cycle time	0 ms																						
Average cycle time	0 ms																						
Failure ratio (in %)	0																						
Distribution of measured times																							
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms		
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																						
Time Stamp	Test Step	Title	Verdict																			
201.6666668		Cycle Time Absolute: Check Start																				
Wait																						
201.7816668	Condition violation at 201.776668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuProtocolVersion' ID = 419366895x (0x18FF07EFx) on bus CAN2.	Fail																			
201.8666668	1	Waited for 200.000 ms																				
201.8666668		Test aborted due to BreakOnFail behavior.																				
201.8666668		Cycle Time Absolute: Check End																				

# Report\_Test\_Configuration\_1

## 151. McuSlowCommandResponse

Fail

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:34 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⚠️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	1
Number of samples	2
Message ID	218038767x (0xCFF01EFx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	100

Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

### >Main Part

Time Stamp	Test Step	Title	Verdict
201.866668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
202.018939	Condition violation at 202.013939	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuSlowCommandResponse' ID = 218038767x (0xCFF01EFx) on bus CAN2.	Fail
202.066668	1	Waited for 200.000 ms	
202.066668		Test aborted due to BreakOnFail behavior.	
202.066668		⬇️ Cycle Time Absolute: Check End	

## 152. McuSoftwareVersion

Pass

Test case begin: 08/14/2024 02:26:34 PM +05:30

Test case end: 08/14/2024 02:26:35 PM +05:30

Duration: 00:00:01

### Background Checks

#### ⚠️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		419366639x (0x18FF06EFx)																
Minimal measured cycle time		99.558129 ms																
Maximal measured cycle time		99.558129 ms																
Average cycle time		99.558129 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
202.066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
202.266668	1	Waited for 200.000 ms	
202.266668		⬇️ Cycle Time Absolute: Check End	

## 153. McuSpecification1 (CT -0)

Fail

Test case begin: 08/14/2024 02:26:35 PM +05:30

Test case end: 08/14/2024 02:26:35 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																					Fail	
Statistic																					Fail	
Runtime of the statistic		200 ms																				Fail
Number of failures		1																				Fail
Message ID		419367151x (0x18FF08EFx)																				Fail
Minimal measured cycle time		0 ms																				Fail
Maximal measured cycle time		0 ms																				Fail
Average cycle time		0 ms																				Fail
Failure ratio (in %)		0																				Fail

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
202.266668		⬇️ Cycle Time Absolute: Check Start	
Wait			
202.381668	Condition violation at 202.376668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuSpecification1' ID = 419367151x (0x18FF08EFx) on bus CAN2.	Fail
202.466668	1	Waited for 200.000 ms	
202.466668		Test aborted due to BreakOnFail behavior.	
202.466668		⬇️_ Cycle Time Absolute: Check End	

## 154. McuThermal1

Fail

Test case begin: 08/14/2024 02:26:35 PM +05:30

Test case end: 08/14/2024 02:26:35 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	3
Number of samples	4
Message ID	419366127x (0x18FF04EFx)
Minimal measured cycle time	0.308337 ms
Maximal measured cycle time	89.796728 ms
Average cycle time	30.351353 ms
Failure ratio (in %)	100

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
202.466668		⬇️ Cycle Time Absolute: Check Start	
Wait			
202.515537	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'McuThermal1' ID = 419366127x (0x18FF04EFx) on bus CAN2. Time = 0.948996 ms	Fail
202.605334	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'McuThermal1' ID = 419366127x (0x18FF04EFx) on bus CAN2. Time = 89.796728 ms	Fail
202.605642	Condition violation	⬇️! Cycle Time Absolute: Minimum cycle time violation for CAN message 'McuThermal1' ID = 419366127x (0x18FF04EFx) on bus CAN2. Time = 0.308337 ms	Fail
202.666668	1	Waited for 200.000 ms	
202.666668		Test aborted due to BreakOnFail behavior.	

# Report\_Test\_Configuration\_1

202.666668

 Cycle Time Absolute: Check End

Pass

Test case begin: 08/14/2024 02:26:35 PM +05:30

Test case end: 08/14/2024 02:26:35 PM +05:30

Duration: 00:00:00

## Background Checks

  Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419369455x (0x18FF11EFx)
Minimal measured cycle time	99.834191 ms
Maximal measured cycle time	99.834191 ms
Average cycle time	99.834191 ms
Failure ratio (in %)	0

## Distribution of measured times

<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms
80 ms	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
202.666668		 Cycle Time Absolute: Check Start	
 Wait			
202.866668	1	Waited for 200.000 ms	
202.866668		 Cycle Time Absolute: Check End	

## 156. McuTorqueSpeedModeVoltSpec1 (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:35 PM +05:30

Test case end: 08/14/2024 02:26:35 PM +05:30

Duration: 00:00:00

## Background Checks

  Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	20 ms
Number of failures	1
Message ID	419367407x (0x18FF09EFx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

Distribution of measured times																						
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
202.866668		⬇️ Cycle Time Absolute: Check Start	
Wait			
202.882668	Condition violation at 202.877668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuTorqueSpeedModeVoltSpec1' ID = 419367407x (0x18FF09EFx) on bus CAN2.	Fail
202.886668	1	Waited for 20.000 ms	
202.886668		Test aborted due to BreakOnFail behavior.	
202.886668		⬇️ Cycle Time Absolute: Check End	

## 157. McuVoltageModeVoltSpec1 (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:35 PM +05:30

Test case end: 08/14/2024 02:26:35 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute	
<b>Statistic</b>	
Runtime of the statistic	20 ms
Number of failures	1
Message ID	419367663x (0x18FF0AEFx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part																				
Time Stamp	Test Step	Title																		Verdict
202.886668		Cycle Time Absolute: Check Start																		
Wait																				
202.902668	Condition violation at 202.897668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'McuVoltageModeVoltSpec1' ID = 419367663x (0x18FF0AEFx) on bus CAN2.																		Fail
202.906668	1	Waited for 20.000 ms																		
202.906668		Test aborted due to BreakOnFail behavior.																		
202.906668		Cycle Time Absolute: Check End																		

## 158. (PCAN CH 3 TX TCP/PCAN CH3 RX) pumpDebug

Pass

Test case begin: 08/14/2024 02:26:35 PM +05:30

Test case end: 08/14/2024 02:26:37 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		2000 ms
Number of samples		2
Message ID		218046376x (0xCFF1FA8x)
Minimal measured cycle time		1000.411996 ms
Maximal measured cycle time		1000.411996 ms
Average cycle time		1000.411996 ms
Failure ratio (in %)		0

### Distribution of measured times

<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
202.906668		Cycle Time Absolute: Check Start	
Wait			
204.906668	1	Waited for 2000.000 ms	
		Cycle Time Absolute: Check End	

# Report\_Test\_Configuration\_1

## 159. pumpStatus1\_P1\_H

Fail

Test case begin: 08/14/2024 02:26:37 PM +05:30

Test case end: 08/14/2024 02:26:39 PM +05:30

Duration: 00:00:02

### Background Checks

#### ⚠️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	2000 ms
Number of failures	4
Number of samples	6
Message ID	419393704x (0x18FF70A8x)
Minimal measured cycle time	0.282258 ms
Maximal measured cycle time	997.157244 ms
Average cycle time	200.408927 ms
Failure ratio (in %)	80

Distribution of measured times																					
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
4	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0

### ⚠️ Main Part

Time Stamp	Test Step	Title	Verdict
204.906668		⬇️ Cycle Time Absolute: Check Start	
↙ Wait			
205.002705	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'pumpStatus1_P1_H' ID = 419393704x (0x18FF70A8x) on bus CAN3. Time = 0.282258 ms	Fail
205.005017	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'pumpStatus1_P1_H' ID = 419393704x (0x18FF70A8x) on bus CAN3. Time = 2.311567 ms	Fail
206.002456	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'pumpStatus1_P1_H' ID = 419393704x (0x18FF70A8x) on bus CAN3. Time = 0.282258 ms	Fail
206.004468	Condition violation	⬇️ Cycle Time Absolute: Minimum cycle time violation for CAN message 'pumpStatus1_P1_H' ID = 419393704x (0x18FF70A8x) on bus CAN3. Time = 2.011308 ms	Fail
206.906668	1	Waited for 2000.000 ms	
206.906668		Test aborted due to BreakOnFail behavior.	
206.906668		⬇️ Cycle Time Absolute: Check End	

## 160. pumpStatus2\_P1\_H

Pass

Test case begin: 08/14/2024 02:26:39 PM +05:30

Test case end: 08/14/2024 02:26:43 PM +05:30

Duration: 00:00:04

### Background Checks

#### ⚠️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	4000 ms
Number of samples	2
Message ID	436202408x (0x19FFEBA8x)
Minimal measured cycle time	2002.073642 ms
Maximal measured cycle time	2002.073642 ms
Average cycle time	2002.073642 ms
Failure ratio (in %)	0

Distribution of measured times																						
<=	1980 ms	1982 ms	1984 ms	1986 ms	1988 ms	1990 ms	1992 ms	1994 ms	1996 ms	1998 ms	2000 ms	2002 ms	2004 ms	2006 ms	2008 ms	2010 ms	2012 ms	2014 ms	2016 ms	2018 ms	2020 ms	> 2020 ms
0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
206.9066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
210.9066668	1	Waited for 4000.000 ms	
210.9066668		⬇️ Cycle Time Absolute: Check End	

## 161. (TCAN CH 2 TX TCU/PCAN CH 3 RX)ACKM\_TCU (CT -0)

Fail

Test case begin: 08/14/2024 02:26:43 PM +05:30

Test case end: 08/14/2024 02:26:43 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																							
Statistic																							
Runtime of the statistic																							
200 ms																							
Number of failures																							
1																							
Message ID																							
417923067x (0x18E8FFFFBx)																							
Minimal measured cycle time																							
0 ms																							
Maximal measured cycle time																							
0 ms																							
Average cycle time																							
0 ms																							
Failure ratio (in %)																							
0																							
Distribution of measured times																							
<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	120 ms	> 120 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			
Time Stamp	Test Step	Title	Verdict
210.906668		⬇️ Cycle Time Absolute: Check Start	
Wait			
211.021668	Condition violation at 211.016668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'ACKM_TCU' ID = 417923067x (0x18E8FFFFBx) on bus CAN3.	Fail
211.106668	1	Waited for 200.000 ms	
211.106668		Test aborted due to BreakOnFail behavior.	
211.106668		⬇️_ Cycle Time Absolute: Check End	

## 162. CI\_TCU (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:43 PM +05:30

Test case end: 08/14/2024 02:26:44 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute		Fail																			
Statistic																					
Runtime of the statistic																					
Runtime of the statistic	200 ms																				
Number of failures	1																				
Message ID	419359739x (0x18FEEFBFx)																				
Minimal measured cycle time	0 ms																				
Maximal measured cycle time	0 ms																				
Average cycle time	0 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
211.106668		⬇️ Cycle Time Absolute: Check Start	
Wait			
211.221668	Condition violation at 211.216668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'CI_TCU' ID = 419359739x (0x18FEEFBFx) on bus CAN2.	Fail
211.306668	1	Waited for 200.000 ms	
211.306668		Test aborted due to BreakOnFail behavior.	
211.306668		⬇️_ Cycle Time Absolute: Check End	

## 163. DM2\_TCU (CT -0)

Fail

Test case begin: 08/14/2024 02:26:44 PM +05:30

# Report\_Test\_Configuration\_1

Test case end: 08/14/2024 02:26:44 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	200 ms
Number of failures	1
Message ID	419351547x (0x18FECBFBx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

Distribution of measured times																					
<=	80 ms	82 ms	84 ms	86 ms	88 ms	90 ms	92 ms	94 ms	96 ms	98 ms	100 ms	102 ms	104 ms	106 ms	108 ms	110 ms	112 ms	114 ms	116 ms	118 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
211.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
211.421668	Condition violation at 211.416668	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'DM2_TCU' ID = 419351547x (0x18FECBFBx) on bus CAN2.	Fail
211.506668	1	Waited for 200.000 ms	
211.506668		Test aborted due to BreakOnFail behavior.	
211.506668		⬇️ Cycle Time Absolute: Check End	

## 164. prop\_TPMS\_features\_rqst

Pass

Test case begin: 08/14/2024 02:26:44 PM +05:30

Test case end: 08/14/2024 02:26:44 PM +05:30

Duration: 00:00:00

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419392507x (0x18FF6BFBx)
Minimal measured cycle time	99.279128 ms
Maximal measured cycle time	99.279128 ms
Average cycle time	99.279128 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																						
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms	
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
211.506668		⬇️ Cycle Time Absolute: Check Start	
Wait			
211.706668	1	Waited for 200.000 ms	
211.706668		⬇️ Cycle Time Absolute: Check End	

## 165. Proprietary\_DMS\_1 (CT -0)

Fail

Test case begin: 08/14/2024 02:26:44 PM +05:30

Test case end: 08/14/2024 02:26:44 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Fail																			
Statistic																					
Runtime of the statistic	20 ms																				
Number of failures																					
Message ID	419403515x (0x18FF96FBx)																				
Minimal measured cycle time	0 ms																				
Maximal measured cycle time	0 ms																				
Average cycle time	0 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
211.706668		⬇️ Cycle Time Absolute: Check Start	
Wait			
211.722668	Condition violation at 211.717668	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'Proprietary_DMS_1' ID = 419403515x (0x18FF96FBx) on bus CAN1.	Fail
211.726668	1	Waited for 20.000 ms	
211.726668		Test aborted due to BreakOnFail behavior.	
211.726668		⬇️ Cycle Time Absolute: Check End	

# Report\_Test\_Configuration\_1

## 166. Proprietary\_DMS3 (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:44 PM +05:30

Test case end: 08/14/2024 02:26:44 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	20 ms
Number of failures	1
Message ID	419404539x (0x18FF9AFBx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
211.726668		Cycle Time Absolute: Check Start	
<b>Wait</b>			
211.742668	Condition violation at 211.737668	Cycle Time Absolute: Maximum cycle time violation for CAN message 'Proprietary_DMS3' ID = 419404539x (0x18FF9AFBx) on bus CAN1.	Fail
211.746668	1	Waited for 20.000 ms	
211.746668		Test aborted due to BreakOnFail behavior.	
211.746668		Cycle Time Absolute: Check End	

## 167. Proprietary\_DMS4 (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:44 PM +05:30

Test case end: 08/14/2024 02:26:44 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic																				
Runtime of the statistic		20 ms																		
Number of failures		1																		
Message ID		419405051x (0x18FF9CFBx)																		
Minimal measured cycle time		0 ms																		
Maximal measured cycle time		0 ms																		
Average cycle time		0 ms																		
Failure ratio (in %)		0																		

Distribution of measured times																					
<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
211.746668		⬇️ Cycle Time Absolute: Check Start	
Wait			
211.762668	Condition violation at 211.757668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'Proprietary_DMS4' ID = 419405051x (0x18FF9CFBx) on bus CAN1.	Fail
211.766668	1	Waited for 20.000 ms	
211.766668		Test aborted due to BreakOnFail behavior.	
211.766668		⬇️ Cycle Time Absolute: Check End	

## 168. Proprietary\_Tilt\_Angle

Pass

Test case begin: 08/14/2024 02:26:44 PM +05:30

Test case end: 08/14/2024 02:26:46 PM +05:30

Duration: 00:00:02

### Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic	2000 ms	
Number of samples	2	
Message ID	419401979x (0x18FF90FBx)	
Minimal measured cycle time	998.94686 ms	
Maximal measured cycle time	998.94686 ms	
Average cycle time	998.94686 ms	
Failure ratio (in %)	0	

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms		
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
211.766668		⬇️ Cycle Time Absolute: Check Start	
Wait			
213.766668	1	Waited for 2000.000 ms	
213.766668		⬇️ Cycle Time Absolute: Check End	

## 169. SOFT\_TCU (CT - 0)

Fail

Test case begin: 08/14/2024 02:26:46 PM +05:30

Test case end: 08/14/2024 02:26:46 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	20 ms
Number of failures	1
Message ID	419355387x (0x18FEDAFBx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
213.766668		⬇️ Cycle Time Absolute: Check Start	
Wait			
213.782668	Condition violation at 213.777668	⬇️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'SOFT_TCU' ID = 419355387x (0x18FEDAFBx) on bus CAN2.	Fail
213.786668	1	Waited for 20.000 ms	
213.786668		Test aborted due to BreakOnFail behavior.	
213.786668		⬇️ Cycle Time Absolute: Check End	

# Report\_Test\_Configuration\_1

## 170. SSI ( Remark- Rx node is NOT defined DB)

Pass

Test case begin: 08/14/2024 02:26:46 PM +05:30

Test case end: 08/14/2024 02:26:46 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	20 ms
Number of samples	2
Message ID	217060347x (0xCF013FBx)
Minimal measured cycle time	10.577322 ms
Maximal measured cycle time	10.577322 ms
Average cycle time	10.577322 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
213.786668		Cycle Time Absolute: Check Start	
Wait			
213.806668	1	Waited for 20.000 ms	
213.806668		Cycle Time Absolute: Check End	

## 171. TD (Remark - received on I & P CAN but Failed)

Fail

Test case begin: 08/14/2024 02:26:46 PM +05:30

Test case end: 08/14/2024 02:26:46 PM +05:30

Duration: 00:00:00

### Background Checks

#### Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	100 ms
Number of failures	1
Message ID	419358459x (0x18FEE6FBx)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 30 ms	30 ms - 32 ms	32 ms - 34 ms	34 ms - 36 ms	36 ms - 38 ms	38 ms - 40 ms	40 ms - 42 ms	42 ms - 44 ms	44 ms - 46 ms	46 ms - 48 ms	48 ms - 50 ms	50 ms - 52 ms	52 ms - 54 ms	54 ms - 56 ms	56 ms - 58 ms	58 ms - 60 ms	60 ms - 62 ms	62 ms - 64 ms	64 ms - 66 ms	66 ms - 68 ms	68 ms - 70 ms	> 70 ms		
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
213.806668		⬇️ Cycle Time Absolute: Check Start	
Wait			
213.871668	Condition violation at 213.866668	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'TD' ID = 419358459x (0x18FEE6FBx) on bus CAN4.	Fail
213.906668	1	Waited for 100.000 ms	
213.906668		Test aborted due to BreakOnFail behavior.	
213.906668		⬇️_ Cycle Time Absolute: Check End	

## 172. (CCAN CH 4 TX TPMS/ RX)ACKM\_TPMS

Pass

Test case begin: 08/14/2024 02:26:46 PM +05:30

Test case end: 08/14/2024 02:26:46 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic	200 ms	
Number of samples	2	
Message ID	417922867x (0x18E8FF33x)	
Minimal measured cycle time	98.104891 ms	
Maximal measured cycle time	98.104891 ms	
Average cycle time	98.104891 ms	
Failure ratio (in %)	0	

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

Main Part			
Time Stamp	Test Step	Title	Verdict
213.906668		⬇️ Cycle Time Absolute: Check Start	
Wait			
214.106668	1	Waited for 200.000 ms	
214.106668		⬇️_ Cycle Time Absolute: Check End	

# Report\_Test\_Configuration\_1

## 173. CI\_TPMS

Pass

Test case begin: 08/14/2024 02:26:46 PM +05:30

Test case end: 08/14/2024 02:26:47 PM +05:30

Duration: 00:00:01

### Background Checks

#### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419359539x (0x18FEEB33x)
Minimal measured cycle time	100.875251 ms
Maximal measured cycle time	100.875251 ms
Average cycle time	100.875251 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

### Main Part

Time Stamp	Test Step	Title	Verdict
214.106668		Cycle Time Absolute: Check Start	
Wait			
214.306668	1	Waited for 200.000 ms	
214.306668		Cycle Time Absolute: Check End	

## 174. DM1\_TPMS

Fail

Test case begin: 08/14/2024 02:26:47 PM +05:30

Test case end: 08/14/2024 02:26:49 PM +05:30

Duration: 00:00:02

### Background Checks

#### Cycle Time Absolute

Fail

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	2000 ms
Number of failures	1
Number of samples	2
Message ID	419351091x (0x18FECA33x)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	100

Distribution of measured times																									
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms				
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

Main Part			
Time Stamp	Test Step	Title	Verdict
214.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
215.350282	Condition violation at 215.345282	⬇️! Cycle Time Absolute: Maximum cycle time violation for CAN message 'DM1_TPMS' ID = 419351091x (0x18FECA33x) on bus CAN1.	Fail
216.306668	1	Waited for 2000.000 ms	
216.306668		Test aborted due to BreakOnFail behavior.	
216.306668		⬇️ Cycle Time Absolute: Check End	

## 175. DM2\_TPMS Pass

Test case begin: 08/14/2024 02:26:49 PM +05:30

Test case end: 08/14/2024 02:26:49 PM +05:30

Duration: 00:00:00

### Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		200 ms
Number of samples		2
Message ID		419351347x (0x18FECB33x)
Minimal measured cycle time		100.65631 ms
Maximal measured cycle time		100.65631 ms
Average cycle time		100.65631 ms
Failure ratio (in %)		0

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
216.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
216.506668	1	Waited for 200.000 ms	
216.506668		⬇️ Cycle Time Absolute: Check End	

## 176. DM4\_TPMS

Pass

Test case begin: 08/14/2024 02:26:49 PM +05:30

Test case end: 08/14/2024 02:26:49 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic	200 ms																				
Number of samples	2																				
Message ID	419351859x (0x18FECD33x)																				
Minimal measured cycle time	99.769473 ms																				
Maximal measured cycle time	99.769473 ms																				
Average cycle time	99.769473 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
216.506668		⬇️ Cycle Time Absolute: Check Start	
Wait			
216.706668	1	Waited for 200.000 ms	
216.706668		⬇️ Cycle Time Absolute: Check End	

## 177. Prop\_Sensor\_ID\_Response

Pass

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:26:49 PM +05:30

Test case end: 08/14/2024 02:26:50 PM +05:30

Duration: 00:00:01

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	419398195x (0x18FF8233x)
Minimal measured cycle time	500.09719 ms
Maximal measured cycle time	500.09719 ms
Average cycle time	500.09719 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
216.706668		Cycle Time Absolute: Check Start	
Wait			
217.706668	1	Waited for 1000.000 ms	
217.706668		Cycle Time Absolute: Check End	

## 178. Prop\_TPMS\_learn\_response

Pass

Test case begin: 08/14/2024 02:26:50 PM +05:30

Test case end: 08/14/2024 02:26:52 PM +05:30

Duration: 00:00:02

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	2000 ms
Number of samples	2
Message ID	419390771x (0x18FF6533x)
Minimal measured cycle time	999.718068 ms
Maximal measured cycle time	999.718068 ms
Average cycle time	999.718068 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																					
<= 980 ms	980 ms - 982 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
217.706668		⬇️ Cycle Time Absolute: Check Start	
Wait			
219.706668	1	Waited for 2000.000 ms	
219.706668		⬇️ Cycle Time Absolute: Check End	

## 179. SOFT\_TPMS

Pass

Test case begin: 08/14/2024 02:26:52 PM +05:30

Test case end: 08/14/2024 02:26:52 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419355187x (0x18FEDA33x)
Minimal measured cycle time	99.88127 ms
Maximal measured cycle time	99.88127 ms
Average cycle time	99.88127 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## Main Part

Time Stamp	Test Step	Title	Verdict
219.706668		⬇️ Cycle Time Absolute: Check Start	
Wait			
219.906668	1	Waited for 200.000 ms	
219.906668		⬇️ Cycle Time Absolute: Check End	

## 180. TIRE1

Pass

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:26:52 PM +05:30

Test case end: 08/14/2024 02:26:53 PM +05:30

Duration: 00:00:01

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	419361843x (0x18FEF433x)
Minimal measured cycle time	500.125777 ms
Maximal measured cycle time	500.125777 ms
Average cycle time	500.125777 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
219.906668		Cycle Time Absolute: Check Start	
Wait			
220.906668	1	Waited for 1000.000 ms	
220.906668		Cycle Time Absolute: Check End	

# 181. TIRE2

Pass

Test case begin: 08/14/2024 02:26:53 PM +05:30

Test case end: 08/14/2024 02:26:54 PM +05:30

Duration: 00:00:01

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	419185203x (0x18FC4233x)
Minimal measured cycle time	494.311527 ms
Maximal measured cycle time	494.311527 ms
Average cycle time	494.311527 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																							
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms		
0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## >Main Part

Time Stamp	Test Step	Title	Verdict
220.906668		⬇️ Cycle Time Absolute: Check Start	
Wait			
221.906668	1	Waited for 1000.000 ms	
221.906668		⬇️ Cycle Time Absolute: Check End	

## 182. TPMS\_data\_1

Pass

Test case begin: 08/14/2024 02:26:54 PM +05:30

Test case end: 08/14/2024 02:26:54 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419390536x (0x18FF6448x)
Minimal measured cycle time	98.283029 ms
Maximal measured cycle time	98.283029 ms
Average cycle time	98.283029 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
221.906668		⬇️ Cycle Time Absolute: Check Start	
Wait			
222.106668	1	Waited for 200.000 ms	
222.106668		⬇️ Cycle Time Absolute: Check End	

## 183. TPMS\_data\_2

Pass

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:26:54 PM +05:30

Test case end: 08/14/2024 02:26:55 PM +05:30

Duration: 00:00:01

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	200 ms
Number of samples	2
Message ID	419391304x (0x18FF6748x)
Minimal measured cycle time	100.325885 ms
Maximal measured cycle time	100.325885 ms
Average cycle time	100.325885 ms
Failure ratio (in %)	0

### Distribution of measured times

<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
222.106668		Cycle Time Absolute: Check Start	
222.306668	1	Waited for 200.000 ms	
222.306668		Cycle Time Absolute: Check End	

## 184. TPMS\_Leakage\_rate\_1

Pass

Test case begin: 08/14/2024 02:26:55 PM +05:30

Test case end: 08/14/2024 02:27:05 PM +05:30

Duration: 00:00:10

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419367475x (0x18FF0A33x)
Minimal measured cycle time	4871.270499 ms
Maximal measured cycle time	4871.270499 ms
Average cycle time	4871.270499 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																								
<= 363 0 ms	3630 ms - 3722 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 547 0 ms			
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
222.306668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
232.306668	1	Waited for 10000.000 ms	
232.306668		⬇️ Cycle Time Absolute: Check End	

## 185. TPMS\_Leakage\_rate\_2

Pass

Test case begin: 08/14/2024 02:27:05 PM +05:30

Test case end: 08/14/2024 02:27:15 PM +05:30

Duration: 00:00:10

## Background Checks

### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419367987x (0x18FF0C33x)
Minimal measured cycle time	4871.682361 ms
Maximal measured cycle time	4871.682361 ms
Average cycle time	4871.682361 ms
Failure ratio (in %)	0

## Distribution of measured times

<= 363 0 ms	3630 ms - 3722 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 547 0 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
232.306668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
242.306668	1	Waited for 10000.000 ms	
242.306668		⬇️ Cycle Time Absolute: Check End	

## 186. TPMS\_Leakage\_rate\_3

Pass

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:27:15 PM +05:30

Test case end: 08/14/2024 02:27:25 PM +05:30

Duration: 00:00:10

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419415091x (0x18FFC433x)
Minimal measured cycle time	4880.193606 ms
Maximal measured cycle time	4880.193606 ms
Average cycle time	4880.193606 ms
Failure ratio (in %)	0

### Distribution of measured times

<=	3630 ms - 3722 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 5470 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
242.306668		Cycle Time Absolute: Check Start	
Wait			
252.306668	1	Waited for 10000.000 ms	
252.306668		Cycle Time Absolute: Check End	

## 187. TPMS\_Leakage\_rate\_4

Pass

Test case begin: 08/14/2024 02:27:25 PM +05:30

Test case end: 08/14/2024 02:27:35 PM +05:30

Duration: 00:00:10

## Background Checks

### Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419415603x (0x18FFC633x)
Minimal measured cycle time	4880.341098 ms
Maximal measured cycle time	4880.341098 ms
Average cycle time	4880.341098 ms
Failure ratio (in %)	0

# Report\_Test\_Configuration\_1

Distribution of measured times																								
<= 363 0 ms	3630 ms - 3722 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 547 0 ms			
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
252.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
262.306668	1	Waited for 10000.000 ms	
262.306668		⬇️ Cycle Time Absolute: Check End	

## 188. TPMS\_Leakage\_rate\_5

Pass

Test case begin: 08/14/2024 02:27:35 PM +05:30

Test case end: 08/14/2024 02:27:45 PM +05:30

Duration: 00:00:10

## Background Checks

### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419416371x (0x18FFC933x)
Minimal measured cycle time	4871.06957 ms
Maximal measured cycle time	4871.06957 ms
Average cycle time	4871.06957 ms
Failure ratio (in %)	0

## Distribution of measured times

<= 363 0 ms	3630 ms - 3722 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 547 0 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
262.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
272.306668	1	Waited for 10000.000 ms	
272.306668		⬇️ Cycle Time Absolute: Check End	

## 189. TPMS\_Leakage\_rate\_6

Fail

# Report\_Test\_Configuration\_1

Test case begin: 08/14/2024 02:27:45 PM +05:30

Test case end: 08/14/2024 02:27:47 PM +05:30

Duration: 00:00:02

## Background Checks

### ⚠️ Cycle Time Absolute

Fail

Statistic	
Runtime of the statistic	2000 ms
Number of failures	1
Number of samples	1
Message ID	419396659x (0x18FF7C33x)
Minimal measured cycle time	0 ms
Maximal measured cycle time	0 ms
Average cycle time	0 ms
Failure ratio (in %)	0

Distribution of measured times																					
<=	980 ms	982 ms - 984 ms	984 ms - 986 ms	986 ms - 988 ms	988 ms - 990 ms	990 ms - 992 ms	992 ms - 994 ms	994 ms - 996 ms	996 ms - 998 ms	998 ms - 1000 ms	1000 ms - 1002 ms	1002 ms - 1004 ms	1004 ms - 1006 ms	1006 ms - 1008 ms	1008 ms - 1010 ms	1010 ms - 1012 ms	1012 ms - 1014 ms	1014 ms - 1016 ms	1016 ms - 1018 ms	1018 ms - 1020 ms	> 1020 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	

## ⚠️ Main Part

Time Stamp	Test Step	Title	Verdict
272.306668		⚠️ Cycle Time Absolute: Check Start	
Wait			
273.321668	Condition violation at 273.316668	⚠️ Cycle Time Absolute: Maximum cycle time violation for CAN message 'TPMS_Leakage_rate_6' ID = 419396659x (0x18FF7C33x) on bus CAN2.	Fail
274.306668	1	Waited for 2000.000 ms	
274.306668		Test aborted due to BreakOnFail behavior.	
274.306668		⬇️ Cycle Time Absolute: Check End	

## 190. TPMS\_Pressure\_1

Pass

Test case begin: 08/14/2024 02:27:47 PM +05:30

Test case end: 08/14/2024 02:27:57 PM +05:30

Duration: 00:00:10

## Background Checks

### ⚠️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419393843x (0x18FF7133x)
Minimal measured cycle time	4871.55716 ms
Maximal measured cycle time	4871.55716 ms
Average cycle time	4871.55716 ms
Failure ratio (in %)	0

Distribution of measured times																								
<=	3630	3722	3814	3906	3998	4090	4182	4274	4366	4458	4550	4642	4734	4826	4918	5010	5102	5194	5286	5378	>	5470	0 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
274.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
284.306668	1	Waited for 10000.000 ms	
284.306668		⬇️ Cycle Time Absolute: Check End	

## 191. TPMS\_Pressure\_2

Pass

Test case begin: 08/14/2024 02:27:57 PM +05:30

Test case end: 08/14/2024 02:28:07 PM +05:30

Duration: 00:00:10

## Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		10000 ms
Number of samples		2
Message ID		419394611x (0x18FF7433x)
Minimal measured cycle time		4981.104721 ms
Maximal measured cycle time		4981.104721 ms
Average cycle time		4981.104721 ms
Failure ratio (in %)		0

Distribution of measured times																								
<=	3630	3722	3814	3906	3998	4090	4182	4274	4366	4458	4550	4642	4734	4826	4918	5010	5102	5194	5286	5378	>	5470	0 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			Verdict
Time Stamp	Test Step	Title	
284.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
294.306668	1	Waited for 10000.000 ms	
294.306668		⬇️ Cycle Time Absolute: Check End	

## 192. TPMS\_Pressure\_3

Pass

Test case begin: 08/14/2024 02:28:07 PM +05:30

Test case end: 08/14/2024 02:28:17 PM +05:30

Duration: 00:00:10

### Background Checks

⬇️ Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic 10000 ms																					
Number of samples 2																					
Message ID 419395123x (0x18FF7633x)																					
Minimal measured cycle time 4871.076995 ms																					
Maximal measured cycle time 4871.076995 ms																					
Average cycle time 4871.076995 ms																					
Failure ratio (in %) 0																					
Distribution of measured times																					
<= 363 ms	3630 ms - 3722 ms	3722 ms - 3814 ms	3814 ms - 3906 ms	3906 ms - 3998 ms	3998 ms - 4090 ms	4090 ms - 4182 ms	4182 ms - 4274 ms	4274 ms - 4366 ms	4366 ms - 4458 ms	4458 ms - 4550 ms	4550 ms - 4642 ms	4642 ms - 4734 ms	4734 ms - 4826 ms	4826 ms - 4918 ms	4918 ms - 5010 ms	5010 ms - 5102 ms	5102 ms - 5194 ms	5194 ms - 5286 ms	5286 ms - 5378 ms	5378 ms - 5470 ms	> 5470 ms
0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	0 ms	1 ms	0 ms						

Main Part			Verdict
Time Stamp	Test Step	Title	
294.306668		⬇️ Cycle Time Absolute: Check Start	
Wait			
304.306668	1	Waited for 10000.000 ms	
304.306668		⬇️ Cycle Time Absolute: Check End	

## 193. TPMS\_Status\_1

Pass

Test case begin: 08/14/2024 02:28:17 PM +05:30

Test case end: 08/14/2024 02:28:27 PM +05:30

Duration: 00:00:10

### Background Checks

⬇️ Cycle Time Absolute		Pass
Statistic		

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419365939x (0x18FF0433x)
Minimal measured cycle time	4871.324516 ms
Maximal measured cycle time	4871.324516 ms
Average cycle time	4871.324516 ms
Failure ratio (in %)	0

Distribution of measured times																								
<=	3630	3722	3814	3906	3998	4090	4182	4274	4366	4458	4550	4642	4734	4826	4918	5010	5102	5194	5286	5378	>	5470	0 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	>	5470	0 ms	

## >Main Part

Time Stamp	Test Step	Title	Verdict
304.3066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
314.3066668	1	Waited for 10000.000 ms	
314.3066668		⬇️ Cycle Time Absolute: Check End	

## 194. TPMS\_Status\_2

Pass

Test case begin: 08/14/2024 02:28:27 PM +05:30

Test case end: 08/14/2024 02:28:37 PM +05:30

Duration: 00:00:10

## Background Checks

Cycle Time Absolute		Pass
Statistic		
Runtime of the statistic		10000 ms
Number of samples		2
Message ID		419366451x (0x18FF0633x)
Minimal measured cycle time		4871.202208 ms
Maximal measured cycle time		4871.202208 ms
Average cycle time		4871.202208 ms
Failure ratio (in %)		0

Distribution of measured times																								
<=	3630	3722	3814	3906	3998	4090	4182	4274	4366	4458	4550	4642	4734	4826	4918	5010	5102	5194	5286	5378	>	5470	0 ms	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	>	5470	0 ms	

# Report\_Test\_Configuration\_1

## >Main Part

Time Stamp	Test Step	Title	Verdict
314.306668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
324.306668	1	Waited for 10000.000 ms	
324.306668		⬇️ Cycle Time Absolute: Check End	

## 195. TPMS\_Status\_3

Pass

Test case begin: 08/14/2024 02:28:37 PM +05:30

Test case end: 08/14/2024 02:28:47 PM +05:30

Duration: 00:00:10

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

Statistic	
Runtime of the statistic	10000 ms
Number of samples	2
Message ID	419366963x (0x18FF0833x)
Minimal measured cycle time	4881.259736 ms
Maximal measured cycle time	4881.259736 ms
Average cycle time	4881.259736 ms
Failure ratio (in %)	0

#### Distribution of measured times

<= 363 ms	363 ms - 372 ms	372 ms - 381 ms	381 ms - 390 ms	390 ms - 399 ms	399 ms - 409 ms	409 ms - 418 ms	418 ms - 427 ms	427 ms - 436 ms	436 ms - 445 ms	445 ms - 455 ms	455 ms - 464 ms	464 ms - 473 ms	473 ms - 482 ms	482 ms - 491 ms	491 ms - 501 ms	501 ms - 510 ms	510 ms - 519 ms	519 ms - 528 ms	528 ms - 537 ms	537 ms - 547 ms	> 547 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0

## Main Part

Time Stamp	Test Step	Title	Verdict
324.306668		⬇️ Cycle Time Absolute: Check Start	
▫ Wait			
334.306668	1	Waited for 10000.000 ms	
334.306668		⬇️ Cycle Time Absolute: Check End	

## 196. TPMS\_Temperature\_1

Pass

Test case begin: 08/14/2024 02:28:47 PM +05:30

Test case end: 08/14/2024 02:28:47 PM +05:30

Duration: 00:00:00

### Background Checks

#### ⬇️ Cycle Time Absolute

Pass

# Report\_Test\_Configuration\_1

Statistic																							
Runtime of the statistic		200 ms																					
Number of samples		2																					
Message ID		419394099x (0x18FF7233x)																					
Minimal measured cycle time		100.622931 ms																					
Maximal measured cycle time		100.622931 ms																					
Average cycle time		100.622931 ms																					
Failure ratio (in %)		0																					

Distribution of measured times																							
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms		
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
334.3066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
334.5066668	1	Waited for 200.000 ms	
334.5066668		⬇️ Cycle Time Absolute: Check End	

## 197. TPMS\_Temperature\_2

Pass

Test case begin: 08/14/2024 02:28:47 PM +05:30

Test case end: 08/14/2024 02:28:47 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																							
Statistic																							
Runtime of the statistic		200 ms																					
Number of samples		2																					
Message ID		419395379x (0x18FF7733x)																					
Minimal measured cycle time		99.050558 ms																					
Maximal measured cycle time		99.050558 ms																					
Average cycle time		99.050558 ms																					
Failure ratio (in %)		0																					

Distribution of measured times																							
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms		
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			Verdict
Time Stamp	Test Step	Title	
334.506668		⬇️ Cycle Time Absolute: Check Start	
Wait			
334.706668	1	Waited for 200.000 ms	
334.706668		⬇️ Cycle Time Absolute: Check End	

## 198. (ICAN CH 1 TX VECU/ TCU RX)CBCU\_Proprietary

Pass

Test case begin: 08/14/2024 02:28:47 PM +05:30

Test case end: 08/14/2024 02:28:48 PM +05:30

Duration: 00:00:01

### Background Checks

⬇️ Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic	1000 ms																				
Number of samples	2																				
Message ID	419389985x (0x18FF6221x)																				
Minimal measured cycle time	500.730037 ms																				
Maximal measured cycle time	500.730037 ms																				
Average cycle time	500.730037 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 480 ms	480 ms - 482 ms	482 ms - 484 ms	484 ms - 486 ms	486 ms - 488 ms	488 ms - 490 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0

Main Part			Verdict
Time Stamp	Test Step	Title	
334.706668		⬇️ Cycle Time Absolute: Check Start	
Wait			
335.706668	1	Waited for 1000.000 ms	
335.706668		⬇️ Cycle Time Absolute: Check End	

## 199. CCVS1\_VECU

Pass

Test case begin: 08/14/2024 02:28:48 PM +05:30

Test case end: 08/14/2024 02:28:48 PM +05:30

Duration: 00:00:00

### Background Checks

⬇️ Cycle Time Absolute		Pass

# Report\_Test\_Configuration\_1

Statistic																		
Runtime of the statistic		200 ms																
Number of samples		2																
Message ID		419361057x (0x18FEF121x)																
Minimal measured cycle time		99.814291 ms																
Maximal measured cycle time		99.814291 ms																
Average cycle time		99.814291 ms																
Failure ratio (in %)		0																

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
335.7066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
335.9066668	1	Waited for 200.000 ms	
335.9066668		⬇️ Cycle Time Absolute: Check End	

## 200. DC1

Pass

Test case begin: 08/14/2024 02:28:48 PM +05:30

Test case end: 08/14/2024 02:28:48 PM +05:30

Duration: 00:00:00

## Background Checks

Cycle Time Absolute																				Pass
Statistic																				
Runtime of the statistic		200 ms																		
Number of samples		2																		
Message ID		419319329x (0x18FE4E21x)																		
Minimal measured cycle time		99.619316 ms																		
Maximal measured cycle time		99.619316 ms																		
Average cycle time		99.619316 ms																		
Failure ratio (in %)		0																		

Distribution of measured times																					
<=	80 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

# Report\_Test\_Configuration\_1

Main Part			Verdict
Time Stamp	Test Step	Title	
335.906668		⬇️ Cycle Time Absolute: Check Start	
Wait			
336.106668	1	Waited for 200.000 ms	
336.106668		⬇️ Cycle Time Absolute: Check End	

## 201. EBC1\_VECU

Pass

Test case begin: 08/14/2024 02:28:48 PM +05:30

Test case end: 08/14/2024 02:28:49 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute		Pass																			
Statistic																					
Runtime of the statistic																					
Runtime of the statistic	200 ms																				
Number of samples	2																				
Message ID	418382113x (0x18F00121x)																				
Minimal measured cycle time	100.088173 ms																				
Maximal measured cycle time	100.088173 ms																				
Average cycle time	100.088173 ms																				
Failure ratio (in %)	0																				
Distribution of measured times																					
<= 80 ms	80 ms - 82 ms	82 ms - 84 ms	84 ms - 86 ms	86 ms - 88 ms	88 ms - 90 ms	90 ms - 92 ms	92 ms - 94 ms	94 ms - 96 ms	96 ms - 98 ms	98 ms - 100 ms	100 ms - 102 ms	102 ms - 104 ms	104 ms - 106 ms	106 ms - 108 ms	108 ms - 110 ms	110 ms - 112 ms	112 ms - 114 ms	114 ms - 116 ms	116 ms - 118 ms	118 ms - 120 ms	> 120 ms
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

Main Part			Verdict
Time Stamp	Test Step	Title	
336.106668		⬇️ Cycle Time Absolute: Check Start	
Wait			
336.306668	1	Waited for 200.000 ms	
336.306668		⬇️ Cycle Time Absolute: Check End	

## 202. Front\_CBCU\_Output\_Status

Pass

Test case begin: 08/14/2024 02:28:49 PM +05:30

Test case end: 08/14/2024 02:28:50 PM +05:30

Duration: 00:00:01

### Background Checks

Cycle Time Absolute		Pass
Statistic		

# Report\_Test\_Configuration\_1

Statistic	
Runtime of the statistic	1000 ms
Number of samples	2
Message ID	419375137x (0x18FF2821x)
Minimal measured cycle time	500.280871 ms
Maximal measured cycle time	500.280871 ms
Average cycle time	500.280871 ms
Failure ratio (in %)	0

Distribution of measured times																						
<=	480 ms	482 ms - 482 ms	484 ms - 484 ms	486 ms - 486 ms	488 ms - 488 ms	490 ms - 492 ms	492 ms - 494 ms	494 ms - 496 ms	496 ms - 498 ms	498 ms - 500 ms	500 ms - 502 ms	502 ms - 504 ms	504 ms - 506 ms	506 ms - 508 ms	508 ms - 510 ms	510 ms - 512 ms	512 ms - 514 ms	514 ms - 516 ms	516 ms - 518 ms	518 ms - 520 ms	> 520 ms	
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	

## >Main Part

Time Stamp	Test Step	Title	Verdict
336.3066668		⬇️ Cycle Time Absolute: Check Start	
Wait			
337.3066668	1	Waited for 1000.000 ms	
337.3066668		⬇️ Cycle Time Absolute: Check End	

## 203. LD (CT - 0)

Fail

Test case begin: 08/14/2024 02:28:50 PM +05:30

Test case end: 08/14/2024 02:28:50 PM +05:30

Duration: 00:00:00

## Background Checks

LD		Fail
Statistic		
Runtime of the statistic		20 ms
Number of failures		1
Message ID		419315745x (0x18FE4021x)
Minimal measured cycle time		0 ms
Maximal measured cycle time		0 ms
Average cycle time		0 ms
Failure ratio (in %)		0

## Distribution of measured times

<= 8 ms	8 ms - 8.2 ms	8.2 ms - 8.4 ms	8.4 ms - 8.6 ms	8.6 ms - 8.8 ms	8.8 ms - 9 ms	9 ms - 9.2 ms	9.2 ms - 9.4 ms	9.4 ms - 9.6 ms	9.6 ms - 9.8 ms	9.8 ms - 10 ms	10 ms - 10.2 ms	10.2 ms - 10.4 ms	10.4 ms - 10.6 ms	10.6 ms - 10.8 ms	10.8 ms - 11 ms	11 ms - 11.2 ms	11.2 ms - 11.4 ms	11.4 ms - 11.6 ms	11.6 ms - 11.8 ms	11.8 ms - 12 ms	> 12 ms
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

## Report\_Test\_Configuration\_1

Main Part				
Time Stamp	Test Step	Title	Verdict	
337.306668		LD: Check Start		
Wait				
337.322668	Condition violation at 337.317668	LD: Maximum cycle time violation for CAN message 'LD' ID = 419315745x (0x18FE4021x) on bus CAN1.	Fail	
337.326668	1	Waited for 20.000 ms		
337.326668		Test aborted due to BreakOnFail behavior.		
337.326668		LD: Check End		

204. RPAS Master Controller

Fail

Test case begin: 08/14/2024 02:28:50 PM +05:30

Test case end: 08/14/2024 02:28:50 PM +05:30

Duration: 00:00:00

## **Background Checks**

Main Part			
Time Stamp	Test Step	Title	Verdict
337.326668		⬇️ RPAS_Master_Controller: Check Start	
Wait			
337.402881	Condition violation	⬇️ RPAS_Master_Controller: Minimum cycle time violation for CAN message 'RPAS_Master_Controller' ID = 419389473x (0x18FF6021x) on bus CAN1. Time = 7.828961 ms	Fail
337.503342	Condition violation	⬇️ RPAS_Master_Controller: Minimum cycle time violation for CAN message 'RPAS_Master_Controller' ID = 419389473x (0x18FF6021x) on bus CAN1. Time = 8.820489 ms	Fail
337.526668	1	Waited for 200.000 ms	
337.526668		Test aborted due to BreakOnFail behavior.	
337.526668		⬇️ RPAS_Master_Controller: Check End	