Presty wh Jaimed 18EE 30021

9	Date: _	 	
	Page:		_

- INCLUDE "M32DEF-INC"
. OR 4 0g00
· OR T OR TO
. EQU PA = R18
EQU PB = R19
· EQU PC = R20
-ERU PD = R21
· FRU RCTR=R23.
FRU RCTR= R23.
INIT 3 LDI R31, HIGH (200) # SRAM location
Containing smage Lata
LDI 1230, ZE LOW (200) & Data anvanged
Where Emage resides
RET
KEI
INPUTS & OUTPUTS
LDE RIG. DOFF # Setting ports high
LDI RIG, DOFF # Setting ports high OUT DDRA, RIG #PORTA serves as address
line for 8255 to select
PORT
OUT DORD, RIG # PORTE: Serves as
data line to 8255
OUT DARC, RIG # PORTD: serves as
vite enable for
3255.

_			9
9	Date:	/_	 _
	Page:		
U			

	# MAIN
	MATIN 3
	R1: SBI PORTA, D
	LDI PA, O
	Loof:
	LDI ECTR, 3
	INC PA
	OUT PORTA, PA
	Loop 1:
	LDI RCTR, 32
	LD PB, Z+
	OUT PORTB, PR.
,	ATEC RCTR
	BRNE LOOPI
	DEC CUER
<u>-</u>	BRNE LOOP.
`	22:
`	SBI PORTD, O
`	CSI PORTA, 1
4	LDI PA, O
	LOOP: LIE CCTR, 3.
	INC PA
4	OUT PORTA, PA
	LE.
	LOOPI: LDR RCTR, 32
	LD PB, 2+
	OUT POFTE, PE
	DEC RCTR
	BRNE LOOPI

-		Date://
	. 1898 - 1	rage,
	DEC CCTR	,
	DEC CCTR BRNE LOOP RJMP MATN	1
3	RJMP MAIN	j\$
>		
5		
3		
9		
3		
3		4 ·
		4.5
3		
3		·
3		1
3		
		4
3		
		
7		
		()
		,
, n · ·		