

# Fast Detection of Advanced Persistent Threats for Smart Grids: A Deep Reinforcement Learning Approach

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**Abstract**—Data management systems in smart grids have to address advanced persistent threats (APTs), where malware injection methods are performed by the attacker to launch stealthy attacks and thus steal more data for illegal advantages. In this paper, we present a hierarchical deep reinforcement learning based APT detection scheme for smart grids, which enables the control center of the data management system to choose the APT detection policy to reduce the detection delay and improve the data protection level without knowing the attack model. Based on the state that consists of the size of the gathered power usage data, the priority level of the data, and the detection history, this scheme develops a two-level hierarchical structure to compress the high-dimensional action space and designs four deep dueling networks to accelerate the optimization speed with less over-estimation. Detection performance bound is provided and simulation results show that the proposed scheme improves both the data protection level and the utility of the control center with less detection delay.

**Index Terms**—Smart grids, advanced persistent threat, reinforcement learning

## I. INTRODUCTION

Data management systems in smart grids gather power usage data from data concentrators (DCs) to monitor the system state and deliver the maintenance and control commands [1] against advanced persistent threats (APTs). APT attackers perform the malware injection methods, such as Stuxnet [2], to compromise the target component such as the DCs, with the aim of stealing the power usage data from smart grids without being caught [3]. For example, Black Energy in year 2016 stole the login information from the power system and used it to modify the control center commands that resulted in 700,000 users' power outages.

The detection interval and the central processing units (CPUs) allocation affect the APT detection performance [2], [4], [5]. For example, the two-player game-based APT detection scheme RSP in [4] chooses the detection interval to reach Nash equilibrium of the game with generating the attack-detection interval pairs randomly and thus increase the

network controlling time. However, RSP suffers from a low data protection level against the smart APT attacker, because the attack intervals are rarely known by practical defenders.

The reinforcement learning (RL) based APT detection schemes in [6]–[8] determine the detection interval without relying on the attack interval information. For example, hotbooting policy hill climbing-based APT detection scheme HP as presented in [8] that chooses the detection interval to rapidly detect APTs with the sufficient number of CPUs improves the data protection level, but suffers a high detection delay if the APT attacker launches heavy attacks with more CPUs.

In this paper, we propose a hierarchical deep RL-based APT detection (HDRAM) for the control center of the data management system in smart grids to determine the detection policy consisting of the number of the allocated CPUs, the detection interval assignment, and the CPU allocation. Specifically, this scheme designs a hierarchical structure consisting of the top-level network and the low-level network to compress the high-dimensional action space, reduce the optimization complexity, and thus improve the detection performance. Instead of using deep Q-networks with the convolutional layers, the top-level network applies two dueling networks with one input layer and three fully-connected (FC) layers to output the Q-values of the number of allocated CPUs with the current state. With the chosen number of allocated CPUs and the state as the input, the low-level network including two dueling networks with the same structure as the top-level network outputs the Q-values of the detection interval assignment and the CPU allocation.

We investigate the Nash equilibrium of the APT detection game and the existence condition to provide the detection performance bound. Simulation results based on a three-DC system show that HDRAM improves the detection performance compared with the benchmarks RSP in [4] and HP in [8].

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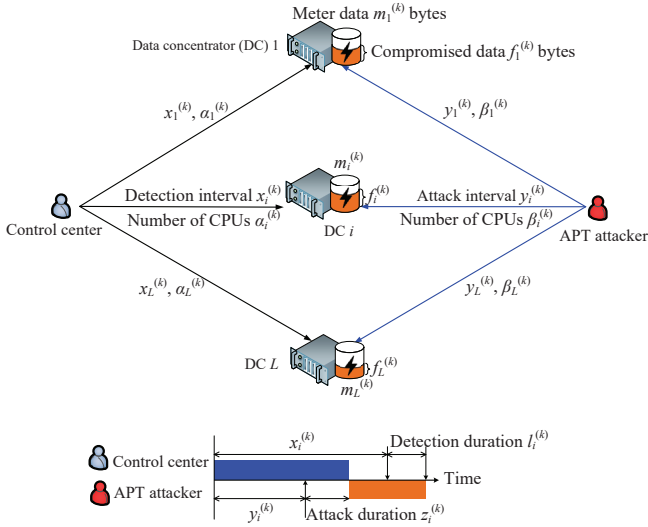


Fig. 1. The control center scans the data and detects DC  $i$  after  $x_i^{(k)}$  time interval with  $\alpha_i^{(k)}$  out of  $b^{(k)}$  CPUs, while the APT attacker uses  $\beta_i^{(k)}$  CPUs to compromise the data at DC  $i$  every  $y_i^{(k)} \in [0, T]$  time interval.

## II. SYSTEM MODEL

In a data management system in smart grids as shown in Fig. 1, a control center manages and coordinates detection tasks assigned to  $L$  DCs against APT attacks. At time slot  $k$ , DC  $i$  with a distributed relational database is assumed to gather and store  $m_i^{(k)}$  bytes power usage data from multiple smart meters for calculating the electricity consumption statistics and monthly billing information. The priority level of the data stored at DC  $i$  is denoted by  $p_i^{(k)} \in \{1, 2, \dots, P\}$ , where  $P$  indicates the highest priority. For example, the power usage data from governments are more critical than the consumers with a higher  $p_i^{(k)}$ .

The control center applies string matching in [9] to scan the data and detects DC  $i$  after  $x_i^{(k)} \in \{nT/N | 0 \leq n \leq N\}$  time interval since the previous detection was finished with  $\alpha_i^{(k)} \in [K, Z]$  out of  $b^{(k)}$  CPUs, where  $T$  is the maximum interval,  $K$  is the minimal number of the CPUs to detect the compromised data in time according to [10] and  $Z$  is the maximal available number of the CPUs. The control center analyzes the system log [11] and matches the data strings to complete the APT detection within the duration denoted by  $l_i^{(k)}$ . The control center recovers the  $f_i^{(k)}$ -byte compromised data with the largest normalized residual method in [12] and restores DC  $i$ .

The APT attacker uses  $\beta_i^{(k)} \in [M, Z]$  CPUs to perform malware injection, e.g., Stuxnet [13], every  $y_i^{(k)} \in [0, T]$  time interval to compromise the data at DC  $i$  based on the greedy algorithm in [3] with the goal of maximizing the amount of the stolen data in each time slot. The attacker knows the network topology and the amount of the data of each DC.

## III. HIERARCHICAL DEEP RL BASED APT DETECTION

We present a hierarchical deep RL-based APT detection (HDRAM) scheme to decrease the detection delay and improve the data protection level and the response speed for the smart grids with a large number of DCs. The two-level hierarchical structure consists of four dueling networks to choose the detection policy. Specifically, the top-level evaluated dueling network (EDN) chooses the number of the allocated CPUs based on the state, while the low-level EDN chooses the detection interval assignment and the CPU allocation to  $L$  DCs based on the top-level EDN output and the state. In addition, the two target dueling networks (TDNs) are designed to update the weights of the two EDNs. Unlike the hierarchical DQN in [14], the dueling network architecture replaces the convolutional layers with FC layers as shown in Fig. 2 to extract the detection features and thus accelerate the policy learning speed.

At time slot  $k$ , the state  $s^{(k)}$  consists of the size of the gathered power usage data  $m_i^{(k)}$ , the priority level of the data stored at DC  $i$   $p_i^{(k)}$ , the size of the previous compromised data  $f_i^{(k-1)}$  with  $1 \leq i \leq L$ , and the previous detection delay  $D^{(k-1)}$ , i.e.,

$$s^{(k)} = \left[ \left[ m_i^{(k)}, p_i^{(k)}, f_i^{(k-1)} \right]_{1 \leq i \leq L}, D^{(k-1)} \right]. \quad (1)$$

The state is input to the top-level EDN that consists of an input layer (with  $3L + 1$  units), three FC layers and an aggregating module. The first FC layer in the top-level EDN has  $g_1$  units that uses the matrix multiplication to extract the state features. The second FC layer with  $2g_2$  units branches off into a value stream with weights  $\omega_1$  and an advantage stream with weights  $\omega_2$ , both of which have  $g_2$  units. The leaky rectified linear unit is chosen as the activation function. Similarly, the final FC layer with  $Z - K + 2$  units outputs the state value  $V(s^{(k)}; \omega_1)$  in the value stream, and the advantage stream outputs the estimated advantages  $A(s^{(k)}, \cdot; \omega_2)$  of the  $Z - K + 1$  feasible number of the allocated CPUs. The aggregating module estimates the Q-values given by

$$Q_1(s^{(k)}, \cdot; \omega) = V(s^{(k)}; \omega_1) + A(s^{(k)}, \cdot; \omega_2) - \frac{1}{Z - K + 1} \sum_{b' \in [K, Z]} A(s^{(k)}, b'; \omega_2), \quad (2)$$

which is the basis to choose the number of allocated CPUs  $b$  via the  $\epsilon$ -greedy algorithm to balance the exploration and exploitation.

The low-level EDN with weights  $\sigma$  inputs both  $b$  and  $s^{(k)}$ , and has the similar structure with the top-level EDN. The three FC layers have  $d_1$ ,  $2d_2$  and  $N^L Z^L + 1$  units, respectively. Based on the value stream with weights  $\sigma_1$  and the advantage stream with weights  $\sigma_2$ , the low-level EDN

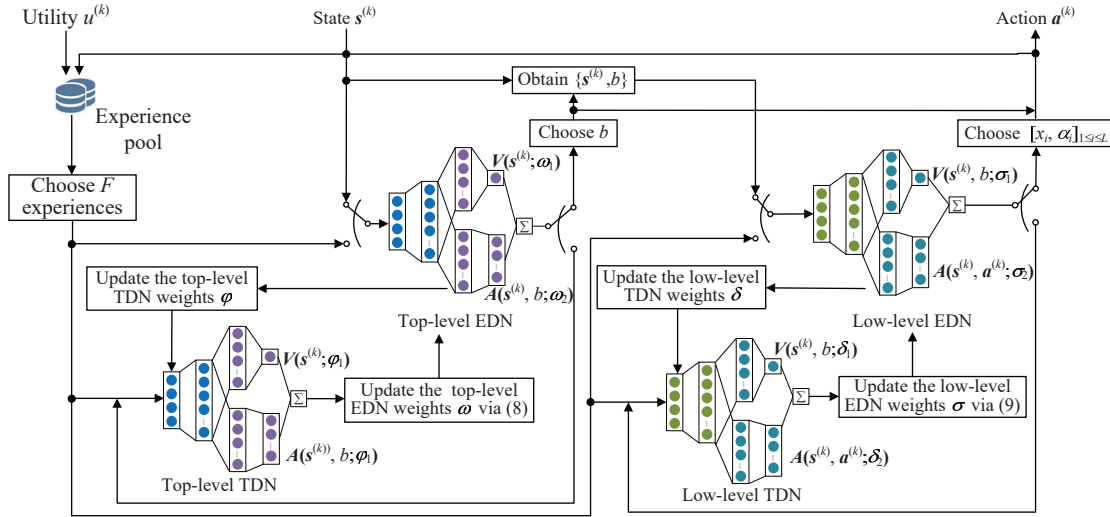


Fig. 2. Illustration of the hierarchical deep RL-based APT detection scheme.

outputs the estimated Q-values as

$$Q_2(s^{(k)}, \cdot; \sigma) = V(s^{(k)}, b; \sigma_1) + A(s^{(k)}, b, \cdot; \sigma_2) - \frac{1}{|\Delta|} \sum_{\chi' \in [K, Z]} A(s^{(k)}, b, \chi'; \omega_2), \quad (3)$$

which is the basis to choose the detection intervals and the allocated CPUs allocated to  $L$  DCs, i.e.,  $\chi = [x_i, \alpha_i]_{1 \leq i \leq L}$  via the  $\epsilon$ -greedy algorithm from the feasible set  $\Delta$  given by

$$\Delta = \left\{ [x_i, \alpha_i]_{1 \leq i \leq L} \mid 0 \leq x_i \leq T; K \leq \alpha_i \leq Z; \sum_{i=1}^L \alpha_i \leq b \right\}. \quad (4)$$

The detection policy  $\mathbf{a}^{(k)}$  consists of the number of allocated CPUs  $b$ , the detection interval assignment  $x_i$ , and the CPU allocation  $\alpha_i$  with  $1 \leq i \leq L$ , i.e.,

$$\mathbf{a}^{(k)} = [b, [x_i, \alpha_i]_{1 \leq i \leq L}] \in \Lambda, \quad (5)$$

where  $\Lambda$  is the action set that contains  $(N+1)^L(Z-K+1)^{L+1}$  feasible policies. The control center detects the  $L$  DCs according to the chosen policy  $\mathbf{a}^{(k)}$ .

The utility  $u^{(k)}$  is calculated as

$$u^{(k)} = \sum_{i=1}^L (\mu e^{-f_i} - x_i - l_i) + c_1 \sum_{i=1}^L x_i - c_2 \sum_{i=1}^L f_i p_i - c_3 b, \quad (6)$$

where  $c_1$ ,  $c_2$  and  $c_3$  represent the importance of the saving energy consumption of scanning, the compromised data, and the CPU scheduling overhead in the APT detection process.

The detection experiences  $\{s^{(k)}, \mathbf{a}^{(k)}, u^{(k)}\}$  are stored in the experience pool  $\mathcal{D}$ , from which  $F$  experiences are randomly chosen to build a minibatch  $[e^{(n_j)}]_{1 \leq j \leq F}$ , where

$n_j \sim U(1, k)$ . As shown in Algorithm 1, the minibatch and two additional TDNs are used to update the weights of the two EDNs: The weights of the top-level EDN denoted by  $\omega$  are updated via stochastic gradient descent (SGD) as

$$\omega \leftarrow \arg \min_{\omega'} \frac{1}{F} \sum_{j=1}^F \left( u^{(n_j)} - Q_1(s^{(n_j)}, b^{(n_j)}; \omega') \right) + \gamma Q_1 \left( s^{(n_j+1)}, \arg \max_{b' \in [K, Z]} Q_1(s^{(n_j+1)}, b'; \omega'); \varphi \right)^2, \quad (7)$$

where  $\gamma \in [0, 1]$  is a discount factor and the weights of the low-level EDN denoted by  $\sigma$  are updated via SGD as (8).

The top-level TDN with weights  $\varphi$  estimates the target Q-value of the number of allocated CPUs  $b$  that maximizes the estimated Q-value. The top-level TDN copies the the top-level EDN weights  $\omega$  every  $C$  steps to update its weights  $\varphi$  and keeps fixed on other steps. Similarly, the low-level TDN with weights  $\delta$  copies the the low-level EDN weights  $\sigma$  every  $C$  steps.

#### IV. PERFORMANCE EVALUATION

In the APT detection game, the control center chooses the number of CPUs  $b \in [K, Z]$ , the detection interval  $x_i \in [0, T]$  and the CPU allocation  $\alpha_i \in [K, Z]$  with  $1 \leq i \leq L$  to maximize the utility given (6), while the APT attacker determines the number of CPUs  $\beta_i \in [M, Z]$  and the attack interval  $y_i \in [0, T]$  to maximize its utility  $u_A$  given by

$$u_A = -u + c_4 \sum_{i=1}^L y_i - c_5 \sum_{i=1}^L \beta_i. \quad (9)$$

$$\sigma \leftarrow \arg \min_{\sigma'} \frac{1}{F} \sum_{j=1}^F \left( u^{(n_j)} - Q_2 \left( s^{(n_j)}, a^{(n_j)}; \sigma' \right) + \gamma Q_2 \left( s^{(n_j+1)}, \arg \max_{\chi \in \Delta} Q_2 \left( s^{(n_j+1)}, b^{(n_j+1)}, \chi; \sigma' \right); \delta \right) \right)^2 \quad (8)$$

**Algorithm 1** Hierarchical Deep RL based APT detection scheme (HDRAM)

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1: Initialize  $L, C, F, \mu, [f_i]_{1 \leq i \leq L} = \mathbf{0}, \omega, \sigma, \varphi, \delta$  and  $\mathcal{D} = \emptyset$ 
2: for  $k = 1, 2, \dots$  do
3:   Obtain  $[m_i]_{1 \leq i \leq L}$  from  $L$  DCs
4:   Evaluate  $[p_i]_{1 \leq i \leq L}$  and  $[l_i]_{1 \leq i \leq L}$ 
5:   Obtain  $s^{(k)}$  via (1)
6:   Input  $s^{(k)}$  to the top-level EDN
7:   Obtain the top-level EDN outputs  $Q_1(s^{(k)}, b; \omega)$ 
8:   Choose  $b$  with  $\epsilon$ -greedy
9:   Input  $\{s^{(k)}, b\}$  to the low-level EDN
10:  Obtain the low-level EDN outputs  $Q_2(s^{(k)}, a^{(k)}; \sigma)$ 
11:  Choose  $[x_i, \alpha_i]_{1 \leq i \leq L}$  with  $\epsilon$ -greedy
12:  Scan the data in DC  $i$  within  $x_i$  time interval
13:  Detect DC  $i$  with  $\alpha_i$  out of  $b$  CPUs
14:  Recover  $f_i$ -byte compromised data in DC  $i$ 
15:  Calculate  $u^{(k)}$  via (6)
16:  Store  $\{s^{(k)}, a^{(k)}, u^{(k)}\}$  in the experience pool  $\mathcal{D}$ 
17:  Sample  $F$  experiences from  $\mathcal{D}$ 
18:  for  $j = 1, 2, \dots, F$  do
19:     $e^{(n_j)} = \mathcal{D}(n_j)$ 
20:  end for
21:  Update  $\omega$  via (7)
22:  Update  $\sigma$  via (8)
23:  if  $k \bmod C = 0$  then
24:    Update  $\varphi$  with  $\omega$ 
25:    Update  $\delta$  with  $\sigma$ 
26:  end if
27: end for

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Similar to [15], the detection delay  $D$  is modeled by

$$D = \sum_{i=1}^L \left( x_i + \frac{X}{\alpha_i} - y_i - \frac{Y}{\beta_i} \right), \quad (10)$$

where  $X$  and  $Y$  denote the total required CPU time for the serial processing to complete a detection and an APT attack, respectively. According to [16], the data protection level  $B$  is given by

$$B = \frac{1}{L} \sum_{i=1}^L \min \left\{ \frac{y_i + Y\beta_i^{-1}}{x_i + X\alpha_i^{-1}}, 1 \right\}. \quad (11)$$

The size of compromised data that depends on the data protection level is modeled as  $f_i = Bm_i$  according to [17].

By (6), (10), and (11), the utility function is given by

$$u = c_1 \sum_{i=1}^L x_i + \frac{c_2}{L} \sum_{i=1}^L m_i p_i \min \left\{ \frac{y_i + Y\beta_i^{-1}}{x_i + X\alpha_i^{-1}}, 1 \right\} - c_3 b - \sum_{i=1}^L \left( x_i + \frac{X}{\alpha_i} - y_i - \frac{Y}{\beta_i} \right). \quad (12)$$

**Theorem 1.** The performance bound of the proposed hierarchical deep RL-based APT detection algorithm with  $L = 1$  is given by

$$u \leq c_1 T + c_2 m_1 p_1 + c_3 K - \frac{X}{K} \quad (13)$$

$$D \leq \frac{X}{K} - \frac{Y}{M} \quad (14)$$

$$B \leq 1 \quad (15)$$

if

$$Y \leq \min \left\{ ZT + \frac{ZX}{K}, c_5 M^2 \right\} \quad (16)$$

$$\min \{c_1, c_4\} \geq 1 \quad (17)$$

$$X \leq c_3 K^2 \quad (18)$$

*Proof.* For given  $L = 1$ , we have  $b = \alpha_1$ . By (12), if (16) holds, we have

$$\min \left\{ \frac{y_1 + Y\beta_1^{-1}}{x_1 + X\alpha_1^{-1}}, 1 \right\} = 1. \quad (19)$$

By (12) and (19), thus we have

$$u([x_1, \alpha_1], [y_1, \beta_1]) = (c_1 - 1)x_1 + c_2 m_1 p_1 - c_3 \alpha_1 - \frac{X}{\alpha_1} + y_1 + \frac{Y}{\beta_1}. \quad (20)$$

Let

$$q_1(x_1) = (c_1 - 1)x_1 + T + c_2 m_1 p_1, \quad (21)$$

and  $\forall x_1 \in [0, T]$ , if (17) holds, we have

$$\frac{dq_1(x_1)}{dx_1} = c_1 - 1 \geq 0. \quad (22)$$

Thus,  $q_1(x_1)$  is maximized at  $x_1^* = T$ . Let

$$q_2(\alpha_1) = \frac{Y}{M} - c_3 \alpha_1 - \frac{X}{\alpha_1}, \quad (23)$$

and  $\forall \alpha_1 \in [K, Z]$ , if (18) holds, we have

$$\begin{aligned} q_2(K) &= -c_3K - \frac{X}{K} \\ &\geq -c_3\alpha_1 - \frac{X}{\alpha_1} = q_2(\alpha_1). \end{aligned} \quad (24)$$

Thus,  $q_2(\alpha_1)$  is maximized at  $\alpha_1^* = K$ . By (20), (21), and (23), we have

$$u([x_1, \alpha_1], [T, M]) = q_1(x_1) + q_2(\alpha_1). \quad (25)$$

As  $x_1$  is independent with  $\alpha_1$ ,  $\forall x_1 \in [0, T]$  and  $\forall \alpha_1 \in [K, Z]$ , we have

$$\begin{aligned} u([T, K], [T, M]) \\ &= c_1T - c_3K - \frac{X}{K} + \frac{Y}{M} + c_2m_1p_1 \end{aligned} \quad (26)$$

$$\geq (c_1 - 1)x_1 - c_3\alpha_1 - \frac{Y}{\alpha_1} + T + \frac{X}{M} + c_2m_1p_1 \quad (27)$$

$$= u([x_1, \alpha_1], [T, M]). \quad (28)$$

Let

$$q_3(y_1) = (c_4 - 1)y_1 + (1 - c_1)T - c_2m_1p_1, \quad (29)$$

and  $\forall y_1 \in [0, T]$ , if (17) holds, we have

$$\frac{dq_3(y_1)}{dy_1} = c_4 - 1 \geq 0. \quad (30)$$

Thus,  $q_3(y_1)$  is maximized at  $y_1^* = T$ . Let

$$q_4(\beta_1) = c_3K + \frac{X}{K} - c_5\beta_1 - \frac{Y}{\beta_1}. \quad (31)$$

and  $\forall \beta_1 \in [M, Z]$ , if (16) holds, we have

$$\begin{aligned} q_4(M) &= c_3K + \frac{X}{K} - c_5M - \frac{Y}{M} \\ &\geq c_3K + \frac{X}{K} - c_5\beta_1 - \frac{Y}{\beta_1} = q_4(\beta_1). \end{aligned} \quad (32)$$

Thus,  $q_4(\beta_1)$  is maximized at  $\beta_1^* = M$ . By (9), (29) and (31), we have

$$u_A([T, K], [y_1, \beta_1]) = q_3(y_1) + q_4(\beta_1). \quad (33)$$

As  $y_1$  is independent with  $\beta_1$ ,  $\forall y_1 \in [0, T]$  and  $\forall \beta_1 \in [M, Z]$ , we have

$$\begin{aligned} u_A([T, K], [T, M]) \\ &= (c_4 - c_1)T - c_5M - \frac{Y}{M} + c_3K + \frac{X}{K} - c_2m_1p_1 \end{aligned} \quad (34)$$

$$\begin{aligned} &\geq (c_4 - 1)y_1 - c_5\beta_1 - \frac{Y}{\beta_1} + (1 - c_1)T \\ &\quad + c_3K + \frac{X}{K} - c_2m_1p_1 \end{aligned} \quad (35)$$

$$= u_A([T, K], [y_1, \beta_1]). \quad (36)$$

Thus, by (26), (28), (34), and (36), we have a Nash equilibrium given by  $([T, K], [T, M])$  and the performance

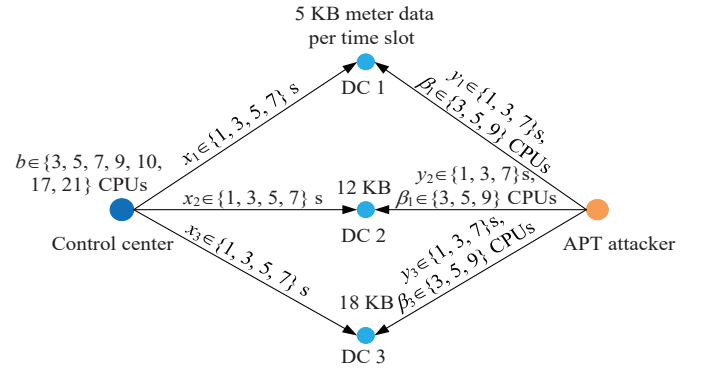


Fig. 3. Simulation topology of the data management system with three DCs in the smart grid.

bound of the HDRAD is given by (13) - (15).  $\square$

**Remark 1:** The control center uses the longest APT detection interval under high energy consumption given by (17). As shown in (18), if the total required CPU time to complete a detection  $X$  is less than the bound that relies on the number of CPUs meeting the basic security requirement  $K$ , the control center completes the APT detection with the minimum number of CPUs.

## V. SIMULATION RESULTS

Simulations were performed for a data management system as shown in Fig. 3 to evaluate the performance of the HDRAD scheme. Three DCs gather  $\{5, 12, 18\}$  kilobytes (KB) power usage data from smart meters in each 10-second time slot [18]. The priority level of the data stored in the three DCs is chosen from  $\{1, 2, 3, 4, 5, 6\}$ . The control center chooses the detection interval from  $\{1, 3, 5, 7\}$  seconds and allocates  $\{3, 5, 7, 9, 10, 17, 21\}$  CPUs to detect APT attacks. The APT attacker applies the greedy algorithm in [3] to choose the attack interval from  $\{1, 3, 7\}$  seconds and allocates  $\{3, 5, 9\}$  CPUs in each DC.

As illustrated in Fig. 4, HDRAD achieves the optimal detection policy, and reduces 15.4% detection delay from 4.7 s to 0.3 s, and improves 15.4% data protection level from 84.1% to 99.4%, and increases 48.3% utility from -41.2 to 0.9 after 1250 time slots. This scheme outperforms RSP in [4] by decreasing 94.1% detection delay and improving 18.9% data protection level and 49.7% utility of the control center. By designing the hierarchical structure to accelerate the exploration speed, HDRAD also exceeds HP in [8] by reducing 87.2% detection delay, improving 11.3% data protection level, and increasing 30.4% utility of the control center.



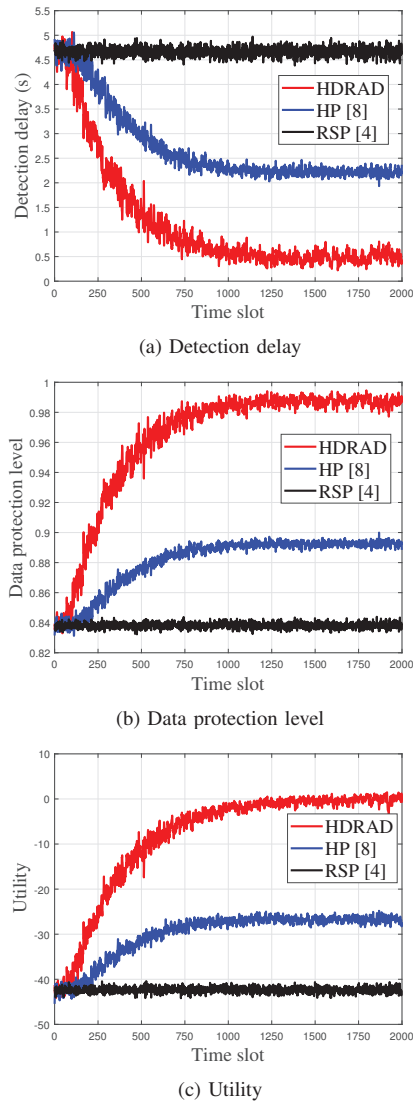


Fig. 4. Performance of the APT detection schemes in the three-DC data management system as shown in Fig. 3.

## VI. CONCLUSION

In this paper, we have proposed a hierarchical deep RL based APT detection scheme for the control center in smart grids to choose the number of the allocated CPUs, the detection interval assignment, and the CPU allocation. The proposed scheme designs a hierarchical structure to optimize the detection performance without requiring the knowledge of the attack model. In the simulation based on a three-DC system, the HDRAD outperforms both the benchmarks RSP in [4] and HP in [8]. For example, the HDRAD decreases 87.2% detection delay, improves 11.3% data protection level, and increases 30.4% utility of the control center after 1250 time slots compared with HP.

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