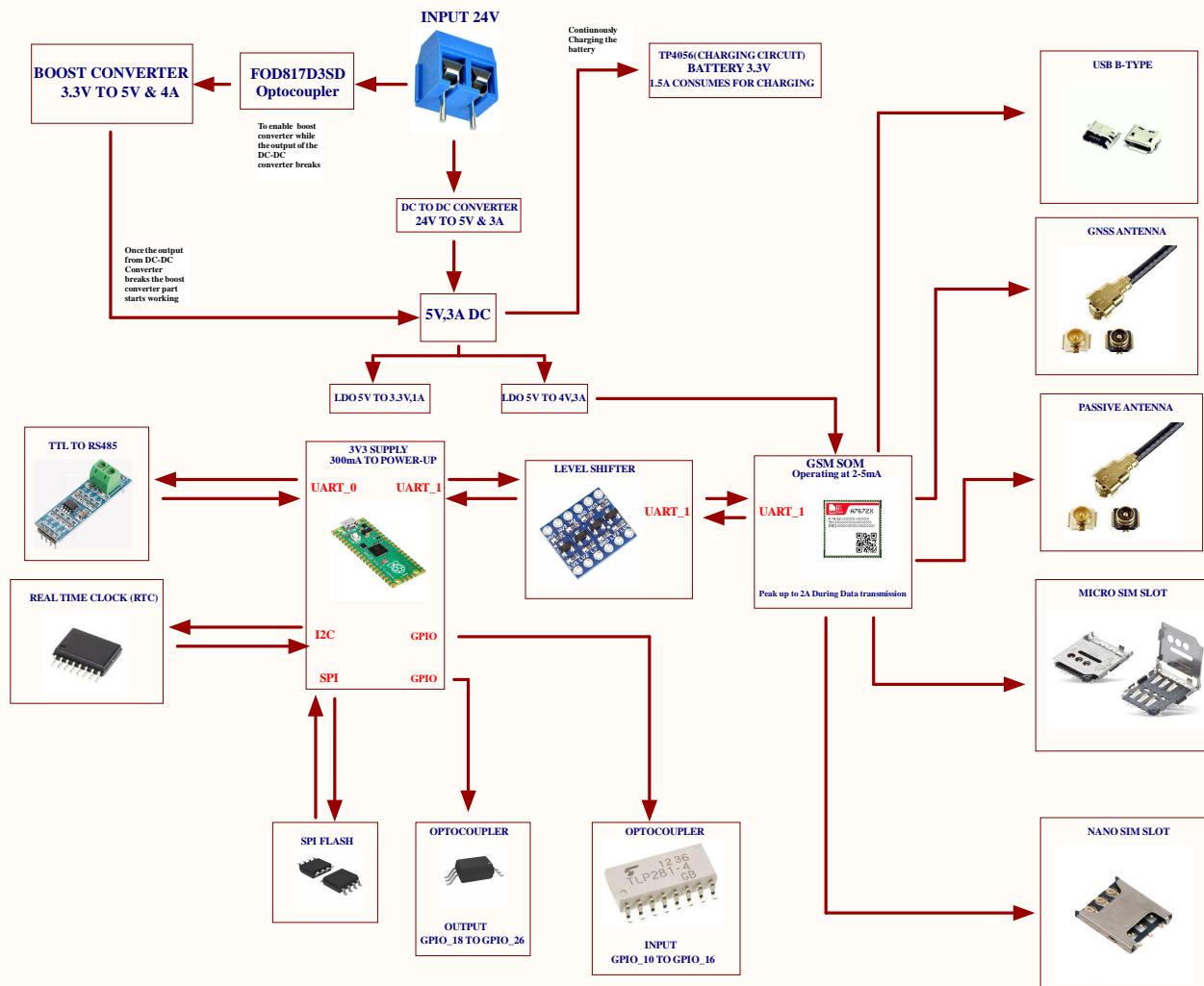
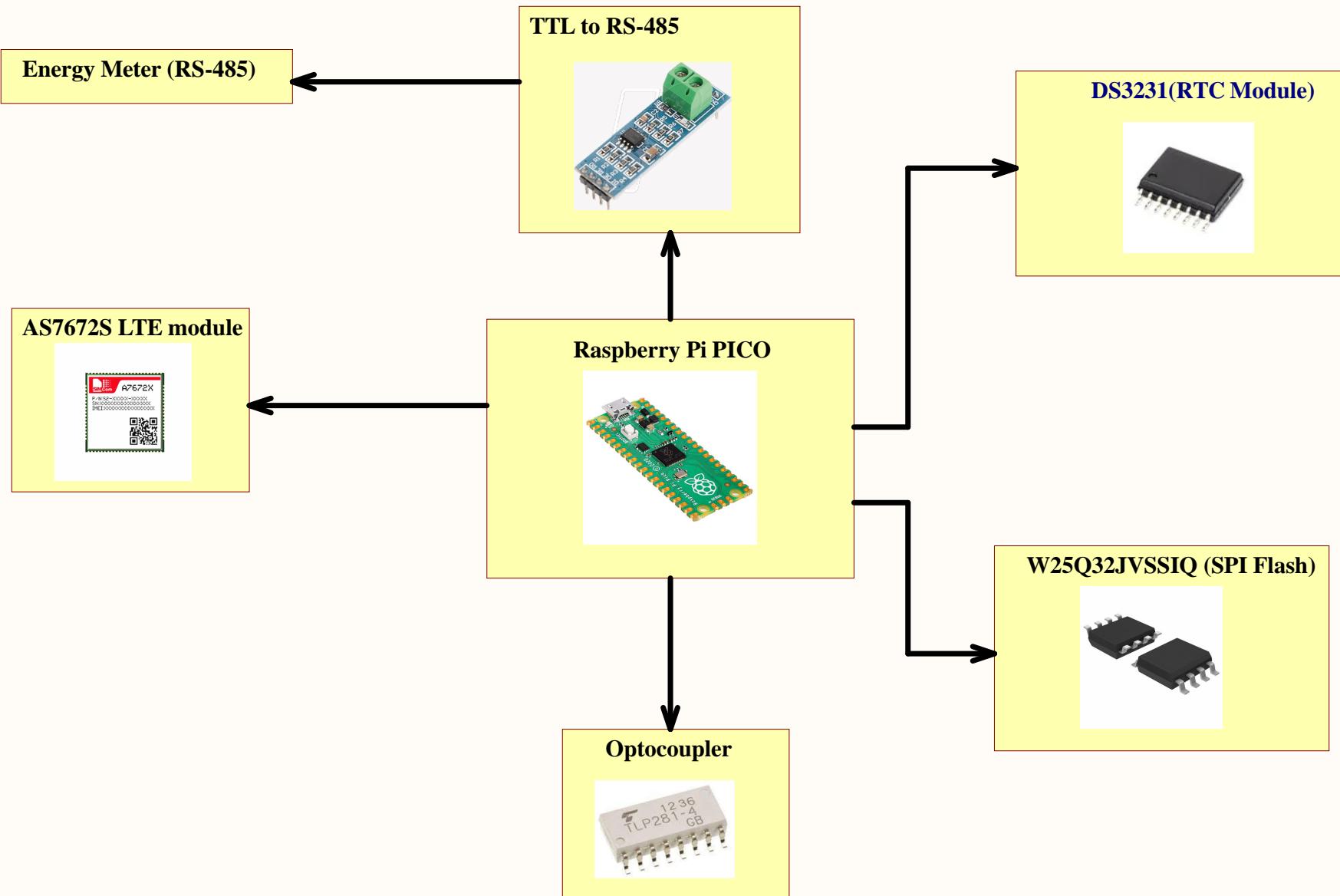


# BLOCK DIAGRAM



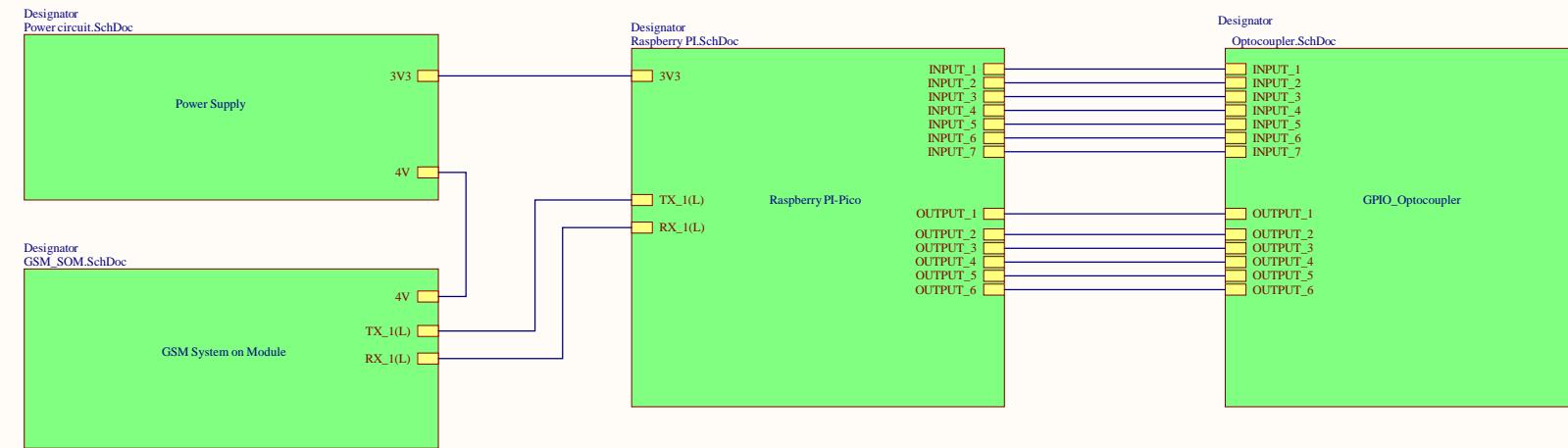
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Size	Number	Revision
A2		
Date: 6-14-2025	Sheet of	
File: C:\Users\...\Block.SchDoc		Drawn By:

## Block Diagram



Title		
Size	Number	Revision
A3		
Date: 6-14-2025	Sheet of	
File: C:\Users\...\GSM Module.SchDoc	Drawn By:	

## Block Diagram



A

A

B

B

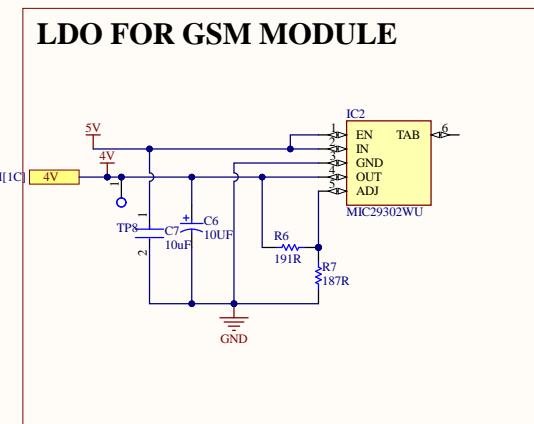
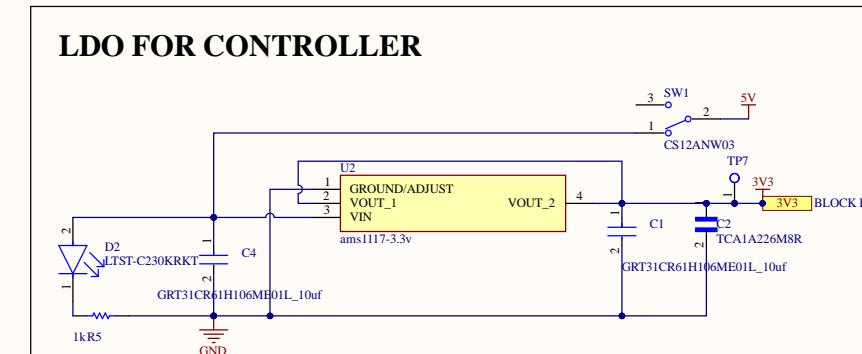
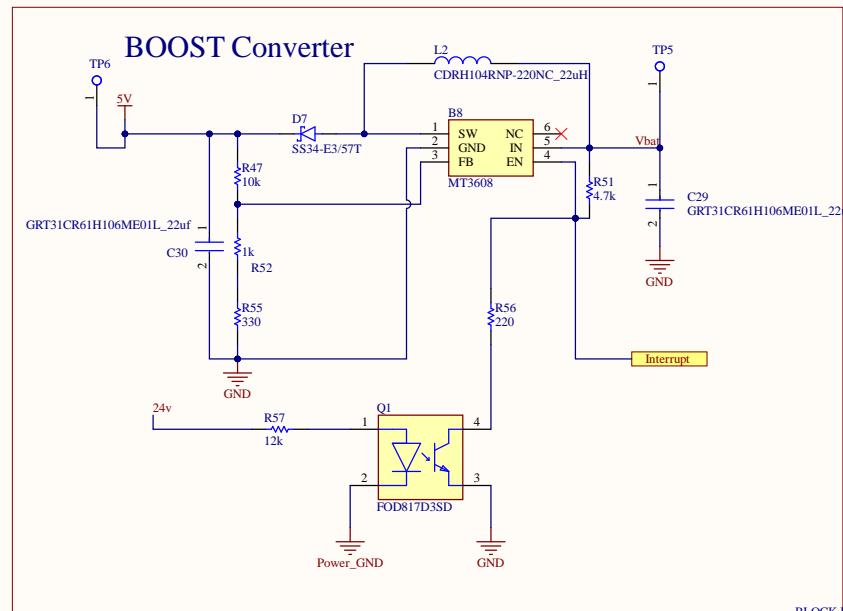
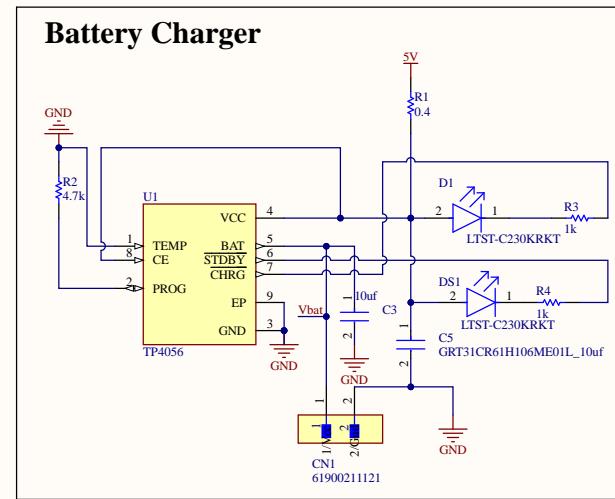
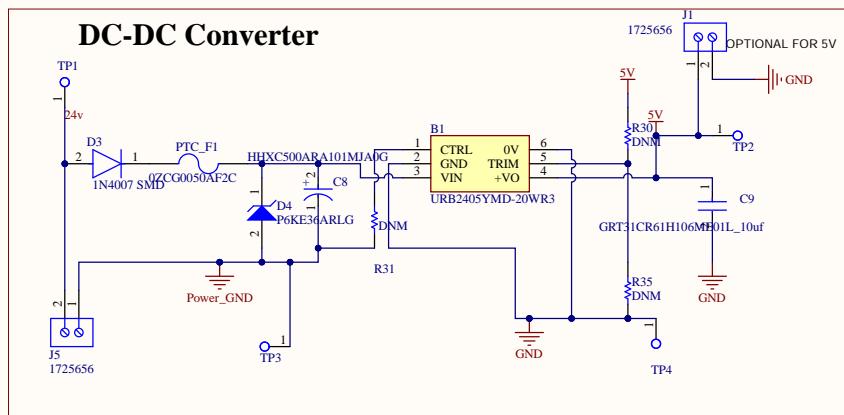
C

C

D

D

## **POWER SUPPLY**



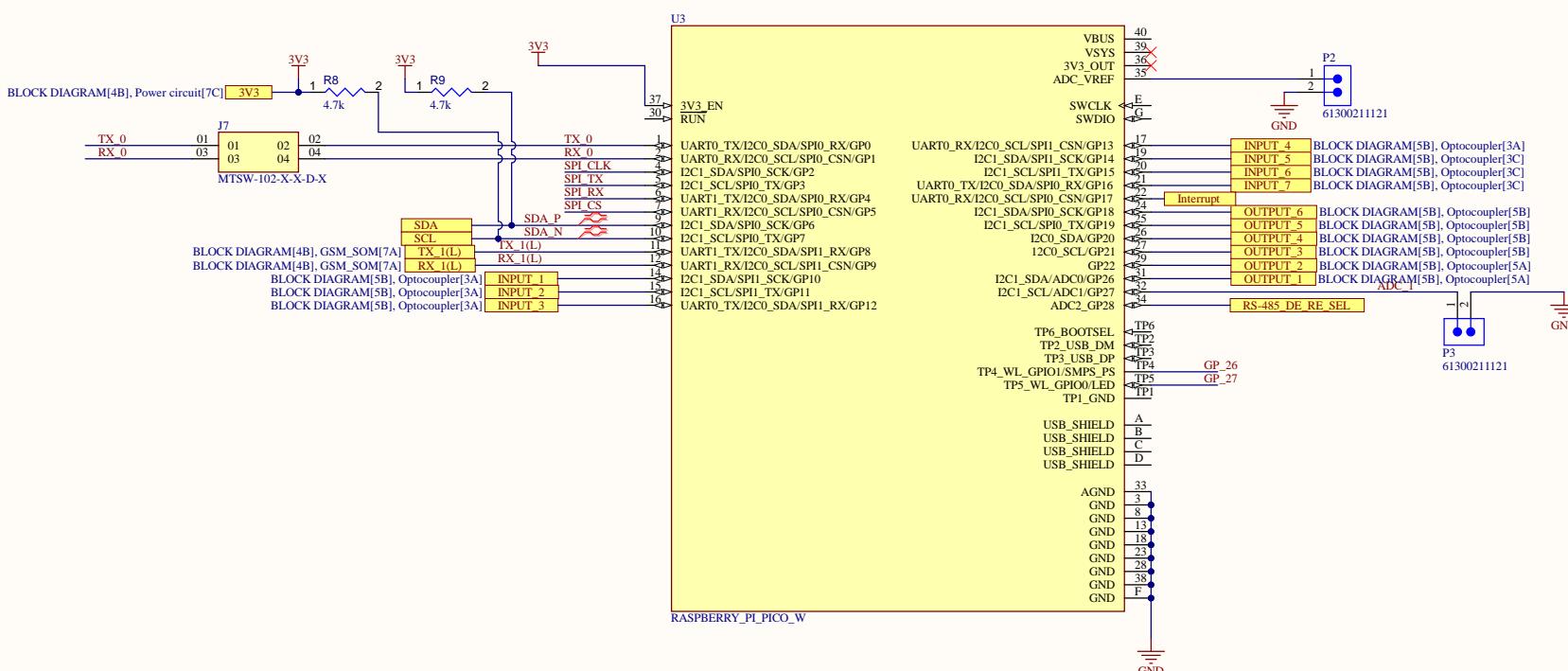
TESTPOINT

TP1 - 24V  
TP2 - 5V  
TP3 - Power\_GND  
TP4 - GND  
TP5 - Vbat  
TP6 - 5V  
TP7 - 3V3  
TP8 - 4V

Title		Revision
Size A3	Number	
Date: 6-14-2025		Sheet of
File: C:\Users\_\Power circuit.SchDoc		Drawn By:

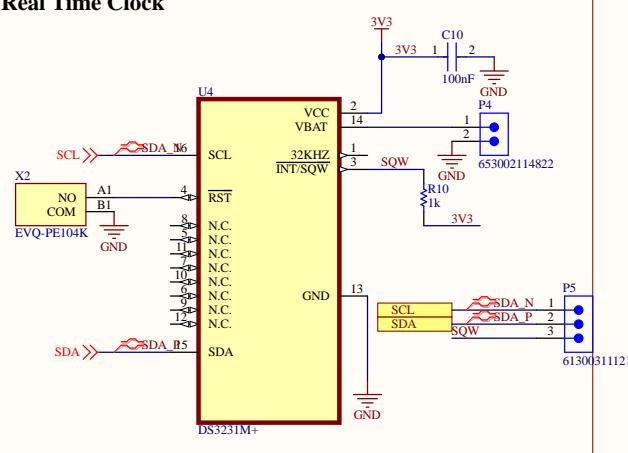
# RASPBERRY\_PI\_PICO\_W

A



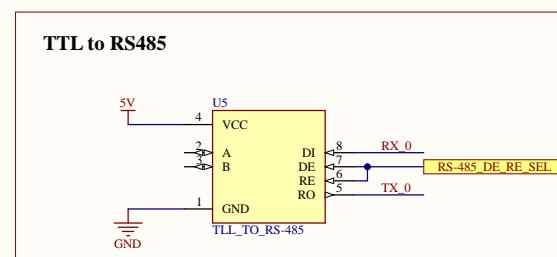
A

## Real Time Clock

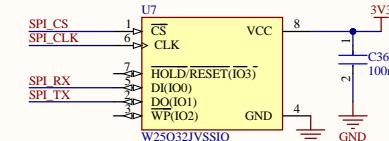


C

## TTL to RS485

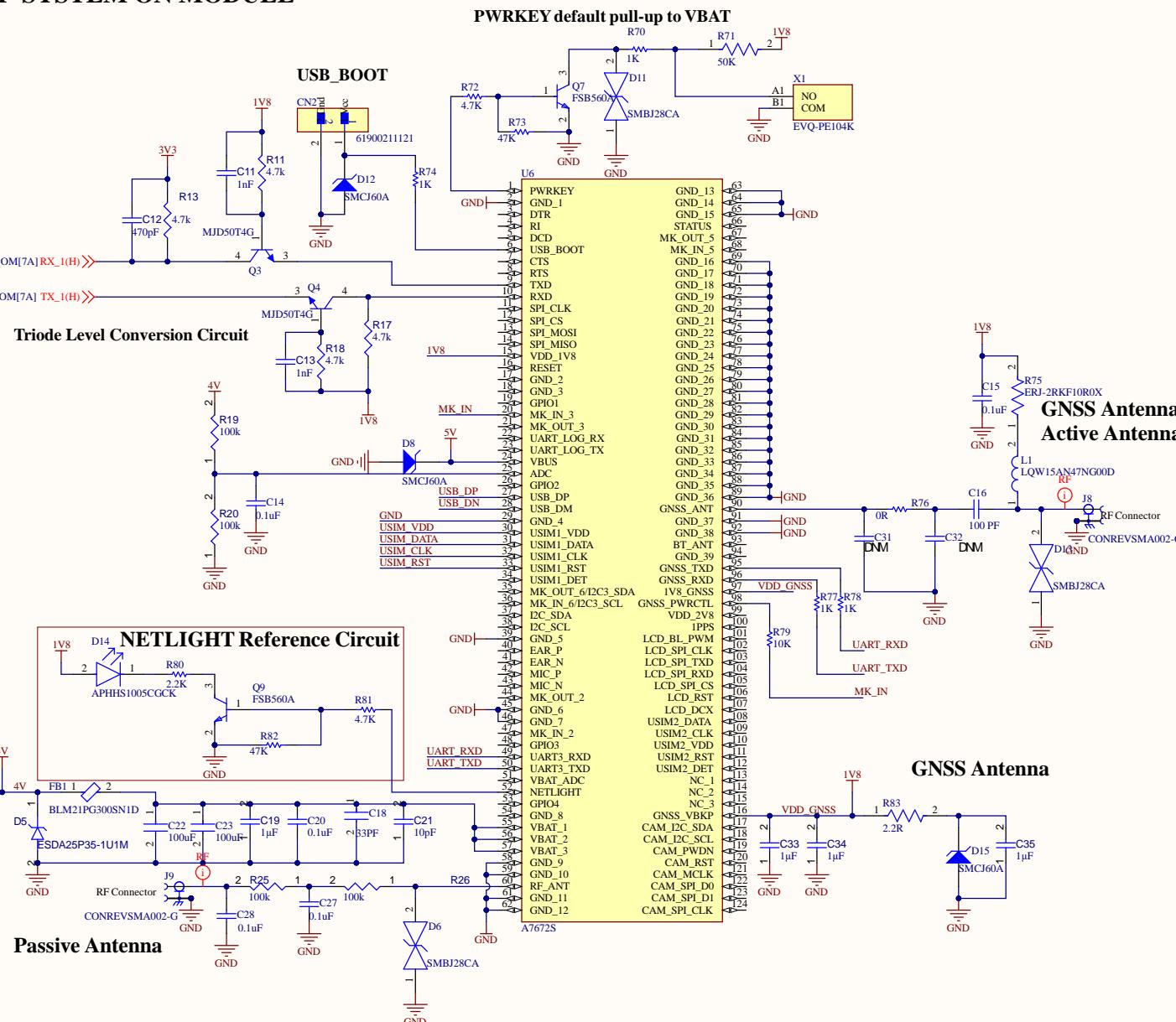


## SPI Flash



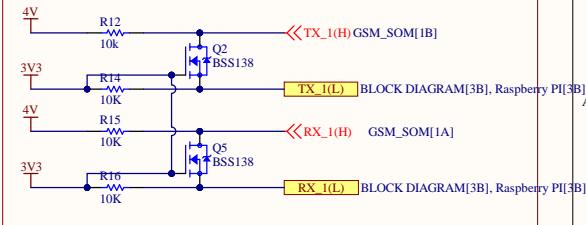
Title		
Size	Number	Revision
A3		
Date: 6-14-2025	Sheet of	
File: C:\Users\.\Raspberry PI.SchDoc		Drawn By:

# GSM -SYSTEM ON MODULE

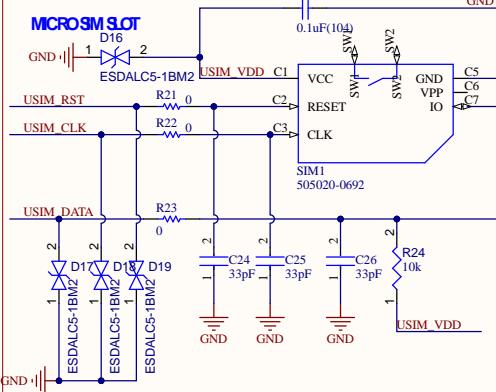


TESTPOINTS :  
TP9,TP10- USB  
REFERENCE DESIGN  
TP11 -GND

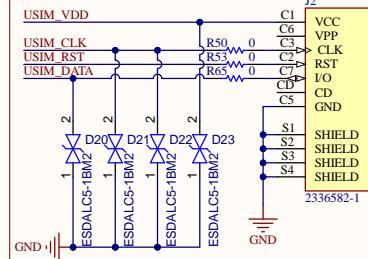
## LEVEL SHIFTER



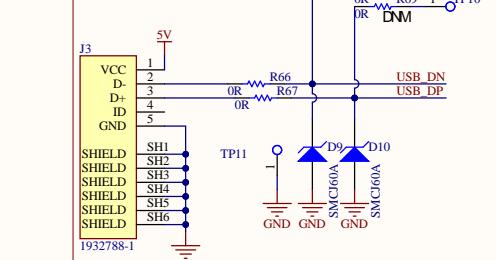
## SIM CARD CONNECTOR



## NANOSIM SLOT



## USB CIRCUIT DIAGRAM

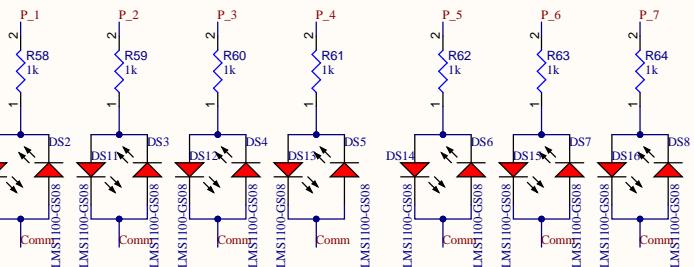
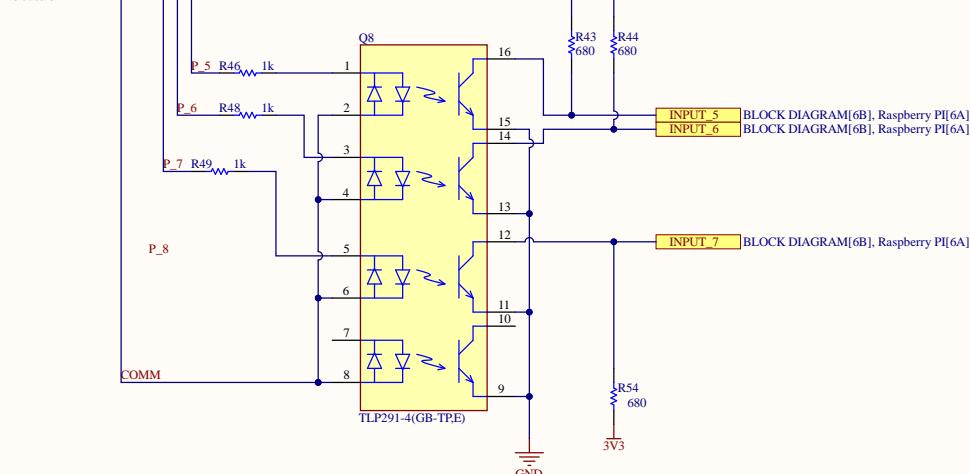
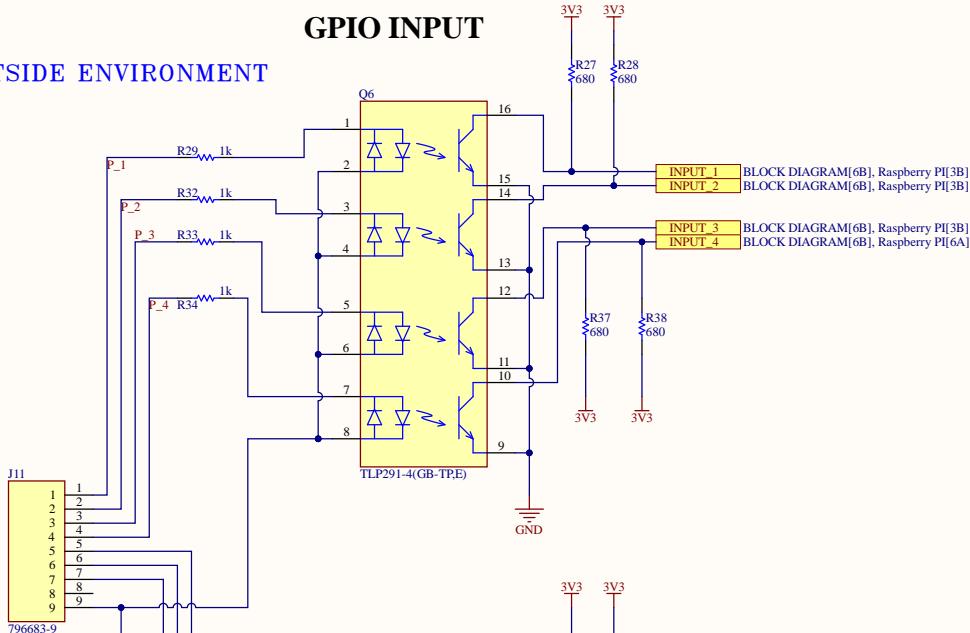


Title		Number	Revision
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File: C:\Users\...\GSM_SOM.SchDoc		Drawn By:	

# GPIO\_OPTOCOUPLER

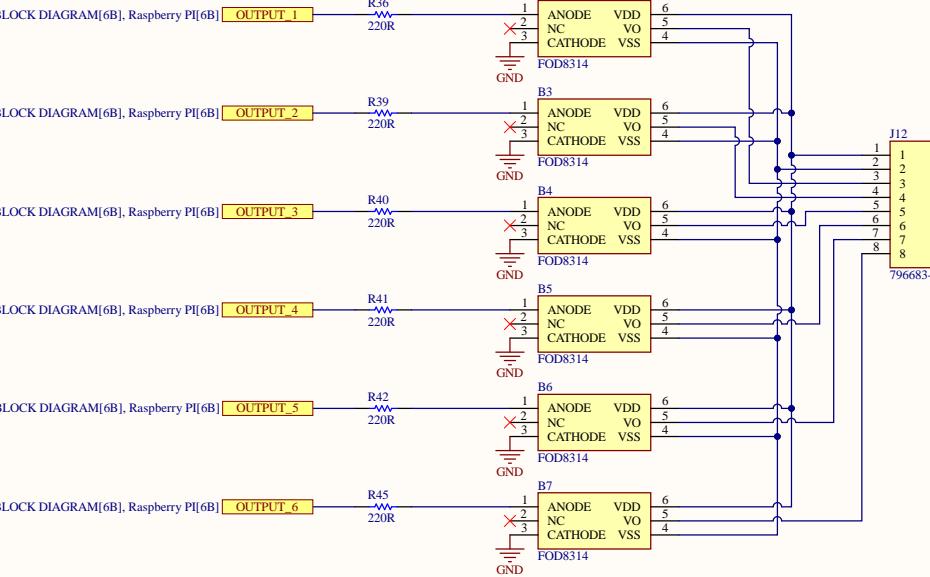
## GPIO INPUT

**OUTSIDE ENVIRONMENT**



## GPIO OUTPUT

**OUTSIDE ENVIRONMENT**



Title		
Size	Number	Revision
A3		
Date:	6-14-2025	Sheet of
File:	C:\Users\...\Optocoupler.SchDoc	Drawn By:

