```
champsim::operable
  + CLOCK SCALE
  + leap operation
  + current cycle
  + operable()
  + operate()
  + operate()
  + print deadlock()
        O3 CPU
+ cpu
+ instr unique id
+ completed executions
+ begin sim cycle
+ begin sim instr
+ last_sim_cycle
+ last sim instr
+ finish sim cycle
+ finish sim instr
+ instrs to read this
cycle
and 46 more...
+ operate()
+ init instruction()
+ check dib()
+ translate fetch()
+ fetch instruction()
+ promote to decode()
+ decode instruction()
+ dispatch instruction()
+ schedule instruction()
+ execute instruction()
and 27 more...
```