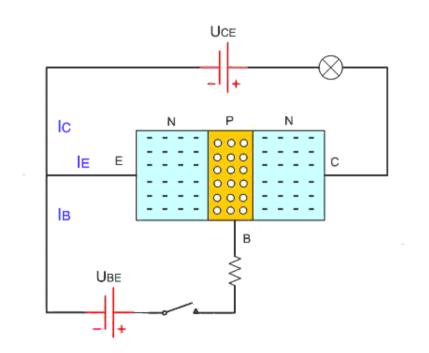


# jee LIVE daily

# Semiconductors

Lecture - 3

**Transistor** 





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B.Tech, **IIT Bombay** 

IIT-JEE AIR - 161

9+ Years of Teaching Experience at

3 Coaching Institutes (multiple under 15 AIRs)

In top 1% INPhO





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Three charges of (+2q), (-q) and (-q) are placed at the corner A, B and C of an equilateral triangle of side a as shown in the adjoining figure.

Then the dipole moment of this combination is

B. Zero

C. qa/3

D.  $\frac{2}{\sqrt{3}}$  qa

Pragual Tirvelli C

Rocky Roy pined

Padam Stirta Yo

Rocky Roy Roy belo so

Frainam Adit Vedor add

Sheshu B: C

Vivex Singh c

Pragham Adit C

rishach yadav pined



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# **PLUS** Courses



HINDI PHYSICS

Course on Center of Mass, Impulse & Collisions for Droppers

Lesson 8 - Today, 8:30 PM

Namo Kaul



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Lesson 32 • Oct 20, 9:00 AM

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Lesson 31 • Oct 20, 11:00 AM Jayant Nagda



HINDI BATCHES AND YEAR LONG CO...

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Lesson 36 • Today, 3:15 PM

Megha Khandelwal



HINDI BATCHES AND YEAR LONG CO ...

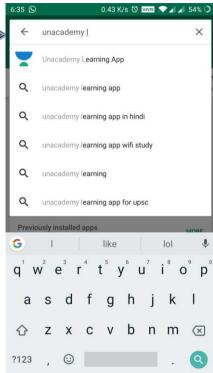
EVOLVE for Class 12 (JEE Main/Advanced 2020) - October '19

Lesson 46 · Today, 10:00 PM

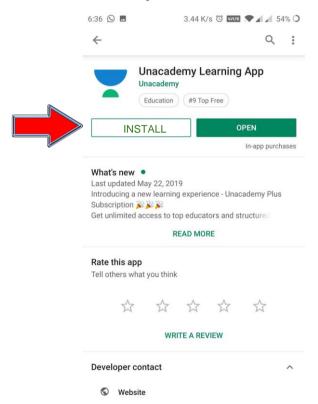
Kailash Sharma

# Step 1

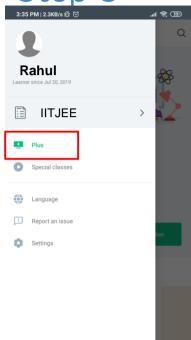




# Step 2



Step 3

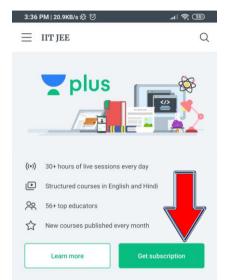


Step 4 3:35 PM | 0.7KB/s Ær ੴ X Choose your goal Recent Foundation & NTSE Foundation - Class X CA Exams IIT JEE Featured ☐ UPSC CSE IIT JEE NEET & AIIMS SSC Exams CAT NTA-UGC NET Bank Exams CA Exams IELTS MPPSC/VYAPAM

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# Step 5



# Step 6



# Step 7



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Proceed to Payment

#### **Transistor**





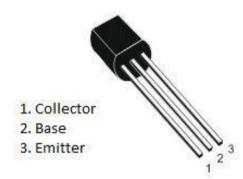
It is a three terminal Semiconductor Device

Transistor: Transfer + Resistor

Current is transferred from Low Resistance to High Resistance circuit

Transistor is used for rectification, amplification, oscillation etc.

#### **Transistor**



(moderale) jee 1. Emitter: Heavily Doped crystal supplies/emits majority charge carriers. Moderate Size: thinner than collector and thicker than base

Collector

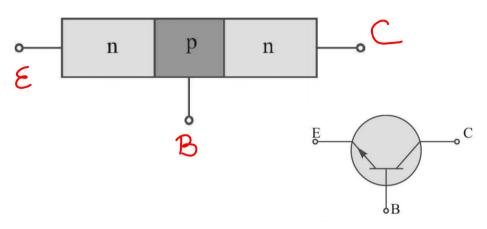
Emitter

- Base: Lightly Doped crystal (Light) Base and made very thin. It passes most of the emitter injected charge carriers to the collector.
- Collector: Moderately Doped crystal thickest in size. Collects/removes majority charge carriers coming from emitter.

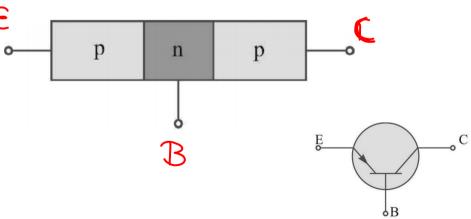
#### **Transistor**



A p-type crystal sandwiching between two n-type crystals



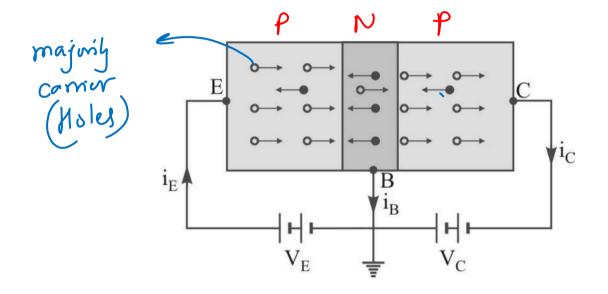
A <u>n-type</u> crystal <u>sandwiching</u> between two p-type crystals



# **Working of a p-n-p Transistor**



Base is common with Emitter and Collector



Emitter-Base junction is given small forward bias

Collector-Base junction is given large reverse bias

# Working of a p-n-p Transistor



holes move from emitter towards base

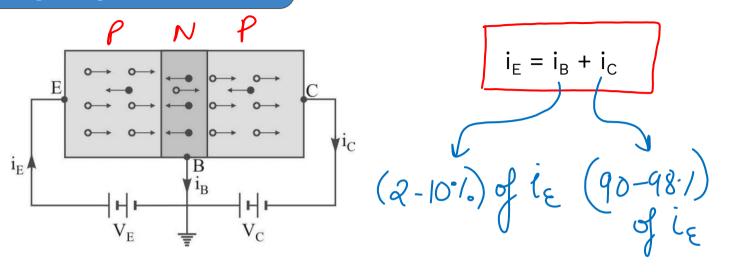
Most of them (nearly 98%) cross base and enter into collector region

while very few (nearly 2%) of the combine with the electrons in base an electron leaves the negative terminal of battery  $V_E$  and enters into base. This causes a small base current  $I_B$ 

The holes entering into the collector region combine with the electrons coming from the negative terminal of  $V_C$ . This causes collector current  $I_C$ 

# **Working of a p-n-p Transistor**





- 1. The base current may be nearly 2 to 10% of the emitter current depending on the doping level. Similarly collector current may be nearly 90 to 98% of the emitter current.
- 2. The holes are the charge carriers within the transistor while electrons are charge carriers in external circuit.



#### MCQ type Question [+4,-1]

The emitter-base junction of a transistor is ......biased while the collector base junction is .k....biased.

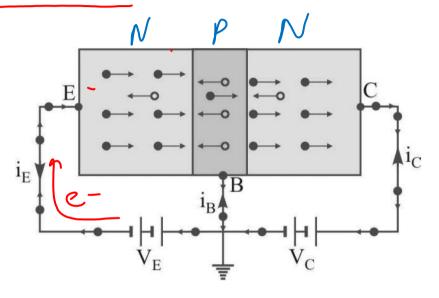
- A. forward, forward, reverse
  - c. reverse, forward D. reverse, reverse

•

# **Working of a n-p-n Transistor**



Base is common with Emitter and Collector



Emitter-Base junction is given small forward bias

Collector-Base junction is given large reverse bias

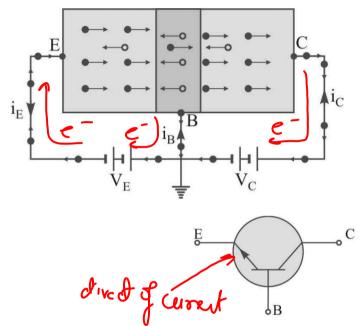
#### Working of a n-p-n Transistor

**y** jee

forward bias in emitter-base region, electrons from emitter region move towards base.

Most of them (nearly 98%) cross the base and enter into collector region

while very few (nearly 2%) combine with the holes in base

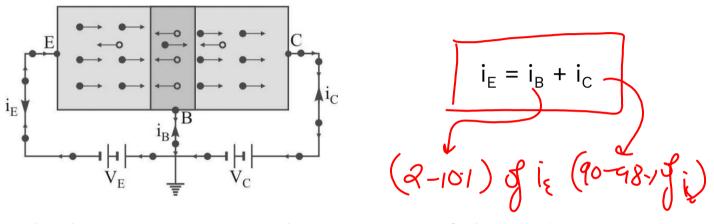


This electron is captured by positive terminal of the battery  $V_E$  and send it towards emitter region. This causes a small base current  $I_B$ . The electrons entering into collector region are attracted by the positive terminal of  $V_C$ . This causes collector current  $I_C$ .

## Working of a n-p-n Transistor



These two currents combine together constitute emitter current i<sub>E</sub>. Thus



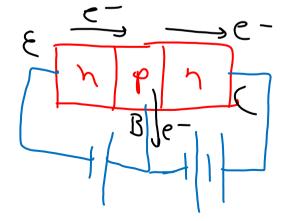
- 1. The base current may be 2 to 10% of the emitter current depending on the doping level. Similarly collector current may be 90 to 98% of the emitter current.
- 2. The electrons are the charge carriers within the transistor as well as in external circuit.



#### MCQ type Question [+4,-1]

In an n-p-n transistor -

- A. holes move from emitter to base.
- B/ negative charge carrier move from emitter to base.
- **ć**. holes move from base to collector.
- D. negative charge carrier move from collector to base



## **Transistor Configurations**



A transistor can be connected in the circuit in the following three ways :

- Common base (CB) connection.
- 2. Common emitter (CE) connection.
- 3. Common collector (CC) connection.

first two are commonly used in practice

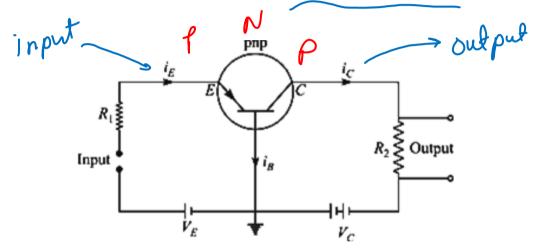
Each circuit connection has specific advantage and disadvantage

In all configurations the emitter is always connected with small forward bias, while the collector always has a large reverse bias

#### **Common Base Connection**



The input is applied between emitter and base and output is taken from collector base



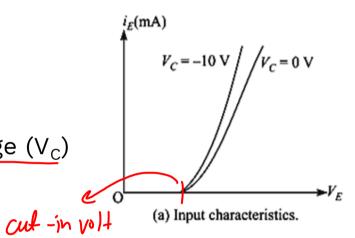
A p-n-p transistor in common base connection

#### **Common Base Connection**



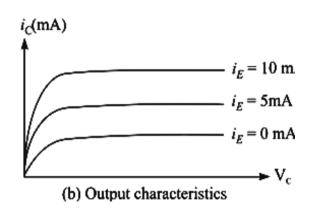
#### Input characteristics

Shows variation of emitter current (i<sub>E</sub>) with emitter voltage (V<sub>E</sub>) at constant collector voltage (V<sub>C</sub>)



#### **Output characteristics**

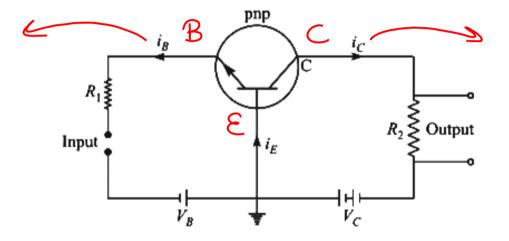
Shows the variation of collector current  $(i_C)$  with collector voltage  $(V_C)$  at constant emitter current  $(i_E)$ 



#### **Common Emitter Connection**



The input is applied between base and emitter and output is taken from collector and emitter



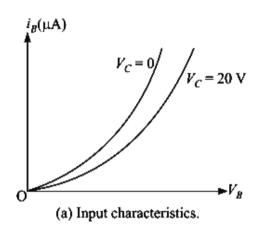
A p-n-p transistor in common emitter connection

#### **Common Emitter Connection**



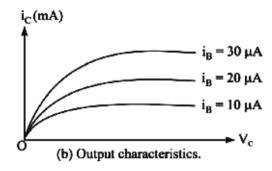
#### Input characteristics

Variation of base current  $i_B$  with base voltage  $V_B$  at constant collector voltage  $V_C$ 



#### **Output characteristics**

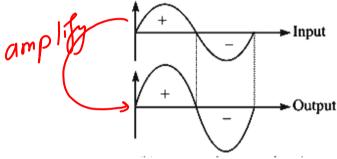
Variation of collector current  $i_C$  with collector voltage  $V_C$  at constant base current  $i_R$ 



#### Transistor as an Amplifier



A transistor can be used for amplification in which strength of output signal is more than input signal.



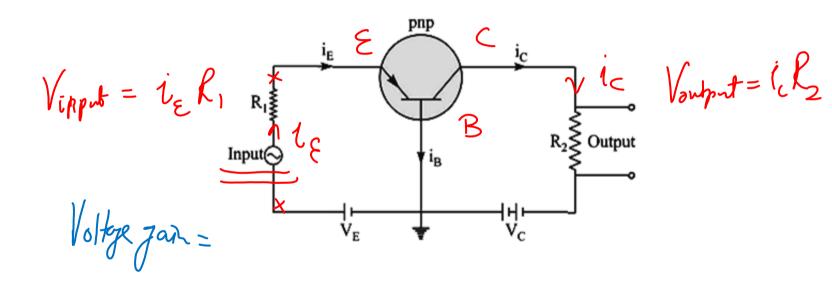
The transistor can be used as an amplifier in the following three configurations

- 1. CB amplifier
- 2. CE amplifier
- 3. CC amplifier

# CB Transistor Amplifier

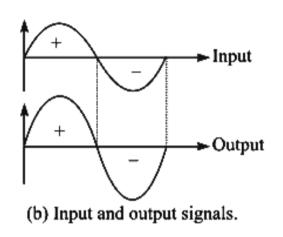


A pnp transistor in common-base-connection



Input signal is fed between the emitter and base

# **CB** Transistor Amplifier



input voltage = 
$$i_E R_1$$
  
output voltage =  $i_C R_2$   
Voltage gain =  $\frac{Output \ voltage}{Input \ voltage} = \frac{i_C R_2}{i_E R_1}$ 

The output signal is in phase with input signal.

# **CB** Transistor Amplifier



The current gain  $\propto$  is defined as:

$$\propto = \frac{[Change in collector current]}{[Change in emitter current]}, V_c Constant$$

$$\propto = \frac{\Delta i_C}{\Delta i_E}$$
,  $V_c$  Constant

The value of  $\propto$  ranges from 0.9 to 0.99.  $l_c < l_c$ 

For dc signal, 
$$\alpha = \frac{i_C}{i_E}$$



#### MCQ type Question [+4,-1]

In a common base transistor circuit, the current gain is 0.98. On changing emitter current by 5.00 mA, the change in collector current is -

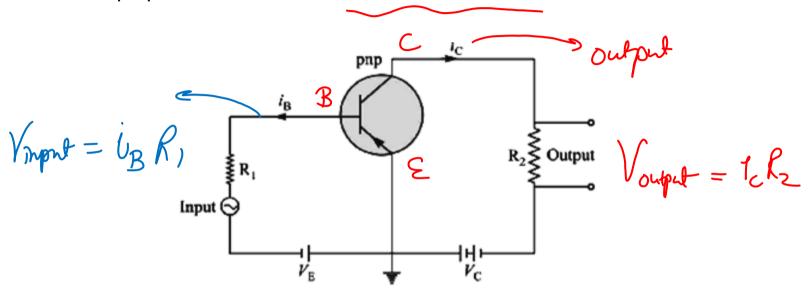
$$Di_{\varepsilon} = Di_{input} = 5 \text{ m A}$$

$$Di_{c} = Di_{output} = 7$$

# CE Transistor Amplifier



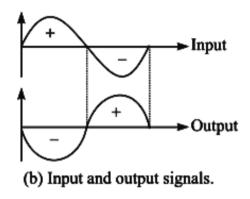
A pnp transistor in common emitter connection



Input signal is fed between the base and emitter

# CE Transistor Amplifier





input voltage =  $i_BR_1$  output voltage =  $I_CR_2$ 

Voltage gain = 
$$V_{output}/V_{input} = i_C R_2/i_B R_1$$

Power gain = Current gain × Voltage gain

The output signal is out of phase with the input signal.

# **CE Transistor Amplifier**



The current gain  $\beta$  is defined as :

$$\beta = \frac{[Change in collector current]}{[Change in base current]}, V_c Constant$$

$$\beta = \frac{\Delta i_{C}}{\Delta i_{B}}$$

$$V_{c} Constant$$

$$\dot{l}_{E} = \frac{l_{R} + l_{C}}{l_{C}}$$

The value of  $\beta$  ranges from 20 to 200

$$\beta = \frac{i_C}{i_B}$$





#### MCQ type Question [+4,-1]

A common emitter transistor amplifier has a current gain of 50. If the load resistance is  $4k\Omega$  and input resistance is  $500\Omega$  the voltage gain of the amplifier -

**A.** 160

**B.** 200

$$V_{gax} = \frac{V_{o}}{V_{l}} =$$

1

# Relationship between ∝ and β

$$I_{E} = I_{B} + I_{C}$$

$$I_{C} = I_{B} + I$$

$$I_{C} = I_{C} + I$$

# Relationship between $\propto$ and $\beta$

$$I_{E} = I_{B} + I_{C}$$

$$\Delta I_{E} = \Delta I_{B} + \Delta I_{C}$$

$$\frac{\Delta i_{E}}{\Delta i_{C}} = \frac{\Delta i_{B}}{\Delta i_{C}} + \frac{\Delta i_{C}}{\Delta i_{C}}$$

$$\alpha = \frac{\beta}{1+\beta}$$

$$\beta = \frac{\alpha}{1-\alpha}$$



#### MCQ type Question [+4,-1]

For a transistor,  $\propto$  = 0.9, the value of  $\beta$  is -

$$\beta = \frac{\alpha}{1 - \alpha} = \frac{0.9}{1 - 0.0}$$
= 9

•



# **Daily Practice Problems**





The part of a transistor which is most heavily doped to produce large number of majority carriers is

[AIEEE 2002]

- A. Emitter B. Base
- **C.** Collector **D.** Can be any of the above three.



If  $I_1$ ,  $I_2$ ,  $I_3$  are the lengths of the emitter, base and the collector of a transistor, then -

**A.** 
$$I_1 = I_2 = I_3$$

**B.** 
$$I_1 < I_2 > I_3$$

**C.** 
$$I_1 < I_2 < I_3$$

**D.** 
$$l_1 > l_2 > l_3$$



To use transistor as an amplifier -

- Emitter-base junction is forward biased and collector base junction is reverse biased.
- **B.** Both junctions are forward biased.
- C. Both junctions are reverse biased.
- It does not matter how the transistor is biased. It always works as an amplifier



When npn transistor is used as an amplifier

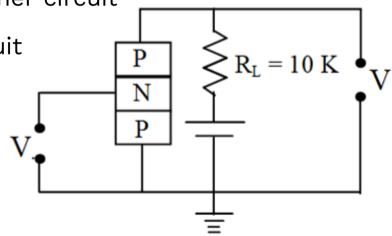
[AIEEE 2003]

- A. Electrons move from collector to base
- B. Holes move from emitter to base
- C. Electrons move from base to collector
- D. Holes move from base to emitter



An P-N-P transistor circuit is arranged as shown. It is a -

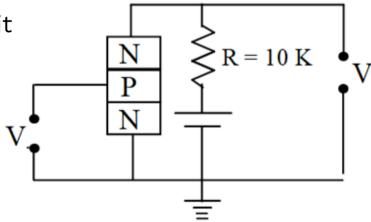
- A. Common base amplifier circuit
- B. Common emitter amplifier circuit
- C. Common collector circuit
- D. None





An N-P-N transistor circuit is arranged as shown. It is a -

- A. Common base amplifier circuit
- B. Common emitter amplifier circuit
- C. Common collector circuit
- D. None





In a common base amplifier, the phase difference between the input voltage and output voltage is

[AIEEE 2005]

Α. π

B.  $\pi/4$ 

C.  $\pi/4$ 

**D.** 0



For a transistor working as common base amplifier, the emitter current is 7.2 mA. The current gain is 0.96. The collector current is -

- **A.**  $0.96 \times 7.2 \text{ mA}$  **B.** 0.96/0.72 mA
- **D.**  $7.2 \text{ A} 2 \times 0.96 \text{ mA}$



In a common base mode of a transistor, the collector current is 5.488 mA for an emitter current of 5.60 mA. The value of the base current amplification factor (β) will be

[AIEEE 2006]

**A.** 49

**B.** 50

**C.** 51

**D.** 48

An n-p-n transistor circuit has  $\propto$  = 0.985. If I<sub>c</sub> = 2mA, then value of I<sub>B</sub> is -

- **A.** 0.03 mA **B.** 0.66 mA
- **C.** 0.003 mA **D.** 0.015 mA







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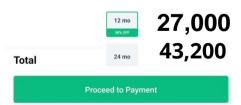


#### Step 6



#### Step 7











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