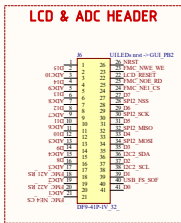
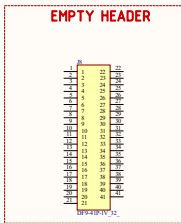
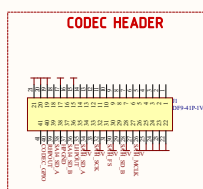
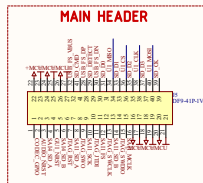
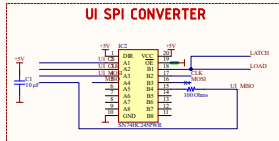
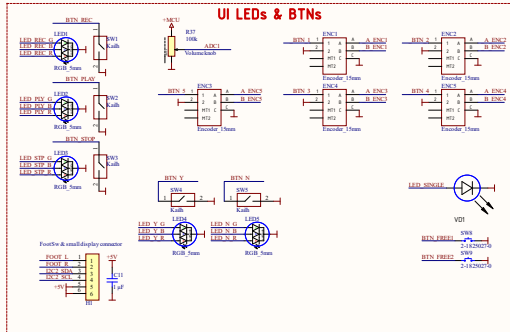
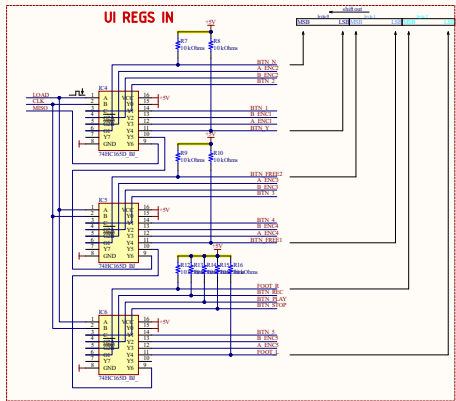
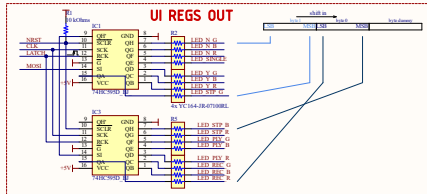


Adjustment of resistors for DC-DC for TV output
Change boost power cap to external low profile
Add TV Out Pin to prevent system from going
Add LDO for low start and current loading of LDO
Change LDO output network to 3.3V
Increase positive DC bias voltage on TV output
Add LDO protection on SD card bus
Add LDO protection on SD card bus
Add LDO protection on SD card bus



1

2

3

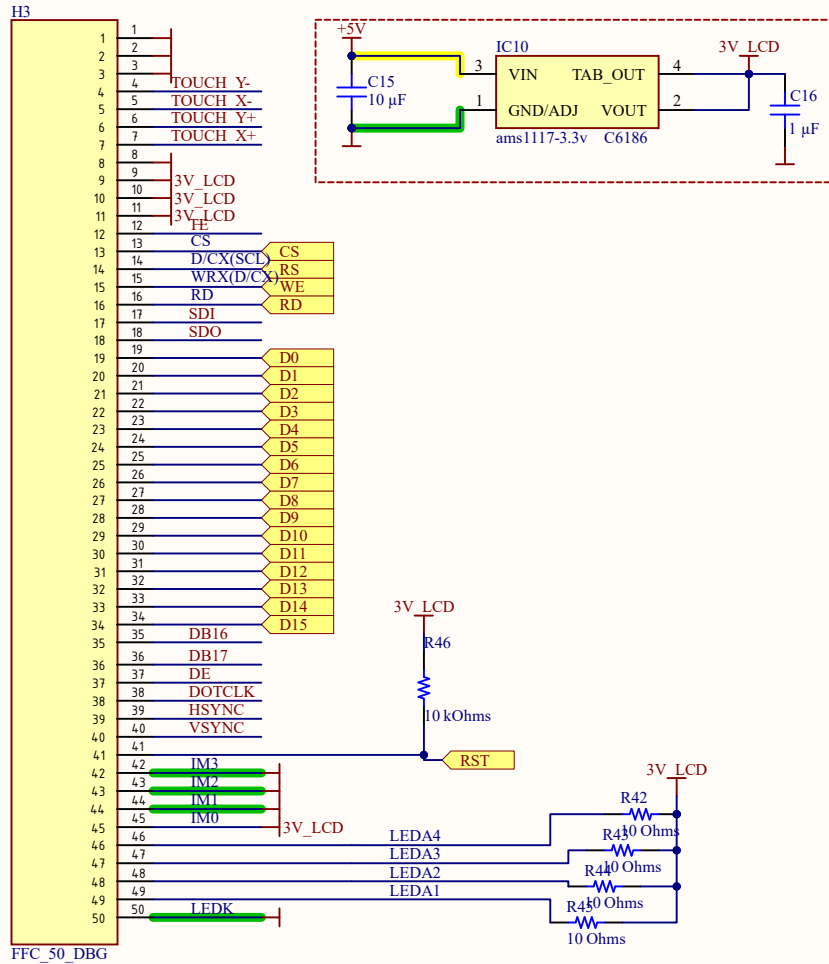
4

A

B

C

D



Note 1: Select the MCU interface Mode

IM3	IM2	IM1	IM0	MCU-Interface Mode	Pin in Use	
					Register/content	GRAM
0	0	0	0	8080 MCU 8-bit Bus Interface I	D[7:0]	D[7:0], WRX, RDX, CSX, D/CX
0	0	0	1	8080 MCU 16-bit Bus Interface I	D[7:0]	D[15:0], WRX, RDX, CSX, D/CX
0	0	1	0	8080 MCU 9-bit Bus Interface I	D[7:0]	D[8:0], WRX, RDX, CSX, D/CX
0	0	1	1	8080 MCU 18-bit Bus Interface I	D[7:0]	D[17:0], WRX, RDX, CSX, D/CX
0	1	0	1	3-wire 9-bit data Serial Interface I	SCL, SDA, CSX	
0	1	1	0	4-wire 8-bit data Serial Interface I	SCL, SDA, D/CX, CSX	
1	0	0	0	8080 MCU 16-bit Bus Interface II	D[8:1]	D[17:0], D[8:1], WRX, RDX, CSX, D/CX
1	0	0	1	8080 MCU 8-bit Bus Interface II	D[17:10]	D[17:10], WRX, RDX, CSX, D/CX
1	0	1	0	8080 MCU 18-bit Bus Interface II	D[8:1]	D[17:0], WRX, RDX, CSX, D/CX
1	0	1	1	8080 MCU 9-bit Bus Interface II	D[17:10]	D[17:9], WRX, RDX, CSX, D/CX
1	1	0	1	3-wire 9-bit data Serial Interface II	SCL, SDI, SDO, CSX	
1	1	1	0	4-wire 8-bit data Serial Interface II	SCL, SDI, D/CX, SDO, CSX	

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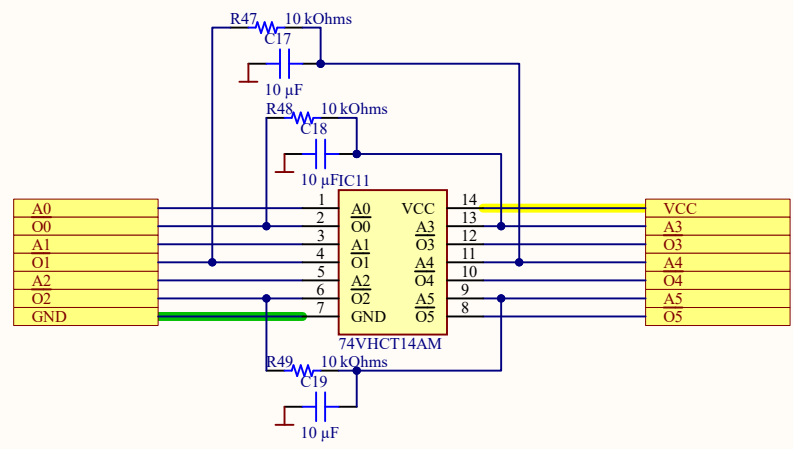
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3

4

C

D



Title		
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